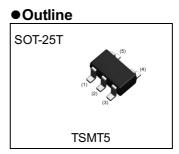


Middle Power Transistor (50V / 3A)

Parameter	Tr1 and Tr2
V _{CEO}	50V
۱ _C	3A



Inner circuit

Features

1)Low saturation voltage, typically

- V_{CE(sat)}=350mV (Max.) (I_C/I_B=1A/50mA)
- 2)High speed switching

(1) Tr1 Base (5) (4) (2) Tr1/Tr2 Emitter (3) Tr2 Base Tr1 Tr2 (4) Tr2 Collector (5) Tr1 Collector (1) (2) (3)

Application

LOW FREQUENCY AMPLIFIER, HIGH SPEED SWITCHING

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
QS5W2	SOT-25T (TSMT5)	2928	TR	180	8	3000	W02

•Absolute maximum ratings ($T_a = 25^{\circ}C$) <It is the same ratings for the Tr1 and Tr2>

• (4)	U		
Parameter	Symbol	Values	Unit
Collector-base voltage	V _{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	6	V
	Ι _C	3	Α
Collector current	I _{CP} *1	6	Α
	P _D *2	0.5	W/Total
Power dissipation	P _D *3*4	1.25	W/Total
Junction temperature	Tj	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

•Electrical characteristics (T_a = 25°C) <It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol Conditions		Values			Unit	
Farameter Symbol		Conditions	Min.	Тур.	Max.	Unit	
Collector-base breakdown voltage	BV_{CBO}	Ι _C = 100μΑ	50	-	-	V	
Collector-emitter breakdown voltage	BV _{CEO}	I _C = 1mA	50	-	-	V	
Emitter-base breakdown voltage	BV_{EBO}	Ι _Ε = 100μΑ	6	-	-	V	
Collector cut-off current	I _{CBO}	V _{CB} = 50V	-	-	1.0	μA	
Emitter cut-off current	I _{EBO}	V _{EB} = 4V	-	-	1.0	μA	
Collector-emitter saturation voltage		I _C = 1A, I _B = 50mA	-	130	350	mV	
DC current gain h _{FE} V _C		V _{CE} = 3V, I _C = 50mA	180	-	450	-	
		V _{CE} = 10V,I _E = -500mA f = 100MHz	-	320	-	MHz	
Output capacitance	$C_{ob} \qquad \begin{cases} V_{CB} = 10V, I_E = 0A \\ f = 1MHz \end{cases}$		-	13	-	pF	
Turn-On time	t _{on}	I _C = 1.5A,V _{CC} ≃ 10V	-	50	-	ns	
Storage time	t _{stg}	I _{B1} = 0.15A	-	450	-	ns	
Fall time	t _f	I _{B2} = -0.15A (See test circuit)	-	80	-	ns	

*1 Pw=10ms, Single pulse

*2 Each terminal mounted on a reference land.

*3 Mounted on a ceramic board(25×25×0.8mm).

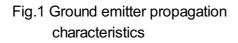
*4 900mW per element must not be exceeded.

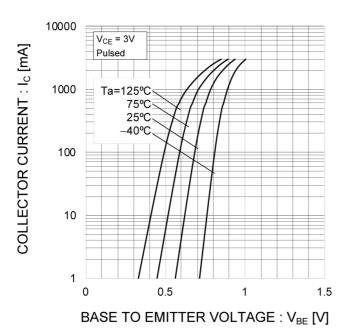
*5 Pulsed



• Electrical characteristic curves ($T_a = 25^{\circ}C$)

<For Tr1 and Tr2 in common>





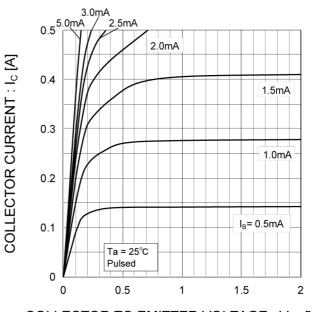
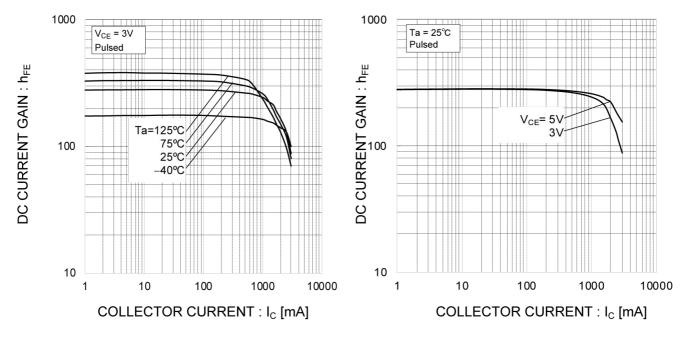


Fig.2 Typical output characteristics

COLLECTOR TO EMITTER VOLTAGE : $V_{\text{CE}}\left[V\right]$

Fig.3 DC current gain vs. collector current (I)

Fig.4 DC current gain vs. collector current (II)





1

0.1

0.01

0.001

1

10

SATURATION VOLTAGE : V_{CE(sat)} [V]

COLLECTOR-EMITTER

• Electrical characteristic curves (T_a = 25°C)

<For Tr1 and Tr2 in common>

 $I_{\rm C}/I_{\rm B} = 20$

Pulsed

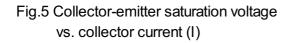


Fig.6 Collector-emitter saturation voltage vs. collector current (II)

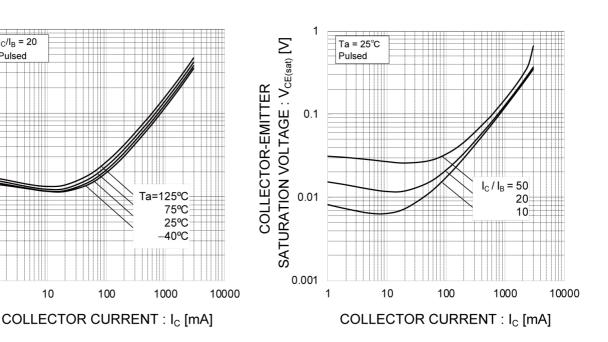
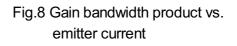
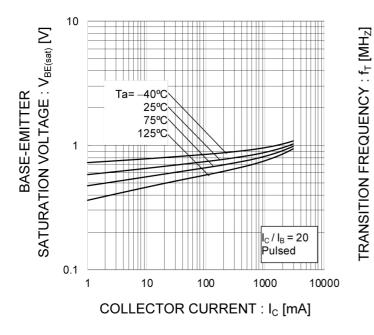
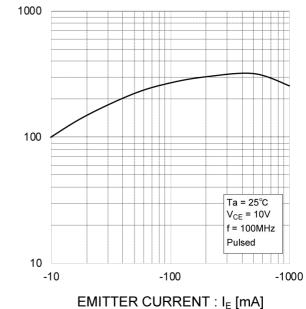


Fig.7 Base-emitter saturation voltage vs. collector current

100





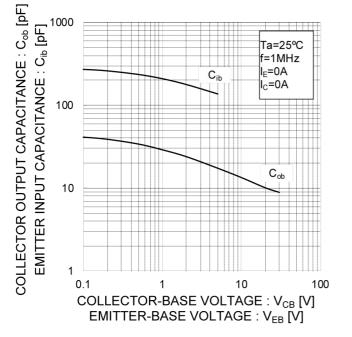




• Electrical characteristic curves (T_a =25°C)

<For Tr1 and Tr2 in common>

Fig.9 Emitter input capacitance vs. Emitter-base voltage Collector output capacitance vs. collector-base voltage



●Switching time test circuit(T_a=25°C)

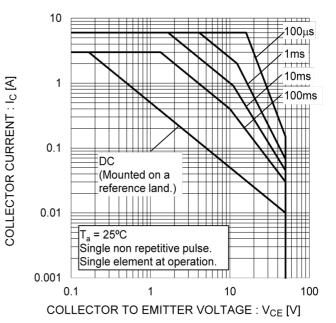
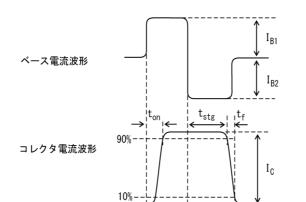


Fig.10 Safe Operating Area

 V_{IN} V_{IN} $P_{W} \leftarrow I_{B1}$ I_{C} I_{C} I

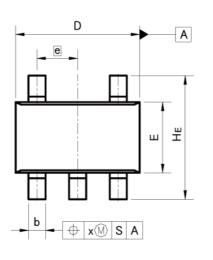




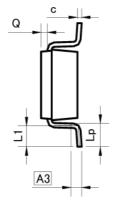
Dimensions

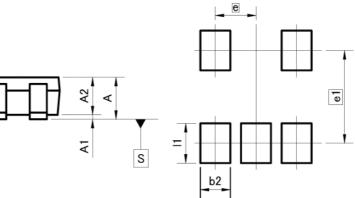
SOT-25T





🛆 y S





Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	-	1.00	-	0.039	
A1	0.00	0.10	0.000	0.004	
A2	0.75	0.95	0.030	0.037	
A3	0.	25	0.0)10	
b	0.35	0.50	0.014	0.020	
С	0.10	0.26	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.95		0.037		
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.05	0.25	0.002	0.010	
x	-	0.20	-	0.008	
У	-	0.10	-	0.004	
DIM	MILIM	ETERS	INC	HES	
	MIN	MAX	MIN	MAX	

 11
 0.90

 Dimension in mm/inches

b2

e1



0.028

0.035

0.083

0.70

2.10

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r					
	JAPAN	USA	EU	CHINA	
	CLASSⅢ	CLASSⅢ	CLASS II b	CLASSII	
	CLASSⅣ	CLASSI	CLASSⅢ	CLASSII	

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
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 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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