

## 30V Nch+Nch Small Signal MOSFET

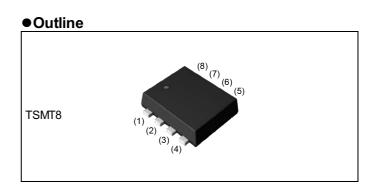
V <sub>DSS</sub>	30V
R <sub>DS(on)</sub> (Max.)	28mΩ
I <sub>D</sub>	±6A
P <sub>D</sub>	1.5W

## Features

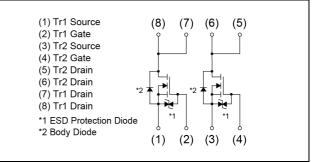
Application

Switching

- 1) Low on resistance.
- 2) Small Surface Mount Package (TSMT8).



#### Inner circuit



## Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Basic ordering unit (pcs)	3000
	Taping code	TCR
	Marking	K13

## • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified) <Tr1 and Tr2>

U (u		, ,		
Parameter	Symbol	Value	Unit	
Drain - Source voltage		V <sub>DSS</sub>	30	V
Continuous drain current	I <sub>D</sub>	±6	А	
Pulsed drain current	۱ <sub>DP</sub> *1	±18	А	
Gate - Source voltage		V <sub>GSS</sub>	±20	V
	total	D *2	1.5	W
Power dissipation	element	P <sub>D</sub> *2	1.25	
	total	P <sub>D</sub> <sup>*3</sup> 1.1		
Junction temperature	Tj	150	°C	
Operating junction and storage temp	T <sub>stg</sub>	-55 to +150	°C	

#### •Thermal resistance

Deremeter		Symbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit	
	total	${\sf R}_{{\sf th}{\sf J}{\sf A}}^{*2}$	-	-	83.3	°C/W
Thermal resistance, junction - ambient	element		-	-	100	
	total	$R_{thJA}^{*3}$	-	-	113	

## •Electrical characteristics ( $T_a = 25^{\circ}C$ ) <Tr1 and Tr2>

Devenueter	Currents et	Conditions	Values				
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T}$		-	34.15	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	$ΔT_j$ referenced to 25°C $I_{DSS}$ $V_{DS}$ = 30V, $V_{GS}$ = 0V		-	1	μΑ	
Gate - Source I <sub>GSS</sub> V <sub>DS</sub>		V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±10	μA	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-2.34	-	mV/°C	
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A	-	20	28		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6A	-	25	35	mΩ	
		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 6A	-	28	39	1	
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain	-	0.6	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>   <sup>*4</sup>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 6A	3.0	-	-	S	



## •Electrical characteristics ( $T_a = 25^{\circ}C$ ) <Tr1 and Tr2>

Deremeter	Cumphol	Conditions	Values			Lipit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	390	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	150	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	70	-	
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 15 V, V_{GS} = 10 V$	-	8	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 3A	-	40	-	20
Turn - off delay time	$t_{d(off)}^{*4}$	R <sub>L</sub> = 5Ω	-	35	-	ns
Fall time	t <sub>f</sub> *4	R <sub>G</sub> = 10Ω	-	7	-	

## •Gate charge characteristics ( $T_a = 25^{\circ}C$ ) <Tr1 and Tr2>

Parameter	Symbol Conditions		Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*4}$	$V_{DD} \simeq 15V$	-	5.5	-	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 6A	-	1.5	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	V <sub>GS</sub> = 5V	-	2.1	-	

## •Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

<Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	I <sub>S</sub>	$T = 25^{\circ}$	-	-	1	^	
Pulse forward current	$I_{SP}^{*1}$	T <sub>a</sub> = 25°C	-	-	18	A	
Forward voltage	$V_{SD}^{*4}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = 6A	-	-	1.2	V	

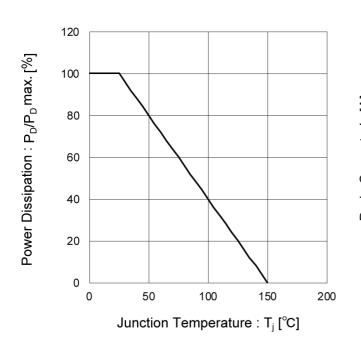
\*1 Pw $\leq$ 10µs , Duty cycle $\leq$ 1%

\*2 Mounted on a ceramic board (30×30×0.8mm)

- \*3 Mounted on a FR4 (25×25×0.8mm)
- \*4 Pulsed







## Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

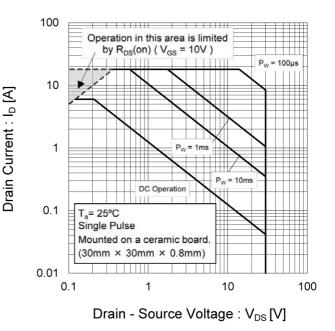
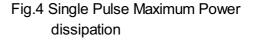
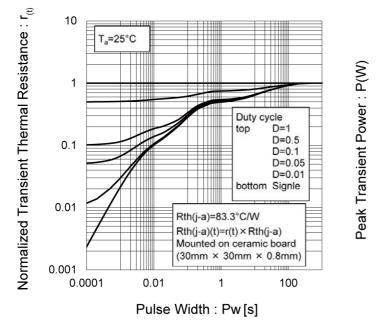


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

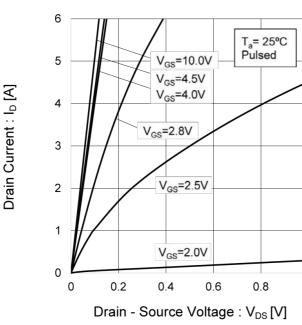




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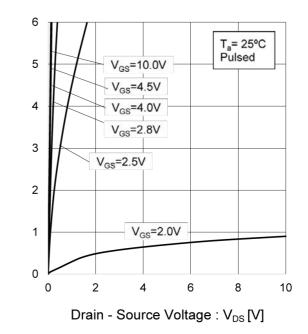


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#### Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

1

Fig.7 Breakdown Voltage vs. Junction Temperature

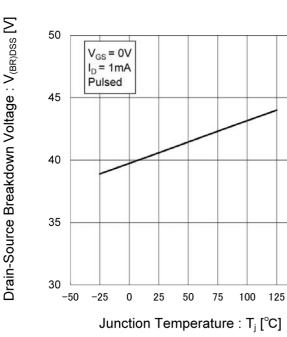
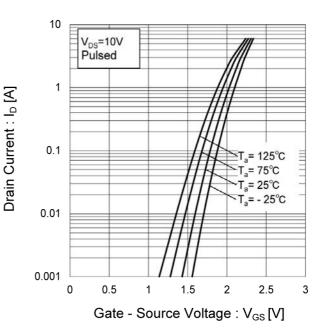
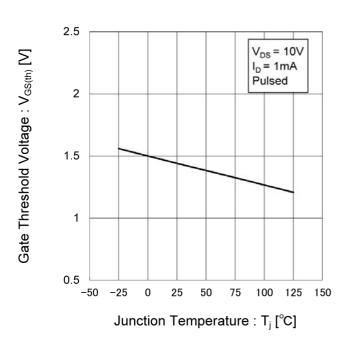


Fig.8 Typical Transfer Characteristics

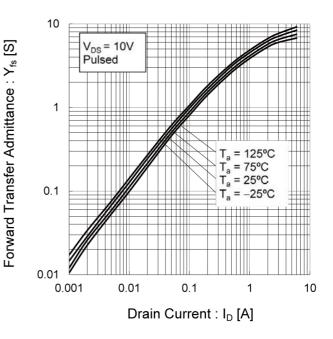


150





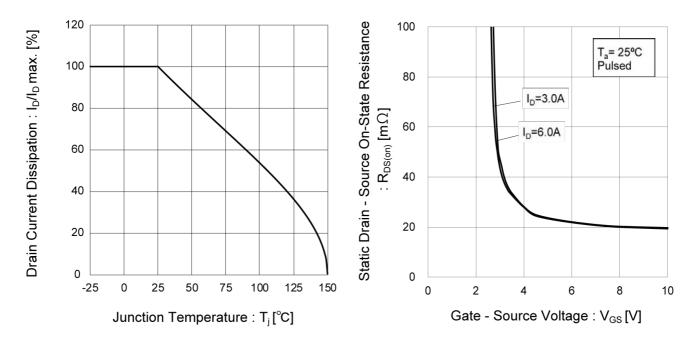
#### Fig.9 Gate Threshold Voltage vs. Junction Temperature



## Fig.10 Forward Transfer Admittance vs. Drain Current



Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage





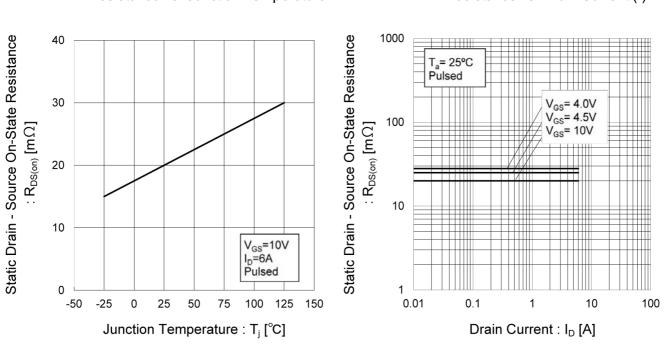


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)





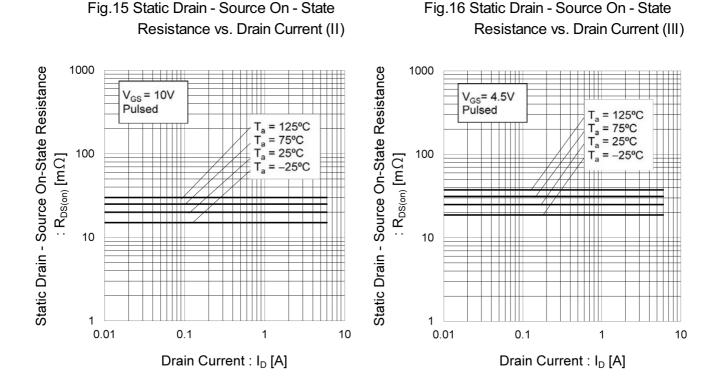
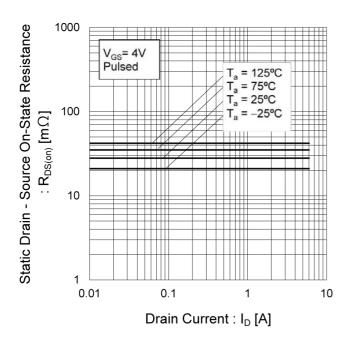


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)







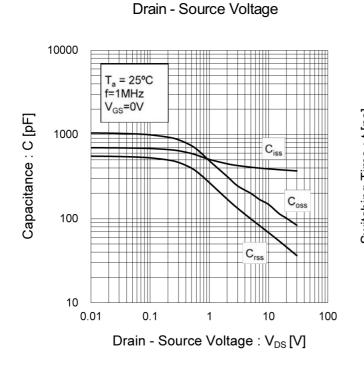


Fig.18 Typical Capacitance vs.

#### Fig.19 Switching Characteristics

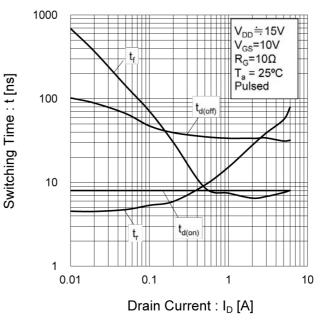


Fig.20 Dynamic Input Characteristics

Gate - Source Voltage : V<sub>GS</sub> [V]

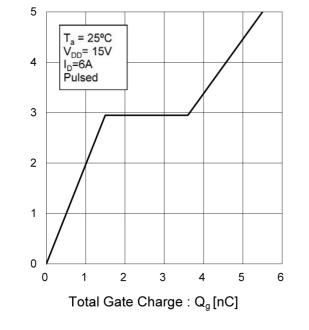
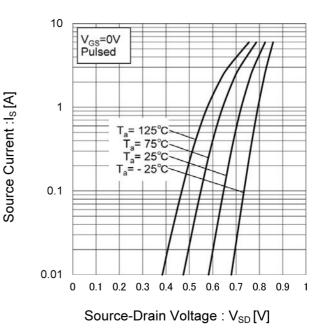


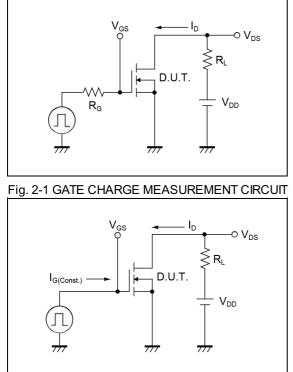
Fig.21 Source Current vs. Source Drain Voltage





## •Measurement circuits <It is the same for the Tr1 and Tr2>





#### Fig. 1-2 SWITCHING WAVEFORMS

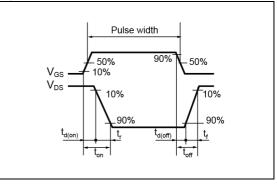
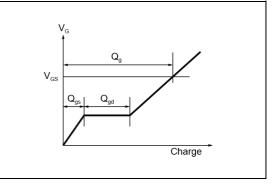


Fig. 2-2 GATE CHARGE WAVEFORM

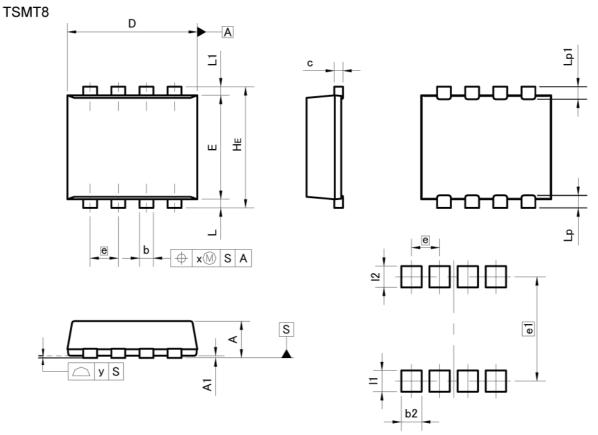


## Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



#### Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX
A	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
с	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.	65	0.026	
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
x	177	0.10	: <del></del>	0.004
v	377	0.10	-	0.004

DIM	MILIM	ETERS	INCHES	
	MIN	MAX	MIN	MAX
b2	2 <del>14</del>	0.47	-	0.019
e1	2.41		0.0	095
11	3 <b>—</b>	0.49	-	0.019
12	. <del></del>	0.49		0.019

Dimension in mm/inches



# Notice

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  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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