

V _{DSS}	600V
R _{DS(on)} (Max.)	1.43Ω
I _D	±4A
P _D	35W

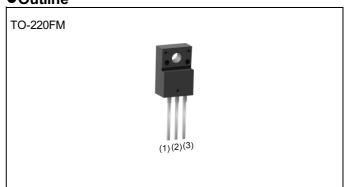
Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

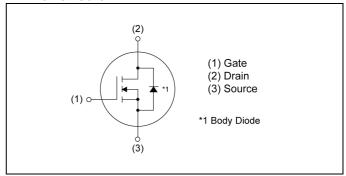
Application

Switching

Outline



•Inner circuit



Packaging specifications

Packing	Tube
Packing code	C7 G
Marking	R6004JNX
Basic ordering unit (pcs)	2000

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	600	V
Continuous drain current (T _c = 25°C)	I _D *1	±4	Α
Pulsed drain current	I _{DP} *2	±12	Α
Gate - Source voltage	V _{GSS}	±30	V
Avalanche current, single pulse	I _{AS} *3	1.0	Α
Avalanche energy, single pulse	E _{AS} *3	52	mJ
Power dissipation (T _c = 25°C)	P _D	35	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Dougnoston	O	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	3.49	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	70	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

●Electrical characteristics (T_a = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		600	-	-	V
Zero gate voltage drain current	I_{DSS} $V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^{\circ}C$		-	-	100	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 450 \mu A$	5.0	6.0	7.0	V
Static drain - source on - state resistance	R _{DS(on)} *5	$V_{GS} = 15V, I_D = 2.0A$ $T_j = 25^{\circ}C$	-	1.10	1.43	Ω
Gate resistance	R_{G}	R _G f = 1MHz, open drain		3.6	-	Ω

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymah al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Uniil
Input capacitance	C _{iss}	V _{GS} = 0V	-	260	-	
Output capacitance	C _{oss}	V _{DS} = 100V	-	18	-	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	0.9	-	_
Effective output capacitance energy related	C _{o(er)} *6	V _{GS} = 0V	-	14	-	pF
Effective output capacitance time related	C _{o(tr)} *7	V _{DS} = 0V to 480V	-	50	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 300V$, $V_{GS} = 15V$	-	13	-	
Rise time	t _r *5	I _D = 2.0A	-	11	-	20
Turn - off delay time	t _{d(off)} *5	R _L ~ 150Ω	-	24	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	33	-	

● Gate charge characteristics (T_a = 25°C)

Darameter	Cymabal	Conditions	Values			Linit
Parameter Symbol Conditions		Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*5}	$V_{DD} \simeq 300V$	-	10.5	-	
Gate - Source charge	Q _{gs} *5	I _D = 4A	-	3.0	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 15V	-	3.5	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 300V$, $I_D = 4A$	-	9.5	-	V

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \simeq 100mH, V_{DD} = 50V, R_G = 25 Ω , starting T_i = 25°C

^{*4} Tc=25°C

^{*5} Pulsed

^{*6} Co(er) is a fixed capacitance that gives the same stored energy as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

^{*7} Co(tr) is a fixed capacitance that gives the same charging time as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current	I _S *1	- T _C = 25°C	1	-	4	Α	
Pulsed source current	I _{SP} *2	1C - 23 C	1	-	12	Α	
Source-Drain voltage	V _{SD} *5	$V_{GS} = 0V$, $I_S = 4A$	-	-	1.7	٧	
Reverse recovery time	t _{rr} *5		1	45	1	ns	
Reverse recovery charge	Q _{rr} *5	I _S = 4A di/dt = 100A/μs	-	100	-	nC	
Peak reverse recovery current	_{rr} *5		-	4.0	-	А	

Fig.1 Power Dissipation Derating Curve

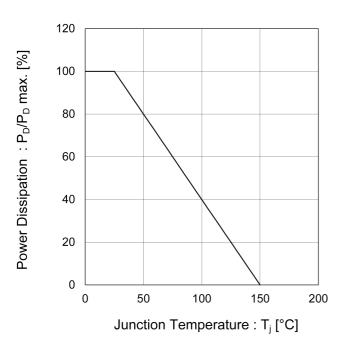


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

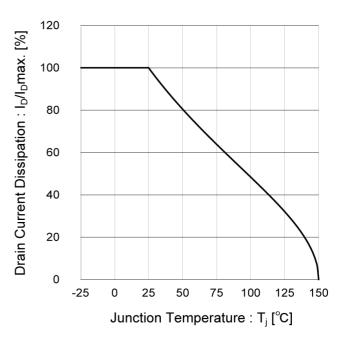


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

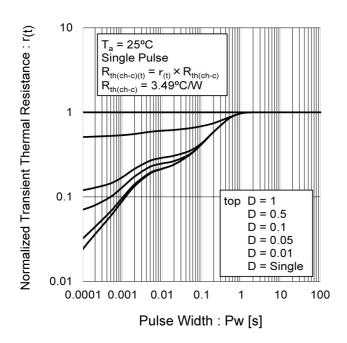


Fig.4 Maximum Safe Operating Area

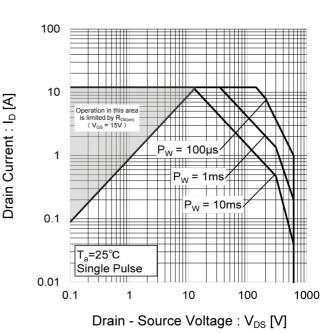


Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

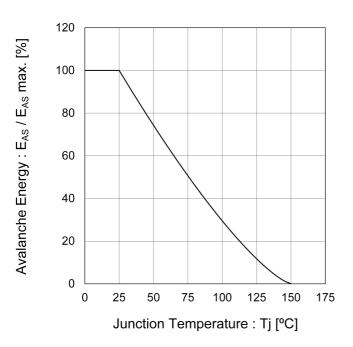


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

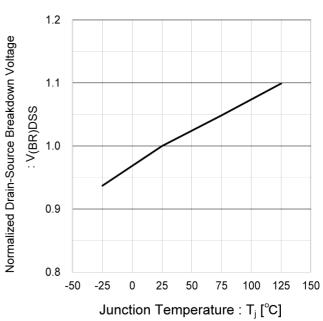


Fig.7 Typical Output Characteristics(I)

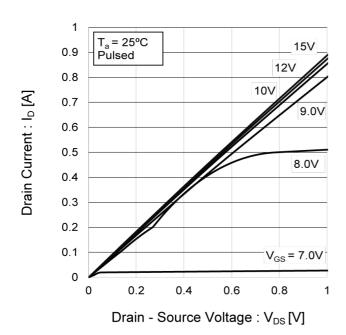
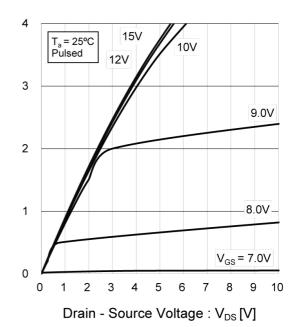


Fig.8 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.9 Typical Transfer Characteristics

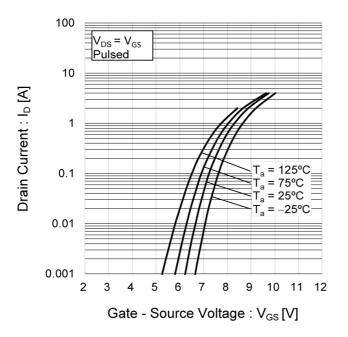


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

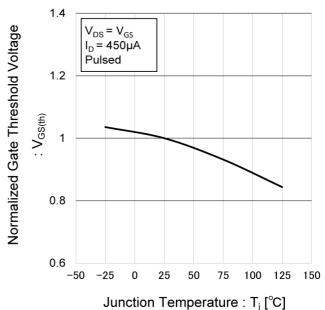


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

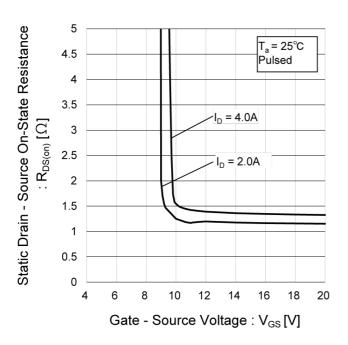


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

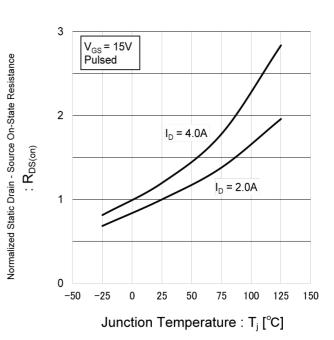


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

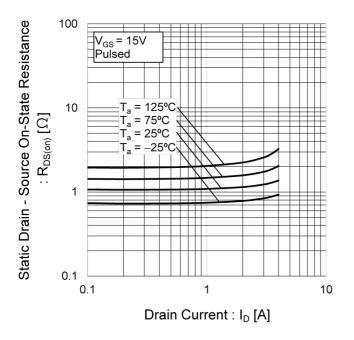


Fig.14 Typical Capacitance vs.

Drain - Source Voltage

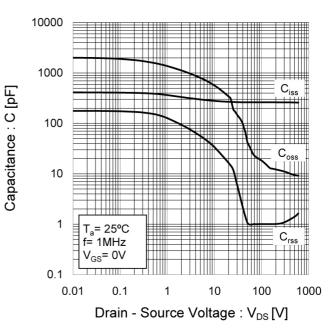


Fig.15 Typical Coss Stored Energy

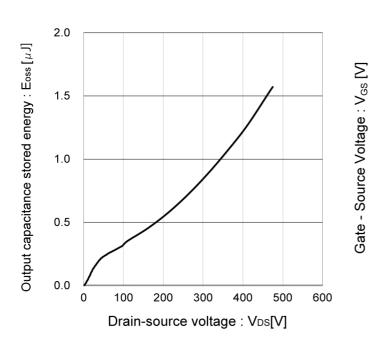


Fig.16 Typicsl Gate Charge

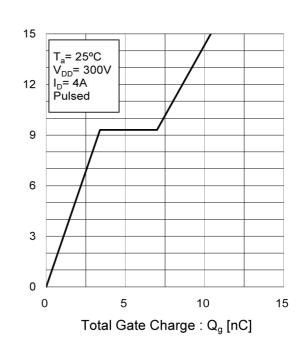
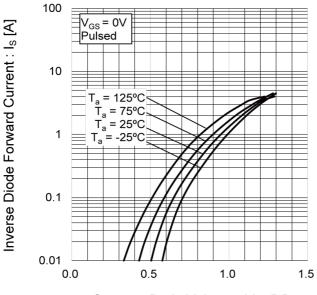
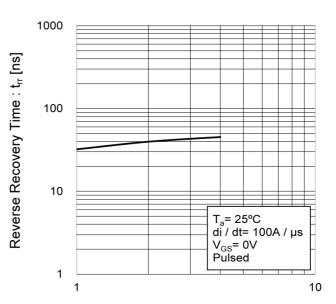


Fig.17 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.18 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current: I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

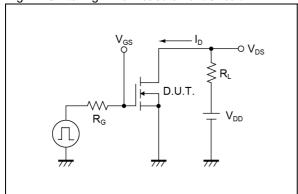


Fig.2-1 Gate Charge Measurement Circuit

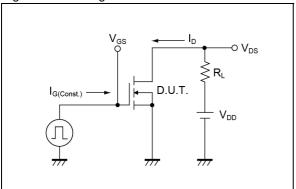


Fig.3-1 Avalanche Measurement Circuit

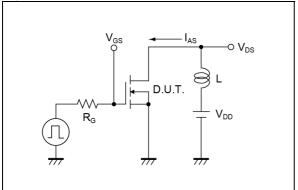


Fig.4-1 Diode Recovery Measurement Circuit

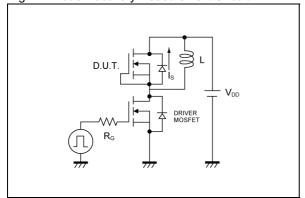


Fig.1-2 Switching Waveforms

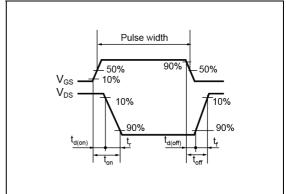


Fig.2-2 Gate Charge Waveform

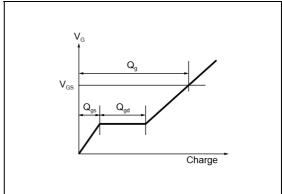


Fig.3-2 Avalanche Waveform

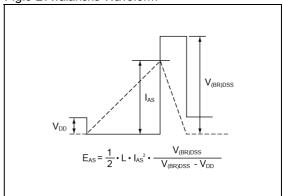
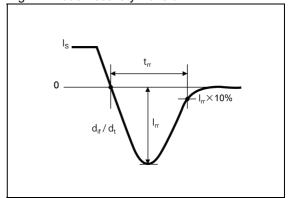
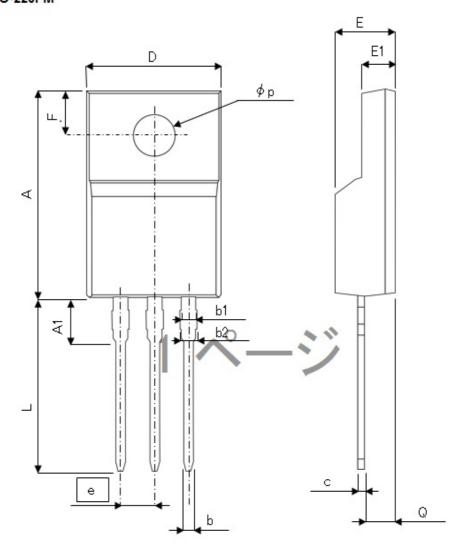


Fig.4-2 Diode Recovery Waveform



Dimensions

TO-220FM



DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	15.67	16.27	0.617	0.641
A1	3.03	3.43	0.119	0.135
Ь	0.70	0.95	0.028	0.037
ь1	1.00	1.40	0.039	0.055
ь2	1.10	1.50	0.043	0.059
С	0.45	0.65	0.018	0.026
D	9.90	10.30	0.390	0.406
E	4.60	5.00	0.181	0.197
E1	2.44	2.74	0.096	0.108
е	2.	54	0.1	00
F	3.10	3.50	0.122	0.138
L	12.6	13.6	0.496	0.535
р	2.98	3.38	0.117	0.133
Q	2.25	3.25	0.089	0.128

Dimension in mm / inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCTI
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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Notice-PGA-E Rev.004

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