| $\mathrm{V}_{\mathrm{DSS}}$ | 600 V |
| :---: | :---: |
| $\mathrm{R}_{\mathrm{DS}(\text { on })}($ Max. $)$ | $0.780 \Omega$ |
| $\mathrm{I}_{\mathrm{D}}$ | $\pm 7 \mathrm{~A}$ |
| $\mathrm{P}_{\mathrm{D}}$ | 46 W |

## -Features

1) Fast reverse recovery time (trr)
2) Low on-resistance
3) Fast switching speed
4) Drive circuits can be simple
5) Pb-free plating ; RoHS compliant

## - Application

Switching

## - Outline



## - Inner circuit


(3)

## - Packaging specifications

| Packing | Tube |
| :--- | :---: |
| Packing code | C7 G |
| Marking | R6007JNX |
| Basic ordering unit (pcs) | 2000 |

- Absolute maximum ratings ( $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$, unless otherwise specified)

| Parameter | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Drain - Source voltage | $\mathrm{V}_{\mathrm{DSS}}$ | 600 | V |
| Continuous drain current $\left(\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}\right)$ | $\mathrm{I}_{\mathrm{D}}{ }^{* 1}$ | $\pm 7$ | A |
| Pulsed drain current | $\mathrm{I}_{\mathrm{DP}}{ }^{*}{ }^{*}$ | $\pm 21$ | A |
| Gate - Source voltage | $\mathrm{V}_{\mathrm{GSS}}$ | $\pm 30$ | V |
| Avalanche current, single pulse | $\mathrm{I}_{\mathrm{AS}}{ }^{* 3}$ | 1.6 | A |
| Avalanche energy, single pulse | $\mathrm{E}_{\mathrm{AS}}{ }^{* 3}$ | 132 | mJ |
| Power dissipation $\left(\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}\right)$ | $\mathrm{P}_{\mathrm{D}}$ | 46 | W |
| Junction temperature | $\mathrm{T}_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Operating junction and storage temperature range | $\mathrm{T}_{\mathrm{stg}}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

- Thermal resistance

| Parameter | Symbol | Values |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | Min. | Typ. | Max. |  |
| Thermal resistance, junction - case | $\mathrm{R}_{\text {thJc }}$ | - | - | 2.70 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal resistance, junction - ambient | $\mathrm{R}_{\text {thJA }}$ | - | - | 70 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Soldering temperature, wavesoldering for 10s | $\mathrm{T}_{\text {sold }}$ | - | - | 265 | ${ }^{\circ} \mathrm{C}$ |

- Electrical characteristics $\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Conditions | Values |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. |  |
| Drain - Source breakdown voltage | $\mathrm{V}_{\text {(BR) }{ }^{\text {dss }}}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}$ | 600 | - | - | V |
| Zero gate voltage drain current | $\mathrm{I}_{\text {DSS }}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DS}}=600 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V} \\ & \mathrm{~T}_{\mathrm{j}}=25^{\circ} \mathrm{C} \end{aligned}$ | - | - | 100 | $\mu \mathrm{A}$ |
| Gate - Source leakage current | $I_{\text {gSs }}$ | $\mathrm{V}_{\mathrm{GS}}= \pm 30 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ | - | - | $\pm 100$ | nA |
| Gate threshold voltage | $\mathrm{V}_{\mathrm{GS}(\mathrm{th})}$ | $\mathrm{V}_{\mathrm{DS}}=\mathrm{V}_{\mathrm{GS}}, \mathrm{I}_{\mathrm{D}}=1.0 \mathrm{~mA}$ | 5.0 | 6.0 | 7.0 | V |
| Static drain - source on - state resistance | $\mathrm{R}_{\mathrm{DS} \text { (on) }}{ }^{\text {² }}$ | $\begin{aligned} & V_{G S}=15 \mathrm{~V}, I_{D}=3.5 \mathrm{~A} \\ & T_{j}=25^{\circ} \mathrm{C} \end{aligned}$ | - | 0.600 | 0.780 | $\Omega$ |
| Gate resistance | $\mathrm{R}_{\mathrm{G}}$ | $\mathrm{f}=1 \mathrm{MHz}$, open drain | - | 2.9 | - | $\Omega$ |

- Electrical characteristics $\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Conditions | Values |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. |  |
| Input capacitance | $\mathrm{C}_{\text {iss }}$ | $\left\{\begin{array}{l} V_{G S}=0 V \\ V_{D S}=100 \mathrm{~V} \\ f=1 \mathrm{MHz} \end{array}\right.$ | - | 475 | - | pF |
| Output capacitance | $\mathrm{C}_{\text {oss }}$ |  | - | 30 | - |  |
| Reverse transfer capacitance | $\mathrm{C}_{\text {rss }}$ |  | - | 1.4 | - |  |
| Effective output capacitance energy related | $\mathrm{C}_{\text {o(er) }}{ }^{6}$ | $\begin{aligned} & V_{G S}=0 \mathrm{~V} \\ & V_{D S}=0 \mathrm{~V} \text { to } 480 \mathrm{~V} \end{aligned}$ | - | 23 | - |  |
| Effective output capacitance time related | $\mathrm{C}_{\text {o(tr) }}{ }^{\text {² }}$ |  | - | 90 | - |  |
| Turn - on delay time | $\mathrm{t}_{\mathrm{d}(\mathrm{O})}{ }^{*}{ }^{\text {a }}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}} \simeq 300 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=15 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{D}}=3.5 \mathrm{~A} \\ & \mathrm{R}_{\mathrm{L}} \simeq 86.6 \Omega \\ & \mathrm{R}_{\mathrm{G}}=10 \Omega \end{aligned}$ | - | 17 | - | ns |
| Rise time | $\mathrm{t}_{\mathrm{r}}{ }^{\text {5 }}$ |  | - | 15 | - |  |
| Turn - off delay time | $\mathrm{t}_{\mathrm{d}(\text { (ff) }}{ }^{*}$ |  | - | 32 | - |  |
| Fall time | $\mathrm{t}_{\mathrm{f}}{ }^{\text {5 }}$ |  | - | 25 | - |  |

- Gate charge characteristics $\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Conditions | Values |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. |  |
| Total gate charge | $\mathrm{Q}_{\mathrm{g}}{ }^{5}$ | $\begin{aligned} & V_{D D} \simeq 300 \mathrm{~V} \\ & I_{D}=7 \mathrm{~A} \\ & V_{G S}=15 \mathrm{~V} \end{aligned}$ | - | 17.5 | - | nC |
| Gate - Source charge | $\mathrm{Q}_{\mathrm{gs}}{ }^{* 5}$ |  | - | 5.1 | - |  |
| Gate - Drain charge | $\mathrm{Qgd}^{*}{ }^{*}$ |  | - | 6.4 | - |  |
| Gate plateau voltage | $\mathrm{V}_{\text {(plateau) }}$ | $\mathrm{V}_{\mathrm{DD}} \simeq 300 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=7 \mathrm{~A}$ | - | 9.1 | - | V |

*1 Limited only by maximum temperature allowed.
*2 Pw $\leq 10 \mu \mathrm{~s}$, Duty cycle $\leq 1 \%$
*3 $\mathrm{L} \simeq 100 \mathrm{mH}, \mathrm{V}_{\mathrm{DD}}=50 \mathrm{~V}, R_{G}=25 \Omega$, starting $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$

* $4 \mathrm{Tc}=25^{\circ} \mathrm{C}$
*5 Pulsed
*6 $\mathrm{Co}(\mathrm{er})$ is a fixed capacitance that gives the same stored energy as Coss while $V_{D S}$ is rising from 0 to $80 \% V_{\text {DSs }}$.
*7 Co (tr) is a fixed capacitance that gives the same charging time as Coss while $\mathrm{V}_{\mathrm{DS}}$ is rising from 0 to $80 \% V_{\text {DSs }}$.
- Body diode electrical characteristics (Source-Drain) $\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$

| Parameter | Symbol | Conditions | Values |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | Min. | Typ. | Max. |  |
| Source current | $\mathrm{Is}^{* 1}$ | $\mathrm{T}_{\mathrm{C}}=25^{\circ} \mathrm{C}$ | - | - | 7 | A |
| Pulsed source current | $\mathrm{ISP}^{*}{ }^{\text {2 }}$ |  | - | - | 21 | A |
| Source-Drain voltage | $\mathrm{V}_{\text {SD }}{ }^{* 5}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{S}}=7 \mathrm{~A}$ | - | - | 1.7 | V |
| Reverse recovery time | $\mathrm{t}_{\text {tr }}{ }^{5}$ | $\begin{aligned} & I_{S}=7 \mathrm{~A} \\ & \mathrm{di} / \mathrm{dt}=100 \mathrm{~A} / \mu \mathrm{s} \end{aligned}$ | - | 60 | - | ns |
| Reverse recovery charge | $\mathrm{Q}_{\text {r }}{ }^{5}$ |  | - | 170 | - | nC |
| Peak reverse recovery current | $1_{\text {Ir }}{ }^{\text {5 }}$ |  | - | 6.5 | - | A |

## - Electrical characteristic curves

Fig. 1 Power Dissipation Derating Curve


Fig. 3 Normalized Transient Thermal Resistance vs. Pulse Width


Fig. 2 Drain Current Derating Curve vs. Junction Temperature


Fig. 4 Maximum Safe Operating Area


## - Electrical characteristic curves

Fig. 5 Avalanche Energy Derating Curve vs. Junction Temperature


Fig. 7 Typical Output Characteristics(I)


Fig. 6 Normalized Breakdown Voltage vs. Junction Temperature


Fig. 8 Typical Output Characteristics(II)


## - Electrical characteristic curves

Fig. 9 Typical Transfer Characteristics


Fig. 11 Static Drain - Source On - State
Resistance vs. Gate Source Voltage


Fig. 10 Normalized Gate Threshold . Voltage vs Junction Temperature


Fig. 12 Normalized Static Drain - Source
On - State Resistance
vs. Junction Temperature


## - Electrical characteristic curves

Fig. 13 Static Drain - Source On - State Resistance vs. Drain Current


Fig. 15 Typical Coss Stored Energy


Fig. 14 Typical Capacitance vs. Drain - Source Voltage


Fig. 16 Typical Gate Charge


## - Electrical characteristic curves

Fig. 17 Inverse Diode Forward Current vs. Source - Drain Voltage


Fig. 18 Reverse Recovery Time vs. Inverse Diode Forward Current


## - Measurement circuits

Fig.1-1 Switching Time Measurement Circuit


Fig.2-1 Gate Charge Measurement Circuit


Fig.3-1 Avalanche Measurement Circuit


Fig.4-1 Diode Recovery Measurement Circuit


Fig.1-2 Switching Waveforms


Fig.2-2 Gate Charge Waveform


Fig.3-2 Avalanche Waveform


Fig.4-2 Diode Recovery Waveform


## - Dimensions

TO-220FM


| DIM | MILIMETERS |  | INCHES |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN | MAX | MIN | MAX |  |
| A | 15.67 | 16.27 | 0.617 | 0.641 |  |
| A1 | 3.03 | 3.43 | 0.119 | 0.135 |  |
| b | 0.70 | 0.95 | 0.028 | 0.037 |  |
| b1 | 1.00 | 1.40 | 0.039 | 0.055 |  |
| b2 | 1.10 | 1.50 | 0.043 | 0.059 |  |
| c | 0.45 | 0.65 | 0.018 | 0.026 |  |
| D | 9.90 | 10.30 | 0.390 | 0.406 |  |
| E | 4.60 | 5.00 | 0.181 | 0.197 |  |
| E1 | 2.44 | 2.74 | 0.096 | 0.108 |  |
| e |  |  |  |  |  |
| F | 3.54 |  | 0.100 |  |  |
| L | 12.6 | 13.50 | 0.122 | 0.138 |  |
| P | 2.98 | 3.38 | 0.496 | 0.535 |  |
| Q | 2.25 | 3.25 | 0.089 | 0.173 |  |

Dimension in mm / inches

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| :---: | :---: | :---: | :---: |
| CLASSIII | CLASSIII | CLASS II b | CLASSIII |
|  |  | CLASSIII |  |

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[h] Use of the Products in places subject to dew condensation
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8. Confirm that operation temperature is within the specified range described in the product specification.
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[c] the Products are exposed to direct sunshine or condensation
[d] the Products are exposed to high Electrostatic
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3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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