# R6007RND3

#### Nch 600V 7A Power MOSFET

Datasheet

V <sub>DSS</sub> (@Tj max.)*5	650V
R <sub>DS(on)</sub> (Max.)	0.940Ω
I <sub>DP</sub> *2	±21A
P <sub>D</sub>	96W

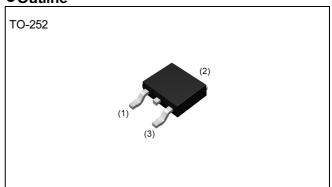
# ● Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

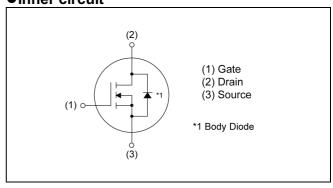
### Application

Switching

#### Outline



#### •Inner circuit



Marking	R6007RND3

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	600	V
Continuous drain current (T <sub>c</sub> = 25°C)	I <sub>D</sub> *1	±7	А
Pulsed drain current	I <sub>DP</sub> *2	±21	А
Gate - Source voltage	$V_{GSS}$	±30	V
Avalanche current, single pulse	I <sub>AS</sub> *3	1.6	А
Avalanche energy, single pulse	E <sub>AS</sub> *3	132	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	96	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

#### ●Thermal resistance

Davamatav	Cymah al	Values			1.1
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	1.29	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	50	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	600	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	-	-	100	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	-	-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 1.0 \text{mA}$	5.0	6.0	7.0	V
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	V <sub>GS</sub> = 15V, I <sub>D</sub> = 3.5A	-	0.730	0.940	Ω
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	2.9	-	Ω

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davanastav	Currente e l	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 100V	-	460	-	
Output capacitance	C <sub>oss</sub>	f = 1MHz	-	23	-	
Effective output capacitance energy related	C <sub>o(er)</sub> <sup>6</sup>	V <sub>GS</sub> = 0V	-	21	-	pF
Effective output capacitance time related	C <sub>o(tr)</sub> <sup>7</sup>	V <sub>DS</sub> = 0V to 480V	-	83	1	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300V$ , $V_{GS} = 15V$	-	14	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 3.5A	-	12	-	
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ~ 85.7Ω	-	30	-	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	22	-	

# • Gate charge characteristics ( $T_a = 25$ °C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	rameter Symbol Conditions		Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	17.5	1	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 7A	-	5.1	1	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 15V	-	6.4	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> ≈ 300V, I <sub>D</sub> = 7A	-	9.5	-	V

### ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Cumbal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current	I <sub>S</sub> *1	T <sub>C</sub> = 25°C	-	-	7	Α	
Pulsed source current	I <sub>SP</sub> *2	1C - 23 C	1	1	21	Α	
Source-Drain voltage	V <sub>SD</sub> *5	$V_{GS} = 0V$ , $I_S = 7A$	-	-	1.7	٧	
Reverse recovery time	<b>t</b> <sub>rr</sub> *5	V <sub>DD</sub> ≃ 300V	-	50	-	ns	
Reverse recovery charge	se recovery charge $Q_{rr}^{*5}$ $I_{S} = 7A$		-	125	-	nC	
Peak reverse recovery current	<sub>rr</sub> *5	di/dt = 100A/μs	-	5	-	Α	

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

<sup>\*3</sup> L  $\simeq$  100mH, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 $\Omega$ , starting T<sub>i</sub> = 25°C

<sup>\*4</sup> Tc=25°C

<sup>\*5</sup> Pulsed

<sup>\*6</sup> Co(er) is a fixed capacitance that gives the same stored energy as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

<sup>\*7</sup> Co(tr) is a fixed capacitance that gives the same charging time as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

#### • Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

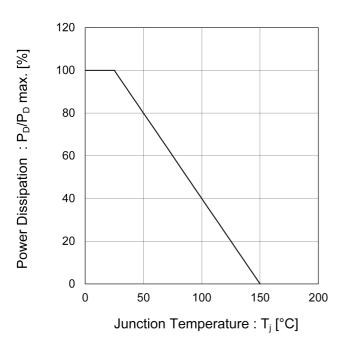


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

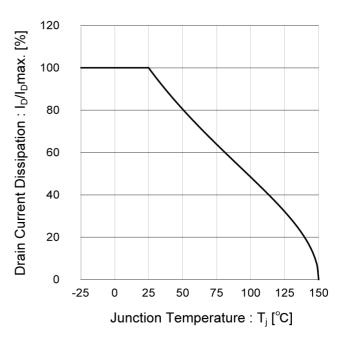


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

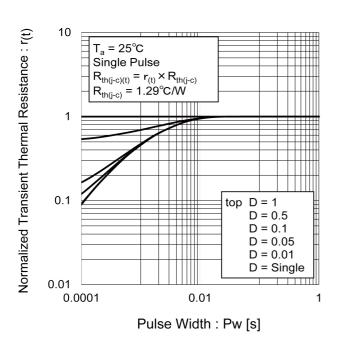
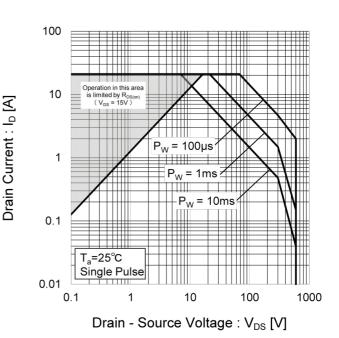


Fig.4 Maximum Safe Operating Area



#### Electrical characteristic curves

Fig.5 Avalanche Energy Derating Curve vs. Junction Temperature

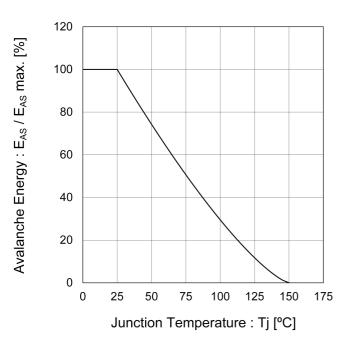


Fig.6 Normalized Breakdown Voltage vs. **Junction Temperature** 

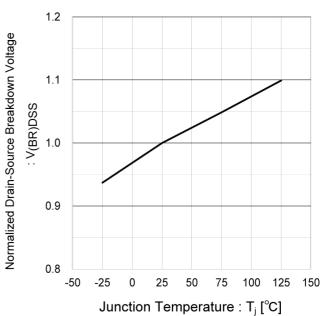
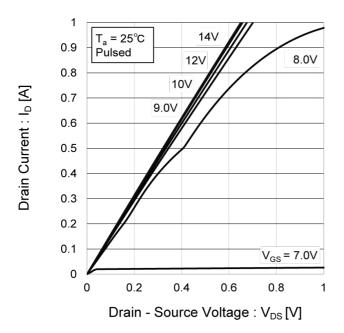


Fig.7 Typical Output Characteristics(I)



Pulsed 6

7 T<sub>a</sub> = 25°C 10V 14V 12V 5 9.0V 4 3 2 8.0V 1  $V_{GS} = 7.0V$ 0 1 3 5 0 4 6

Fig.8 Typical Output Characteristics(II)

Drain - Source Voltage : V<sub>DS</sub> [V]

Drain Current : I<sub>D</sub> [A]

#### Electrical characteristic curves

Fig.9 Typical Transfer Characteristics

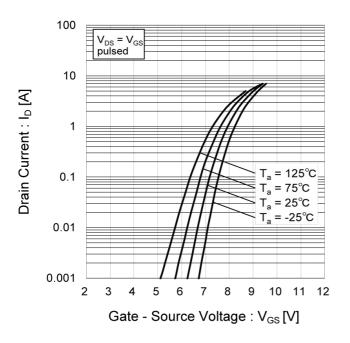


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

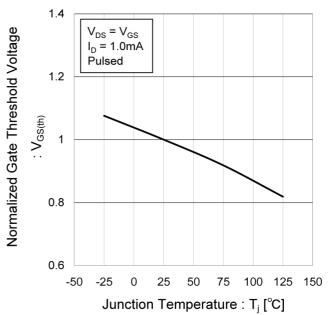


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

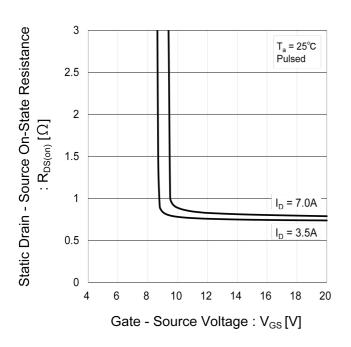
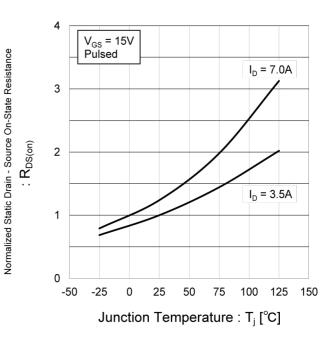


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature



#### • Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

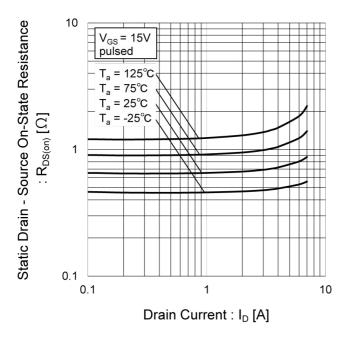


Fig.14 Typical Capacitance vs.
Drain - Source Voltage

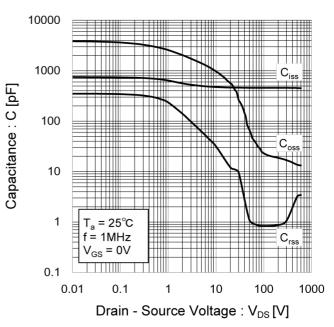


Fig.15 Typical Coss Stored Energy

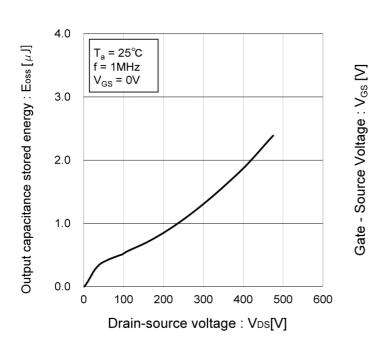
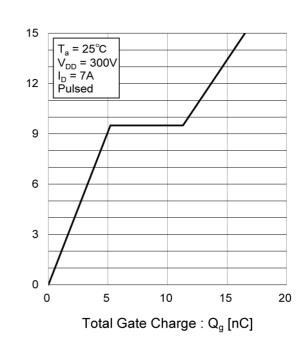


Fig.16 Typical Gate Charge



#### • Electrical characteristic curves

Fig.17 Source Current vs. Source - Drain Voltage

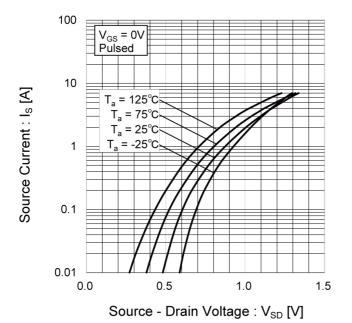
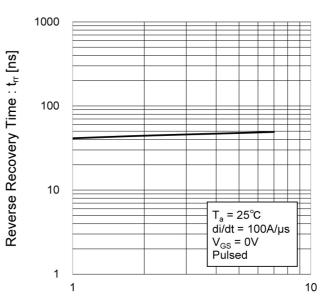


Fig.18 Reverse Recovery Time vs. Source Current



Source Current : I<sub>S</sub> [A]

#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

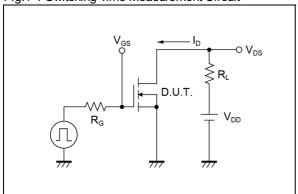


Fig.2-1 Gate Charge Measurement Circuit

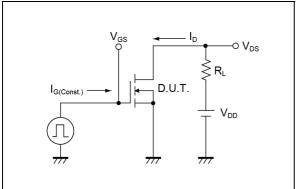


Fig.3-1 Avalanche Measurement Circuit

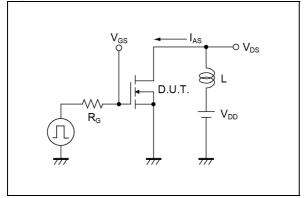


Fig.4-1 Diode Recovery Measurement Circuit

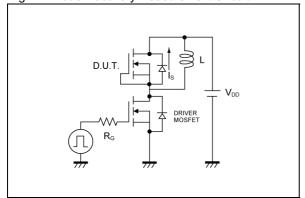


Fig.1-2 Switching Waveforms

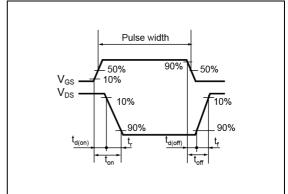


Fig.2-2 Gate Charge Waveform

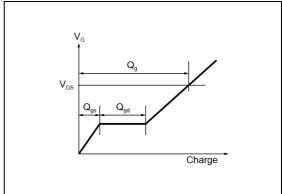


Fig.3-2 Avalanche Waveform

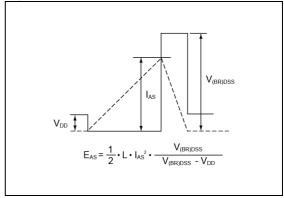
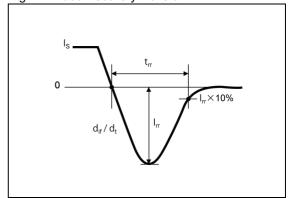
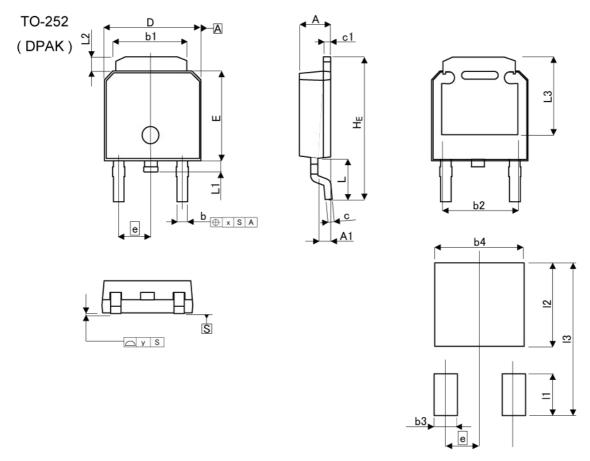


Fig.4-2 Diode Recovery Waveform



#### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIMETERS		INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	2.20	2.40	0.087	0.094
A1	0.70	1.10	0.028	0.043
b	0.60	0.90	0.024	0.035
b1	5.20	5.50	0.205	0.217
b2	4.	80	0.1	189
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.0	)91
E	6.00	6.40	0.236	0.252
HE	9.40	10.40	0.370	0.409
L	2.	90	0.1	114
L1	0.60	1.00	0.024	0.039
L2	0.70	1.30	0.028	0.051
L3	5.	5.30		209
х	-	0.25	-	0.010
у	-	0.10	-	0.004
DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b3	- 1	1.15	-	0.045
b4	-	5.55	-	0.219
I1	-	2.77	-	0.109
12	-	5.50	(-)	0.217
13	-	10.40	-	0.409

Dimension in mm/inches



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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
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  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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