

R6009ENJ

V _{DSS}	600V
R _{DS(on)} (Max.)	0.535Ω
I _D	±9A
PD	94W

Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V_{GSS}) guaranteed to be ± 20 V.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.

Application

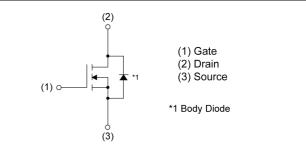
Switching

6) Pb-free lead plating ; RoHS compliant

• Outline TO-263S (2) SC-83



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	24
	Quantity (pcs)	1000
	Taping code	TL
	Marking	R6009ENJ

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V _{DSS}	600	V
Continuous ducin cumont	T _C = 25°C	۱ _D *1	±9	А
Continuous drain current $T_{\rm C} = 100^{\circ}{\rm C}$		۱ _D *1	±4.9	А
Pulsed drain current		^{*2}	±18	А
Cata Cauraa valtaga	static	V _{GSS}	±20	V
Gate - Source voltage	AC(f>1Hz)		±30	V
Avalanche current, repetitive		I _{AR}	1.4	Α
Avalanche energy, single pulse		E_{AS}^{*3}	153	mJ
Avalanche energy, repetitive		E_{AR}^{*3}	0.23	mJ
Power dissipation ($T_C = 25^{\circ}C$)		P _D ^{*4}	94	W
Junction temperature		Tj	150	°C
Operating junction and storage ter	nperature range	T _{stg}	-55~+150	°C

● Absolute maximum ratings (T_a = 25°C)

Parameter	Symbol	Conditions	Values	Unit
Reverse diode dv/dt	dv/dt	-	15	V/ns
Drain - Source voltage slope	dv/dt	V _{DS} = 480V, T _j = 25°C	50	V/ns

•Thermal resistance

Perameter	Sumbol		Values		Unit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	1.3	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

•Electrical characteristics (T_a = 25°C)

Parameter	Sumbol	Conditions		Values		Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	600	-	-	V
		V _{DS} = 600V, V _{GS} = 0V				
Zero gate voltage drain current	I _{DSS}	T _j = 25°C	-	0.1	100	μA
		T _j = 125°C	-	-	1000	
Gate - Source leakage current	I _{GSS}	V_{GS} = ±20V, V_{DS} = 0V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V _{DS} = 10V, I _D = 1mA	2	-	4	V
		V _{GS} = 10V, I _D = 2.8A				
Static drain - source on - state resistance	${\sf R}_{\sf DS(on)}{}^{*6}$	T _j = 25°C	-	0.500	0.535	Ω
		T _j = 125°C	-	1.00	-	
Gate resistance	R _G	f =1MHz, open drain	-	9.6	-	Ω



• Electrical characteristics (T_a = 25°C)

Devenetor	Currence of	Conditions		Values		l locit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Transfer Admittance	Y _{fs} ⁵	V _{DS} = 10V, I _D = 4.5A	2.3	4.5	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	430	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	470	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	55	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V	-	23	-	. 5
Effective output capacitance, time related	C _{o(tr)}	V _{DS} = 0V to 480V	-	100	-	рF
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 300 \text{V}, \text{V}_{GS} = 10 \text{V}$	-	25	-	
Rise time	t _r *6	I _D = 4.5A	-	35	-	
Turn - off delay time	t _{d(off)} *6	R _L ≃ 66.7Ω	-	75	-	ns
Fall time	t _f *6	R _G = 10Ω	-	30	-	

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Sumbol	Conditions		Values		Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*6}	V _{DD} ≃ 300V,	-	23	-	
Gate - Source charge	Q _{gs} *6	I _D = 9.0A,	-	4	-	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	15	-	
Gate plateau voltage	V _(plateau)	V _{DD} = 300V, I _D = 9A	-	6.4	-	V

*1 Limited only by maximum channel temperature allowed.

*2 Pw \leq 10µs, Duty cycle \leq 1%

*3 L \doteqdot 100mH, V_{DD}=50V, R_G=25 Ω , STARTING T_i=25°C

*4 T_C=25°C

*5 Mounted on a epoxy PCB FR4 (25mm x 27mm x 0.8mm)

*6 Pulsed



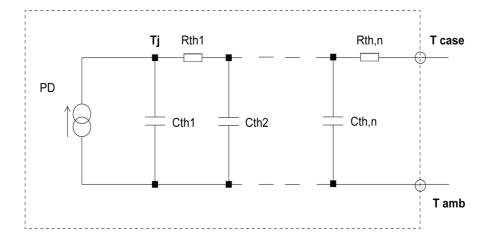


•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions		Values		Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	ا _S *1	T _c = 25°C	-	-	9	A
Pulse forward current	۱ _{SP} *2	$T_{c} = 25 C$	-	-	18	А
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = 9.0A	-	-	1.5	V
Reverse recovery time	t _{rr} *6		-	380	-	ns
Reverse recovery charge	Q _{rr} *6	I _S = 9A, V _{GS} =0V di/dt = 100A/µs	-	3.8	-	μC
Peak reverse recovery current	I _{mm} *6		-	20	-	А

•Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	0.154		C _{th1}	0.00110	
R _{th2}	0.621	K/W	C _{th2}	0.00322	Ws/K
R _{th3}	0.634		C _{th3}	0.156	





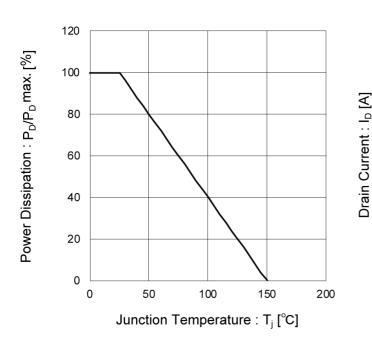


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

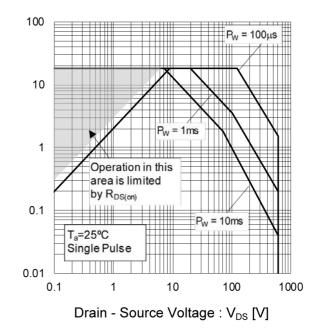
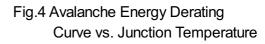


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



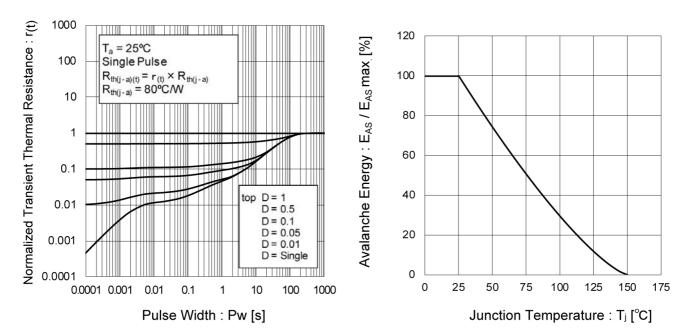






Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)

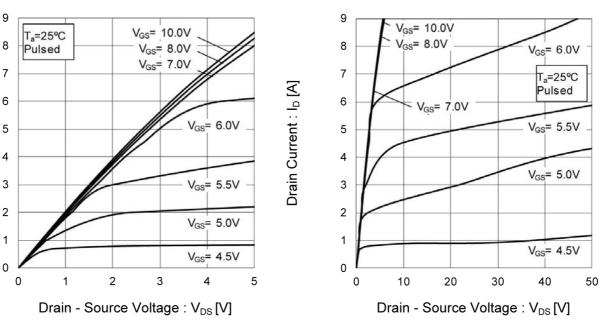


Fig.7 Tj = 150°C Typical Output Characteristics (I)



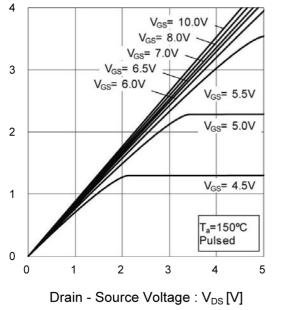
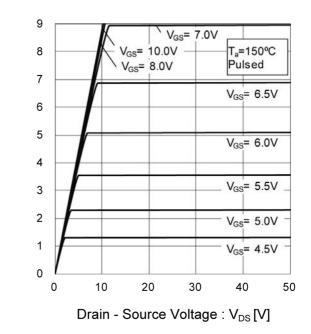


Fig.8 Tj = 150°C Typical Output Characteristics (II)



Drain Current : I_D [A]



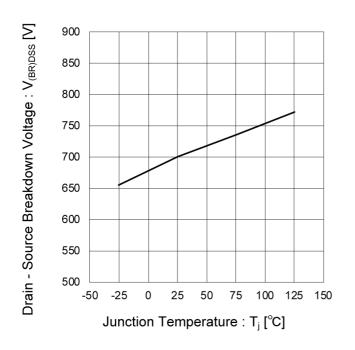


Fig.9 Breakdown Voltage vs. Junction Temperature

Fig.10 Typical Transfer Characteristics

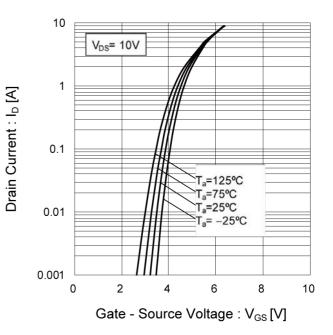


Fig.11 Gate Threshold Voltage vs. Junction Temperature

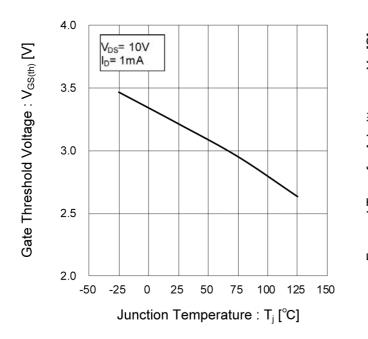
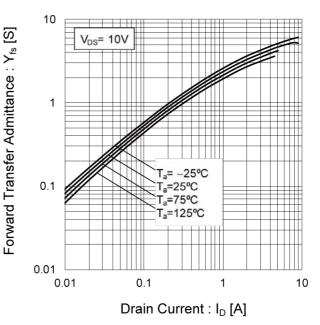


Fig.12 Forward Transfer Admittance vs. Drain Current



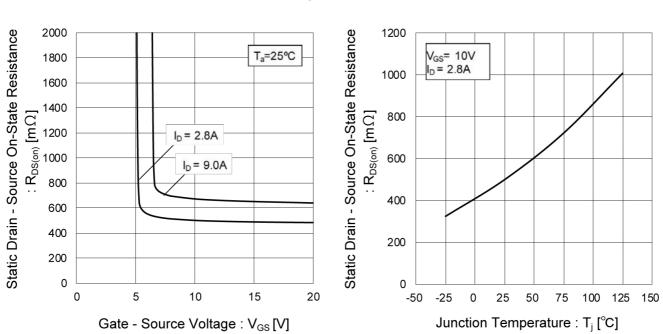
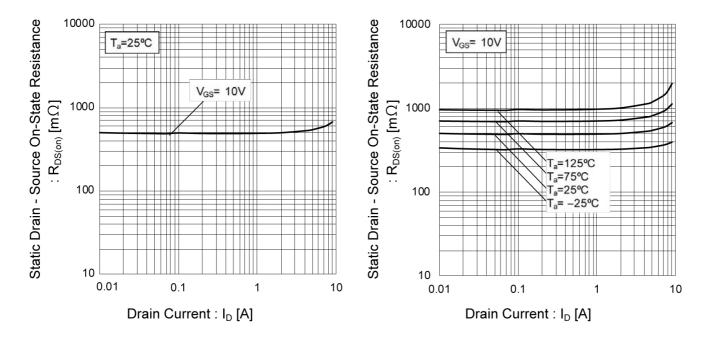


Fig.13 Static Drain - Source On - State Resistance vs. Gate Source Voltage Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(I) Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(II)





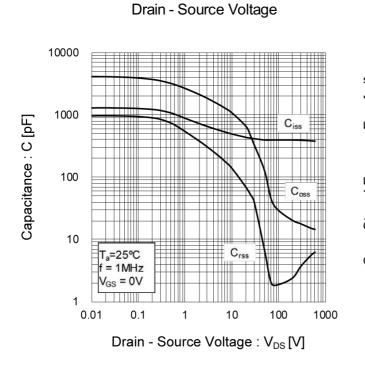


Fig.17 Typical Capacitance vs.

Fig.18 Coss Stored Energy

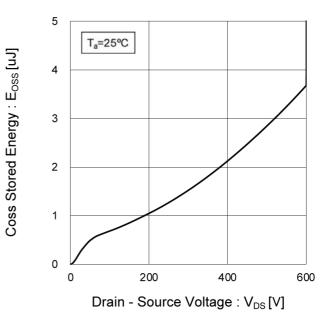
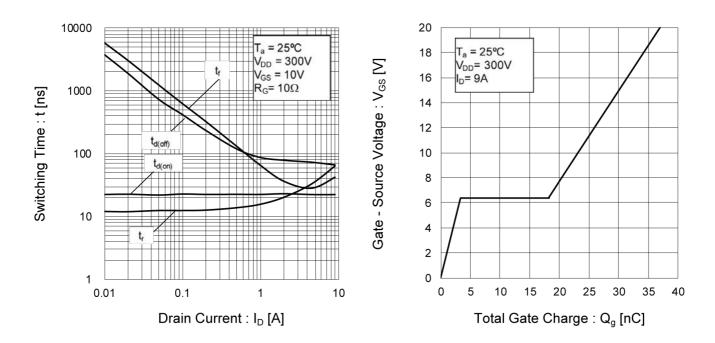


Fig.19 Switching Characteristics

Fig.20 Dynamic Input Characteristics





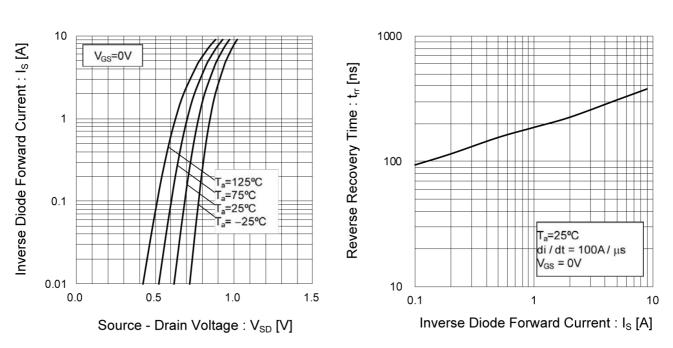
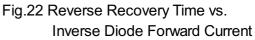


Fig.21 Inverse Diode Forward Current vs. Source - Drain Voltage





Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

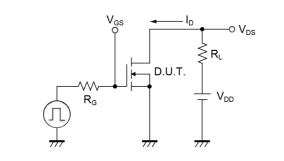


Fig.2-1 Gate Charge Measurement Circuit

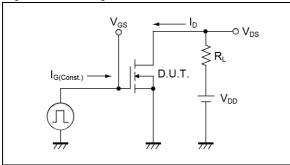


Fig.3-1 Avalanche Measurement Circuit

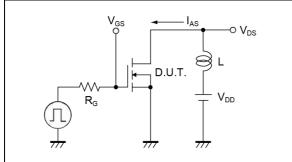


Fig.4-1 dv/dt Measurement Circuit

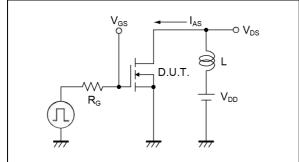


Fig.5-1 dv/dt Measurement Circuit

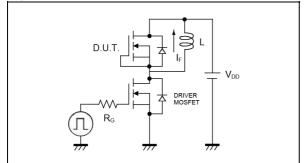


Fig.1-2 Switching Waveforms

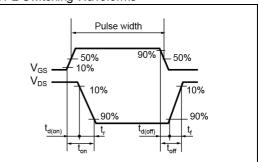


Fig.2-2 Gate Charge Waveform

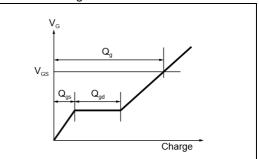


Fig.3-2 Avalanche Waveform

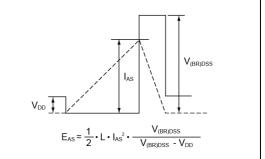


Fig.4-2 dv/dt Waveform

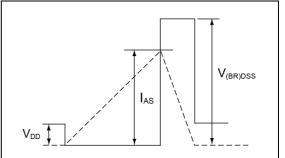
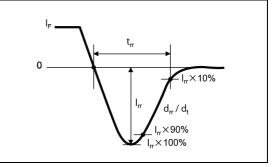
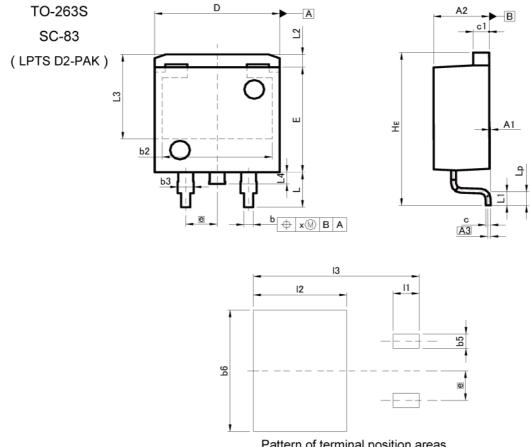


Fig.5-2 dv/dt Waveform





Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES	
DIM -	MIN	MAX	MIN	MAX	
A1	0.00	0.30	0.000	0.012	
A2	4.30	4.70	0.169	0.185	
A3	0.25		0.0	10	
b	0.68	0.98	0.027	0.039	
b2	8.90			350	
b3	1.14	1.44	0.045	0.057	
C	0.30	0.60	0.012	0.024	
c1	1.10	1.50	0.043	0.059	
D	9.80	10.40	0.386	0.409	
E	8.80	9.20	0.346	0.362	
e	2.	54	0.100		
HE	12.80	13.40	0.504	0.528	
L	2.70	3.30	0.106	0.130	
L1	1.	20	0.047		
L2	1.	10	0.043		
L3	7.	25	0.285		
L4	1.	00	0.0)39	
Lp	0.90	1.50	0.035	0.059	
x	A	0.25	-	0.010	
	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
b5		1.23	-	0.049	
b6	-	10.40	· · · · · · · · · · · · · · · · · · ·	0.409	
11	<u>22</u> 8	2.10	<u>, 12</u>	0.083	
12		7.55	1. 1979:	0.297	
13	-	13.40	-	0.528	

Dimension in mm/inches





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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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