

# $V_{\text{DSS}}$ 600V R<sub>DS(on)</sub>(Max.) $0.585\Omega$ ±9A

# $I_D$ $P_D$ 53W

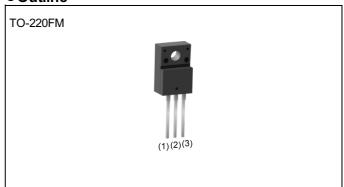
## Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

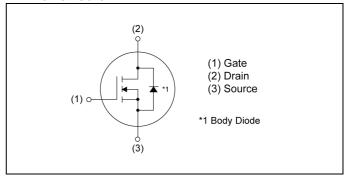
## Application

Switching

## Outline



## •Inner circuit



Packaging specifications

Packing	Tube
Packing code	C7 G
Marking	R6009JNX
Basic ordering unit (pcs)	2000

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	600	V
Continuous drain current (T <sub>c</sub> = 25°C)	I <sub>D</sub> *1	±9	Α
Pulsed drain current	I <sub>DP</sub> *2	±27	Α
Gate - Source voltage	V <sub>GSS</sub>	±30	V
Avalanche current, single pulse	I <sub>AS</sub> *3	1.8	Α
Avalanche energy, single pulse	E <sub>AS</sub> *3	177	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	53	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Davamatav	Cymah al	Values			l lait
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	2.37	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	70	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter 	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		600	-	-	V
Zero gate voltage drain current	$I_{DSS}$ $V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^{\circ}C$		1	-	100	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30V$ , $V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 1.38 \text{mA}$	5.0	6.0	7.0	V
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	$V_{GS} = 15V, I_D = 4.5A$ $T_j = 25^{\circ}C$	-	0.450	0.585	Ω
Gate resistance	R <sub>G</sub> f = 1MHz, open drain		-	2.1	-	Ω

## ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatav	Cymah al	Conditions	Values			Linit
Parameter	Symbol	Symbol Conditions		Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	645	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 100V	-	40	-	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	1.5	-	_
Effective output capacitance energy related	C <sub>o(er)</sub> 6	V <sub>GS</sub> = 0V	-	32	-	pF
Effective output capacitance time related	C <sub>o(tr)</sub> <sup>7</sup>	V <sub>DS</sub> = 0V to 480V	-	120	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300V$ , $V_{GS} = 15V$	-	20	-	
Rise time	<b>t</b> <sub>r</sub> *5	I <sub>D</sub> = 4.5A	-	16	-	20
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ≃ 68.1Ω	-	38	-	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	20	-	

## ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Darameter	Cymabal	Conditions	Values			1.1:4
Parameter	Symbol	ool Conditions		Тур.	Max.	Unit
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	22.0	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 9A	-	6.4	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 15V	-	8.0	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> ≈ 300V, I <sub>D</sub> = 9A	-	9.2	-	V

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> L  $\simeq$  100mH, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 $\Omega$ , starting T<sub>i</sub> = 25°C

<sup>\*4</sup> Tc=25°C

<sup>\*5</sup> Pulsed

<sup>\*6</sup> Co(er) is a fixed capacitance that gives the same stored energy as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

<sup>\*7</sup> Co(tr) is a fixed capacitance that gives the same charging time as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

## ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current	I <sub>S</sub> *1	- T <sub>C</sub> = 25°C	1	-	9	Α	
Pulsed source current	I <sub>SP</sub> *2	1C - 23 C	1	-	27	Α	
Source-Drain voltage	V <sub>SD</sub> *5	$V_{GS} = 0V$ , $I_S = 9A$	-	-	1.7	V	
Reverse recovery time	<b>t</b> <sub>rr</sub> *5		-	65	-	ns	
Reverse recovery charge	Q <sub>rr</sub> *5	I <sub>S</sub> = 9A di/dt = 100A/μs	-	195	-	nC	
Peak reverse recovery current	<sub>rr</sub> *5		-	7.0	-	Α	

## • Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

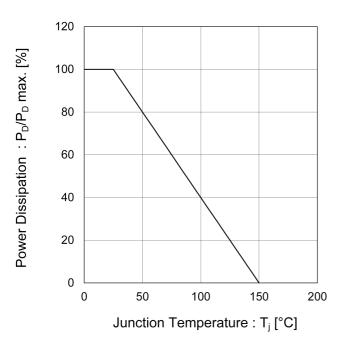


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

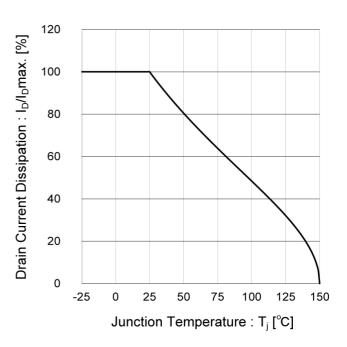


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

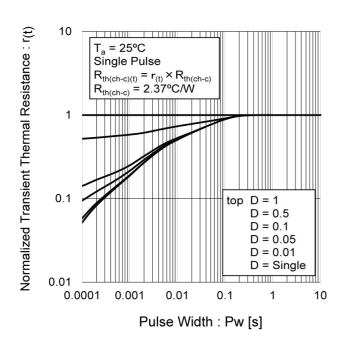
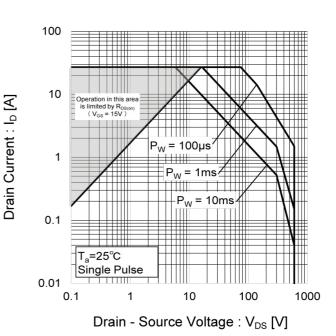


Fig.4 Maximum Safe Operating Area



## • Electrical characteristic curves

Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

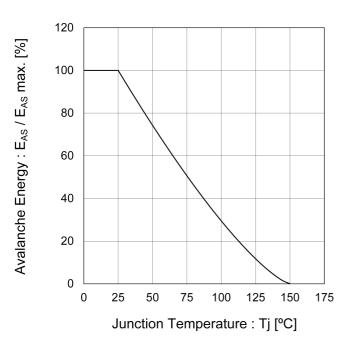


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

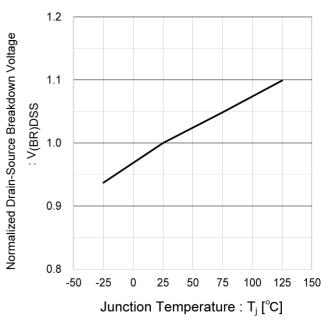
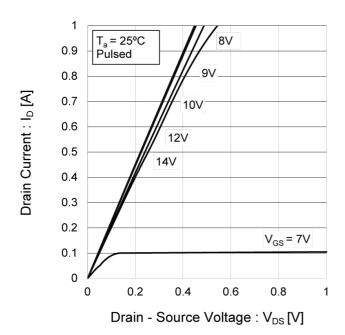


Fig.7 Typical Output Characteristics(I)



Drain Current : I<sub>D</sub> [A]

7 10V T<sub>a</sub> = 25°C Pulsed 6 14V 9.0V 5 12V 4 3 8.0V 2 1  $V_{GS} = 7.0V$ 0 0 1 3 4 5 6 Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.8 Typical Output Characteristics(II)

## Electrical characteristic curves

Fig.9 Typical Transfer Characteristics

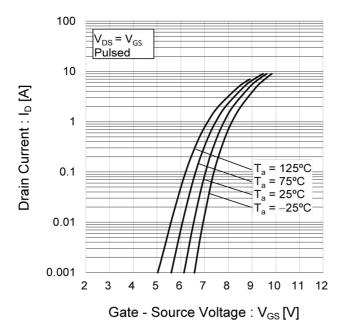


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

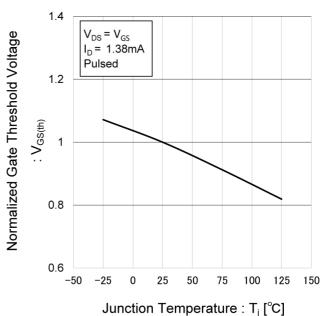


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

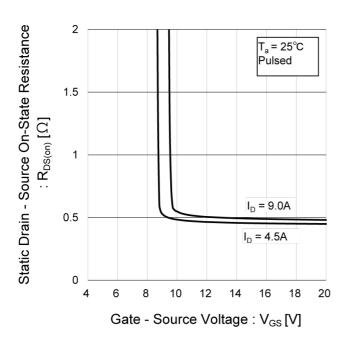
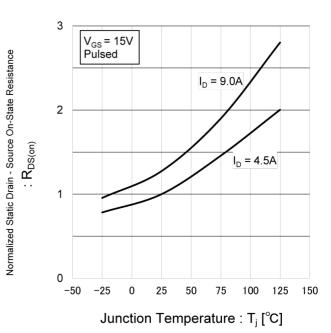


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature



## Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

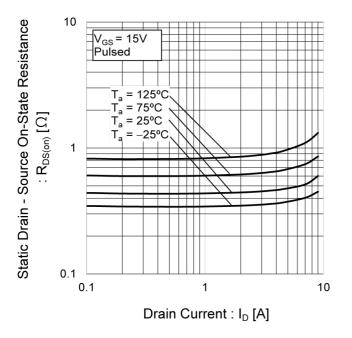


Fig.14 Typical Capacitance vs.
Drain - Source Voltage

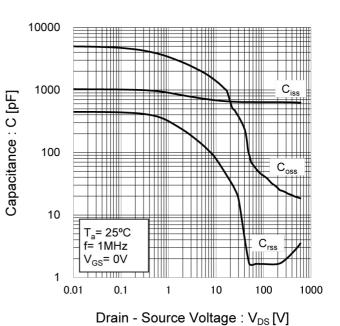


Fig.15 Typical Coss Stored Energy

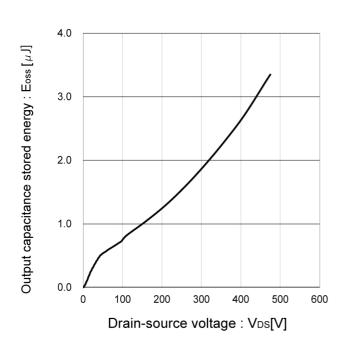
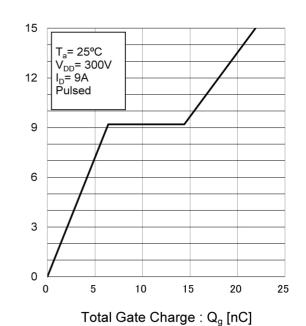


Fig.16 Typical Gate Charge



Gate - Source Voltage :  $V_{GS}$  [V]

## • Electrical characteristic curves

Fig.17 Inverse Diode Forward Current vs. Source - Drain Voltage

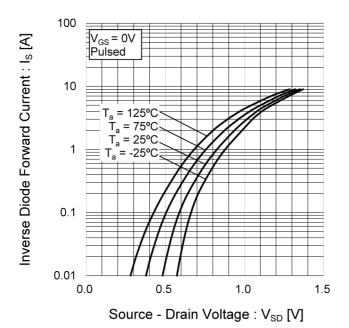
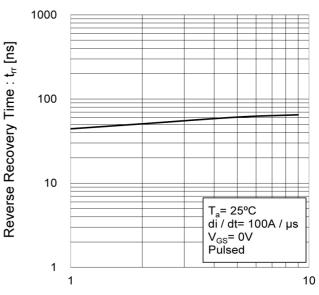


Fig.18 Reverse Recovery Time vs.
Inverse Diode Forward Current



## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

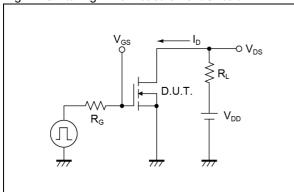


Fig.2-1 Gate Charge Measurement Circuit

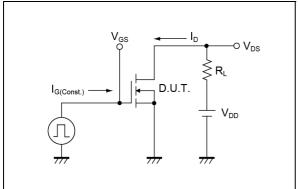


Fig.3-1 Avalanche Measurement Circuit

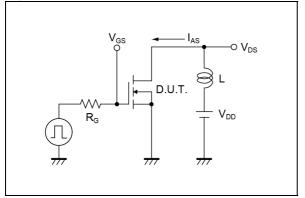


Fig.4-1 Diode Recovery Measurement Circuit

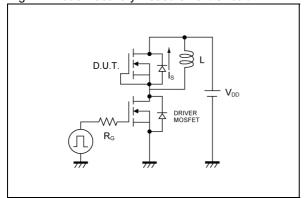


Fig.1-2 Switching Waveforms

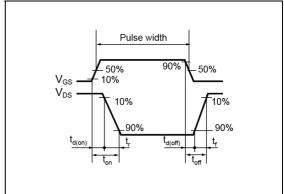


Fig.2-2 Gate Charge Waveform

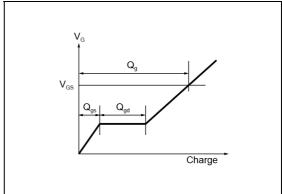


Fig.3-2 Avalanche Waveform

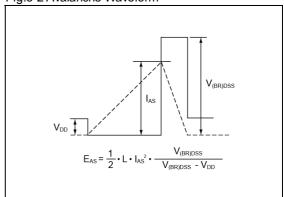
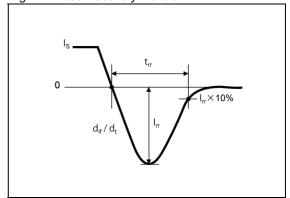
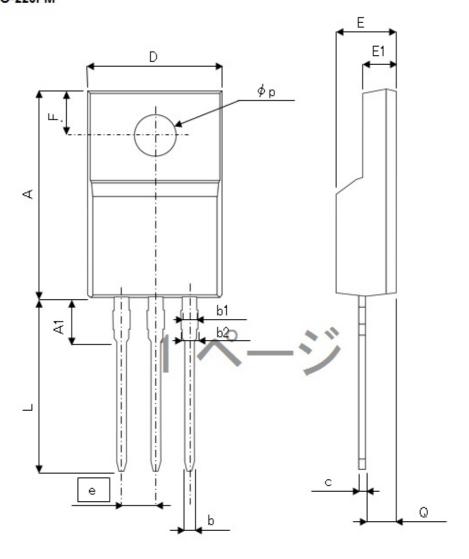


Fig.4-2 Diode Recovery Waveform



## Dimensions

TO-220FM



DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	15.67	16.27	0.617	0.641
A1	3.03	3.43	0.119	0.135
Ь	0.70	0.95	0.028	0.037
Ь1	1.00	1.40	0.039	0.055
ь2	1.10	1.50	0.043	0.059
С	0.45	0.65	0.018	0.026
D	9.90	10.30	0.390	0.406
E	4.60	5.00	0.181	0.197
E1	2.44	2.74	0.096	0.108
е	2.	54	0.1	00
F	3.10	3.50	0.122	0.138
L	12.6	13.6	0.496	0.535
р	2.98	3.38	0.117	0.133
Q	2.25	3.25	0.089	0.128

Dimension in mm / inches



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  - [h] Use of the Products in places subject to dew condensation
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