Nch 600V 18A Power MOSFET

V_{DSS}	600V
R _{DS(on)} (Max.)	0.286Ω
I _D	±18A
P _D	220W

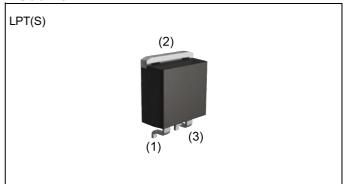
●Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

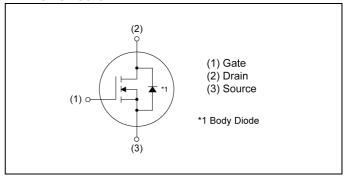
Application

Switching applications

Outline



•Inner circuit



Packaging specifications

<u>_ </u>	
Packing	Embossed Tape
Packing code	TL
Marking	R6018JNJ
Quantity (pcs)	1000

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	600	V
Continuous drain current (T _c = 25°C)	I _D *1	±18	А
Pulsed drain current	I _{DP} *2	±54	А
Gate - Source voltage	V _{GSS}	±30	V
Avalanche current, single pulse	I _{AS} *3	4.4	Α
Avalanche energy, single pulse	E _{AS} *3	526	mJ
Power dissipation (T _c = 25°C)	P _D	220	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Dougnoston	Currele e l	Values			11.11
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	0.57	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

●Electrical characteristics (T_a = 25°C)

Davamatar	Cymah al	Conditions	Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	-	V	
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^{\circ}C$	-	-	100	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	-	-	±100	nΑ	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 4.2 \text{mA}$	5.0	6.0	7.0	V	
Static drain - source on - state resistance	R _{DS(on)} *5	$V_{GS} = 15V, I_D = 9.0A$ $T_j = 25^{\circ}C$	-	0.220	0.286	Ω	
Gate resistance	R_{G}	f = 1MHz, open drain	-	1.7	-	Ω	

● Electrical characteristics (T_a = 25°C)

Davamatar	Cymah al	Conditions	Values			Linit		
Parameter	Symbol	Conditions		Symbol Conditions		Тур.	Max.	Unit
Input capacitance	C _{iss} V _{GS} = 0V		-	1300	-			
Output capacitance C_{oss} $V_{DS} = 100V$		V _{DS} = 100V	-	70	-			
Reverse transfer capacitance C_{rss} $f = 1MHz$		f = 1MHz	-	1.8	1	_		
Effective output capacitance energy related	$C_{o(er)}^{*6}$ $V_{GS} = 0V$		-	63	-	pF		
Effective output capacitance time related	C _{o(tr)} *7	$C_{o(tr)}^{*7}$ $V_{DS} = 0V \text{ to } 480V$		235	1			
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 300V$, $V_{GS} = 15V$	-	26	-			
Rise time	t _r *5	I _D = 9.0A	-	20	-	20		
Turn - off delay time	$t_{d(off)}^{*5} \qquad R_L \cong 33.2\Omega$		-	50	-	ns		
Fall time	t _f *5	$R_G = 10\Omega$	-	12	-			

● Gate charge characteristics (T_a = 25°C)

Darameter	Cumb al	Conditions	Values			Lleit	
Parameter	Symbol Conditions –		Min.	Тур.	Max.	Unit	
Total gate charge	Q_g^{*5} $V_{DD} \simeq 300V$		-	42	-		
Gate - Source charge	Q _{gs} *5	I _D = 18A	-	12	-	nC	
Gate - Drain charge	Q _{gd} *5	V _{GS} = 15V	-	15	-		
Gate plateau voltage	V _(plateau)	V _{DD} ≃ 300V, I _D = 18A	-	9.3	-	V	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10µs, Duty cycle \leq 1%

^{*3} L \simeq 50mH, V_{DD} = 50V, R_G = 25 Ω , starting T_i = 25°C

^{*4} Tc=25°C

^{*5} Pulsed

^{*6} Co(er) is a fixed capacitance that gives the same stored energy as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

^{*7} Co(tr) is a fixed capacitance that gives the same charging time as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current	Is*1		1	-	18	Α	
Pulsed source current	l _{SP} *2	T _C = 25°C	1	-	54	Α	
Source-Drain voltage	V _{SD} *5	V _{GS} = 0V, I _S = 18A	-	-	1.7	V	
Reverse recovery time	t _{rr} *5		1	80	1	ns	
Reverse recovery charge	Q _{rr} *5	I _S = 18A di/dt = 100A/μs	-	280	-	nC	
Peak reverse recovery current	_{rr} *5		-	7.5	-	Α	

Fig.1 Power Dissipation Derating Curve

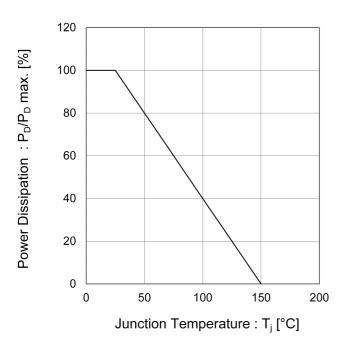


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

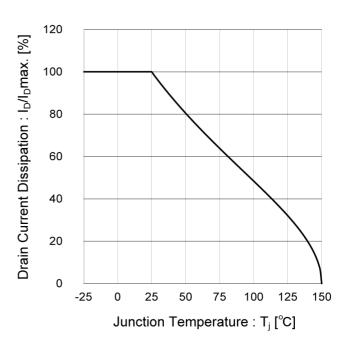


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

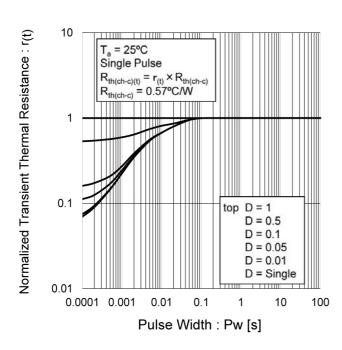


Fig.4 Maximum Safe Operating Area

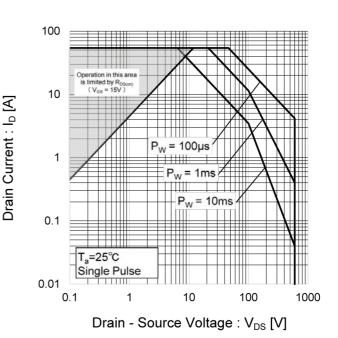


Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

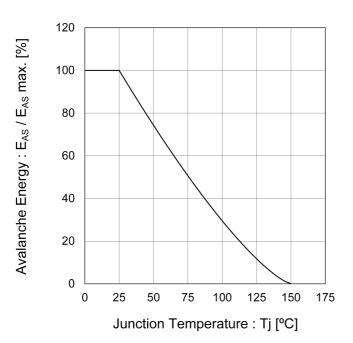


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

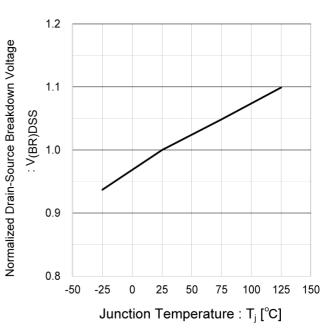


Fig.7 Typical Output Characteristics(I)

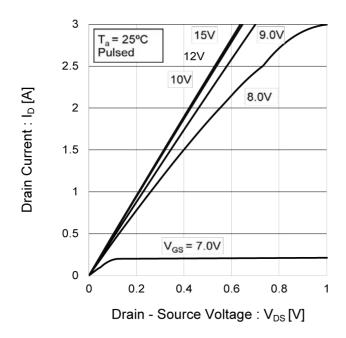
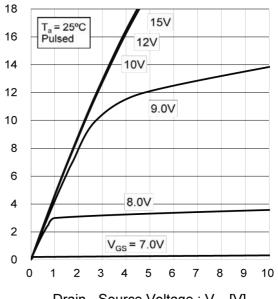


Fig.8 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.9 Typical Transfer Characteristics

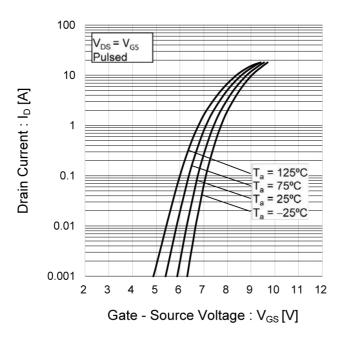


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

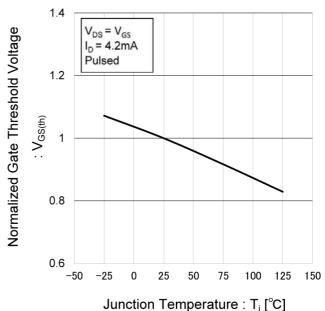


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

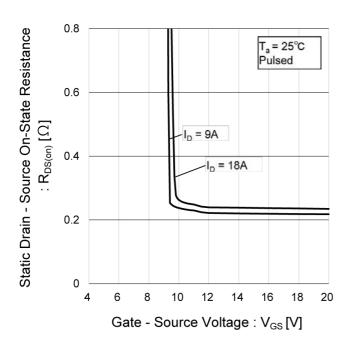


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

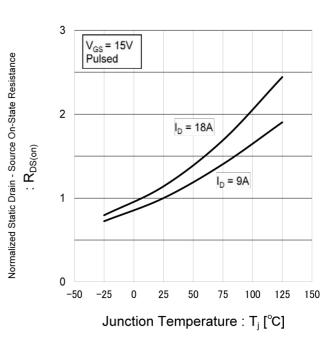


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

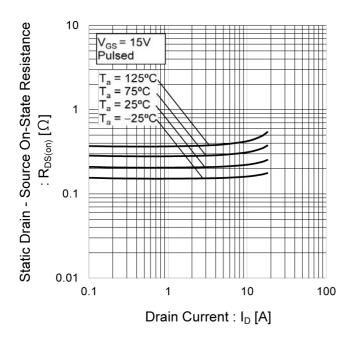


Fig.14 Typical Capacitance vs.
Drain - Source Voltage

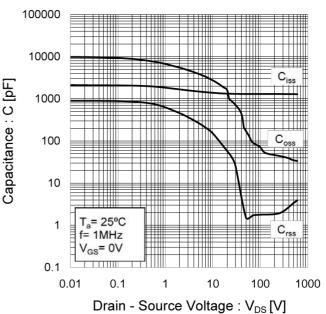


Fig.15 Typical Coss Stored Energy

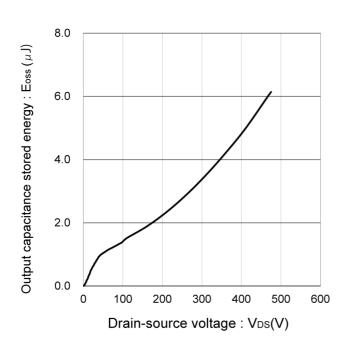
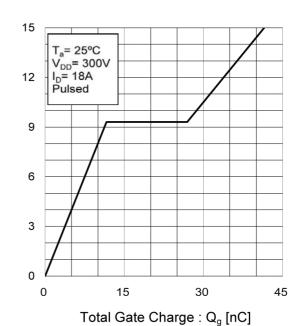


Fig.16 Dynamic Input Characteristics



Gate - Source Voltage : V_{GS} [V]

Fig.17 Inverse Diode Forward Current vs. Source - Drain Voltage

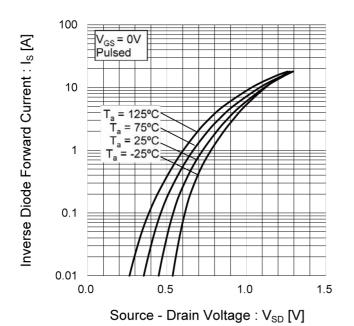
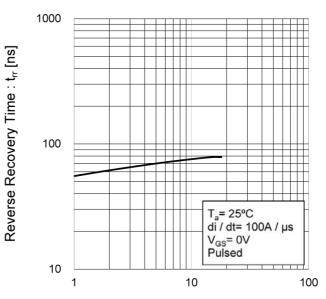


Fig.18 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

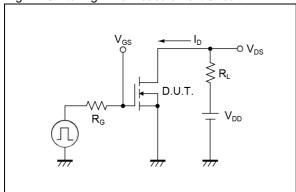


Fig.2-1 Gate Charge Measurement Circuit

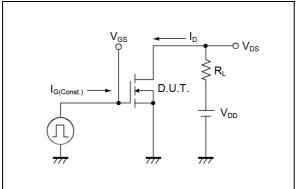


Fig.3-1 Avalanche Measurement Circuit

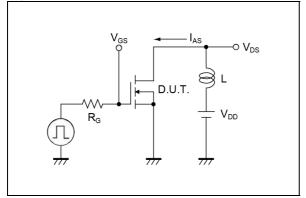


Fig.4-1 Diode Recovery Measurement Circuit

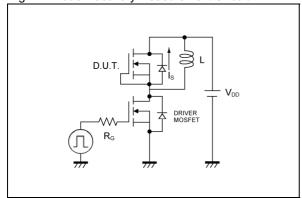


Fig.1-2 Switching Waveforms

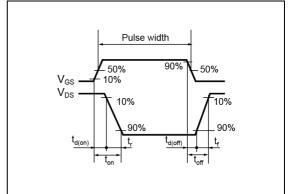


Fig.2-2 Gate Charge Waveform

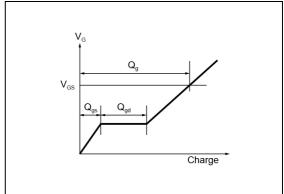


Fig.3-2 Avalanche Waveform

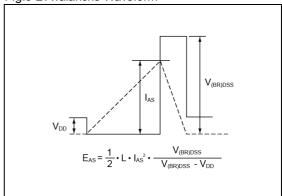
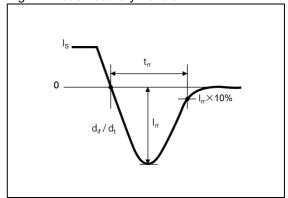
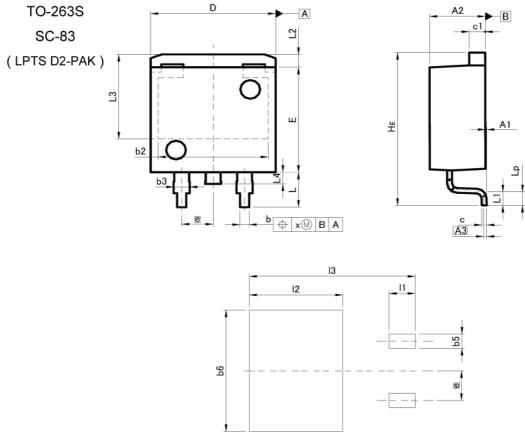


Fig.4-2 Diode Recovery Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.:		0.0	
b	0.68	0.98	0.027	0.039
b2	8.9			50
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.	54	0.1	00
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	1.	20	0.047	
L2	1.	10	0.043	
L3	7.25		0.2	85
L4	1,0	00	0.0	39
Lp	0.90	1.50	0.035	0.059
Х	770	0.25	-	0.010
	MILIMA		INC	1150

DIM	MILIM	MILIMETERS		CHES	
DIM	MIN	MAX	MIN	MAX	
b5	Ξ.	1.23	-	0.049	
b6	=(10.40		0.409	
11	<u>144</u> 6	2.10	, 12	0.083	
12	= X	7.55	1.00	0.297	
13	-	13.40		0.528	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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