Nch 600V 20A Power MOSFET

V_{DSS}	600V
R _{DS(on)} (Max.)	0.234Ω
I _D	±20A
P _D	252W

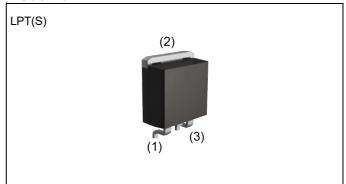
●Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

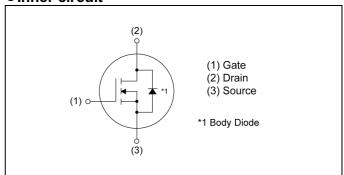
Application

Switching applications

Outline



•Inner circuit



Packaging specifications

Packing	Embossed Tape
Packing code	TL
Marking	R6020JNJ
Quantity (pcs)	1000

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	600	V
Continuous drain current (T _c = 25°C)	I _D *1	±20	Α
Pulsed drain current	I _{DP} *2	±60	Α
Gate - Source voltage	V _{GSS}	±30	V
Avalanche current, single pulse	I _{AS} *3	4.8	Α
Avalanche energy, single pulse	E _{AS} *3	618	mJ
Power dissipation (T _c = 25°C)	P _D	252	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Dougnoston	Cumala a l	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	0.49	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

●Electrical characteristics (T_a = 25°C)

Darameter	Cumb al	Conditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	600	-	-	V	
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^{\circ}C$	-	-	100	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 30 \text{V}, V_{DS} = 600 \text{V}$	-	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 3.5 \text{mA}$	5.0	6.0	7.0	V	
Static drain - source on - state resistance	R _{DS(on)} *5	$V_{GS} = 15V, I_D = 10A$ $T_j = 25^{\circ}C$	-	0.180	0.234	Ω	
Gate resistance	R_{G}	f = 1MHz, open drain	-	2.0	-	Ω	

● Electrical characteristics (T_a = 25°C)

Davameter	Cymah al	Conditions	Values			Linit		
Parameter	Symbol	ool Conditions		Symbol Conditions		Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	1500	-			
Output capacitance C_{oss} V_{DS} = 100V		-	90	-				
Reverse transfer capacitance C_{rss} $f = 1MHz$		-	1.9	-	_			
Effective output capacitance energy related	$C_{o(er)}^{*6}$ $V_{GS} = 0V$		-	68	-	pF		
Effective output capacitance time related	C _{o(tr)} *7	V _{DS} = 0V to 480V	-	270	1			
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 300V$, $V_{GS} = 15V$	-	29	-			
Rise time	t _r *5	I _D = 10A	-	22	-	20		
Turn - off delay time	$t_{d(off)}^{*5}$ $R_L \simeq 30.1\Omega$		-	56	-	ns		
Fall time	t _f *5	$R_G = 10\Omega$	-	13	-			

● Gate charge characteristics (T_a = 25°C)

Darameter	Cumb al	Conditions	Values			l lmit	
Parameter	Symbol	nbol Conditions –		Тур.	Max.	Unit	
Total gate charge	Q_g^{*5} $V_{DD} \simeq 300V$		-	45	-		
Gate - Source charge	Q _{gs} *5	I _D = 20A	-	15	-	nC	
Gate - Drain charge	Q _{gd} *5	V _{GS} = 15V	-	17	-		
Gate plateau voltage	V _(plateau)	V _{DD} ≈ 300V, I _D = 20A	-	9.5	-	V	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10µs, Duty cycle \leq 1%

^{*3} L \simeq 50mH, V_{DD} = 50V, R_G = 25 Ω , starting T_i = 25°C

^{*4} Tc=25°C

^{*5} Pulsed

^{*6} Co(er) is a fixed capacitance that gives the same stored energy as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

^{*7} Co(tr) is a fixed capacitance that gives the same charging time as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current	I _S *1		1	-	20	Α	
Pulsed source current	I _{SP} *2	T _C = 25°C	1	-	60	Α	
Source-Drain voltage V _{SD} *5		$V_{GS} = 0V, I_{S} = 20A$	-	-	1.7	V	
Reverse recovery time	t _{rr} *5		-	85	-	ns	
Reverse recovery charge	Q _{rr} *5	I _S = 20A di/dt = 100A/μs	-	280	-	nC	
Peak reverse recovery current	_{rr} *5		-	7.5	-	Α	

Fig.1 Power Dissipation Derating Curve

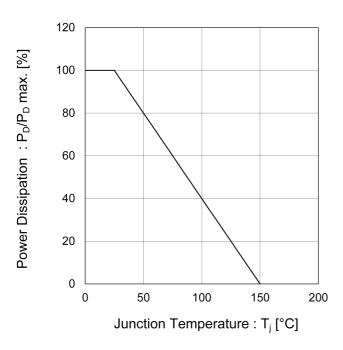


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

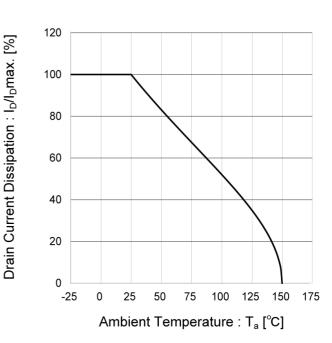


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

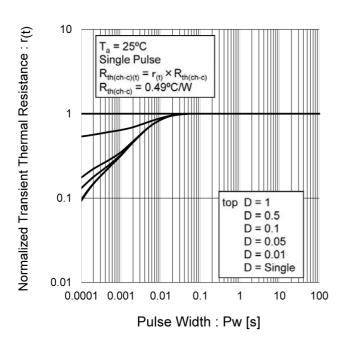


Fig.4 Maximum Safe Operating Area

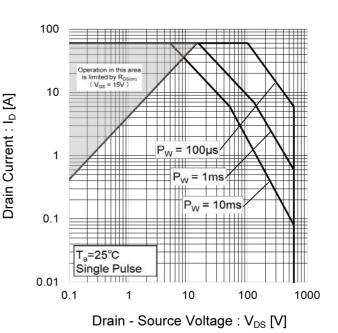


Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

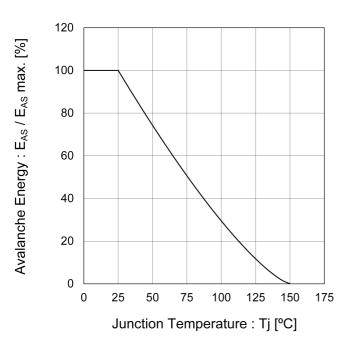


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

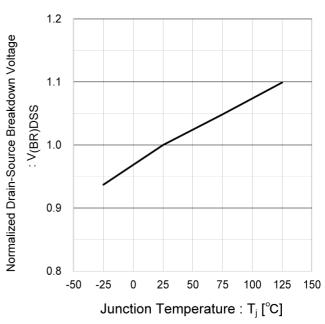


Fig.7 Typical Output Characteristics(I)

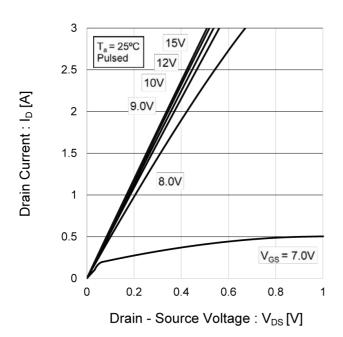


Fig.8 Typical Output Characteristics(II)

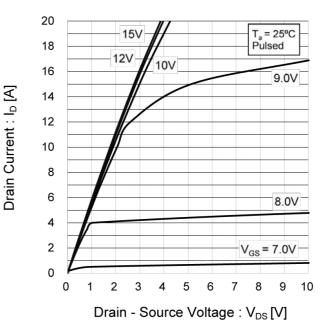


Fig.9 Typical Transfer Characteristics

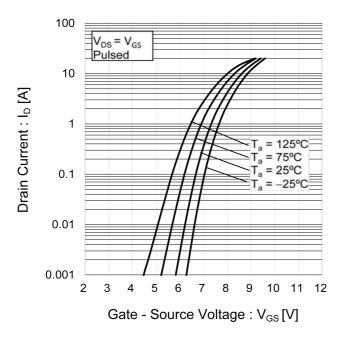


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

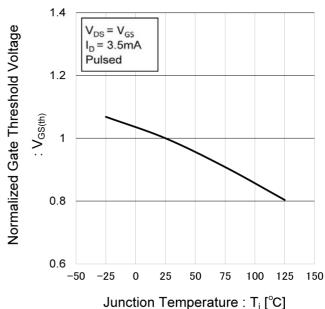


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

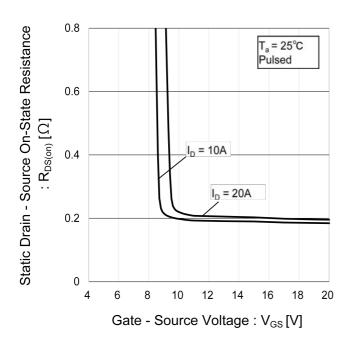


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

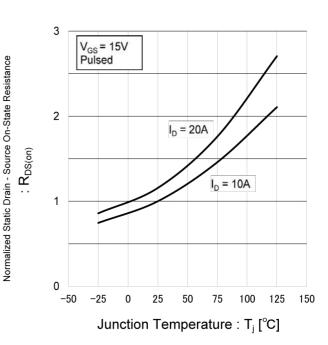


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

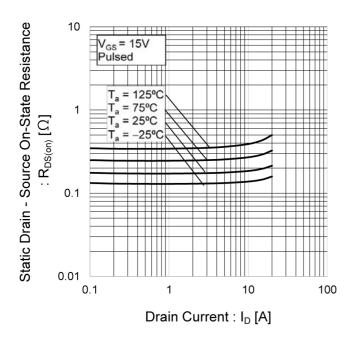


Fig.14 Typical Capacitance vs.
Drain - Source Voltage

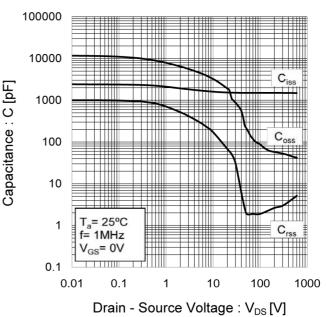


Fig.15 Typical Coss Stored Energy

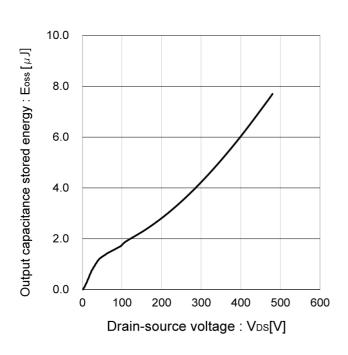
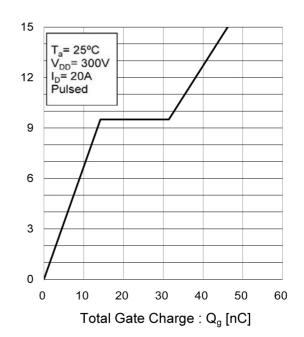


Fig.16 Dynamic Input Characteristics



Gate - Source Voltage : V_{GS} [V]

Fig.17 Inverse Diode Forward Current vs. Source - Drain Voltage

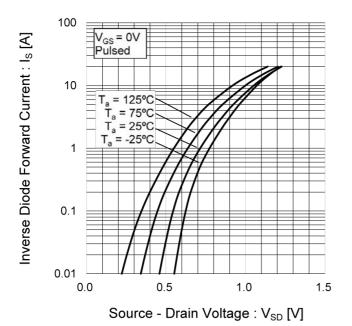
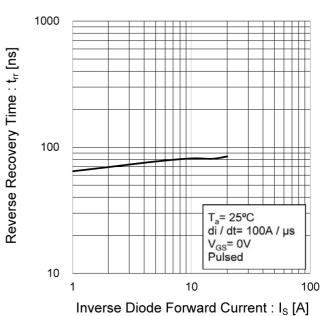


Fig.18 Reverse Recovery Time vs.
Inverse Diode Forward Current



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

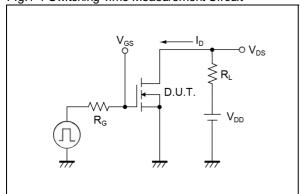


Fig.2-1 Gate Charge Measurement Circuit

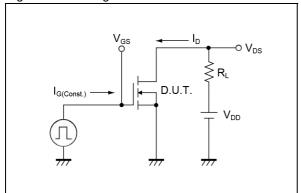


Fig.3-1 Avalanche Measurement Circuit

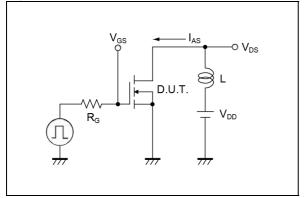


Fig.4-1 Diode Recovery Measurement Circuit

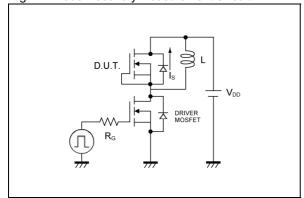


Fig.1-2 Switching Waveforms

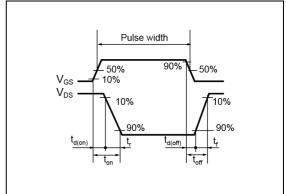


Fig.2-2 Gate Charge Waveform

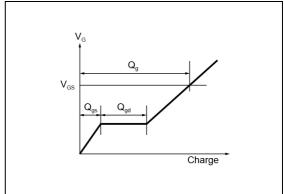


Fig.3-2 Avalanche Waveform

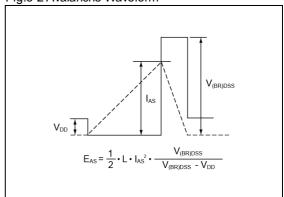
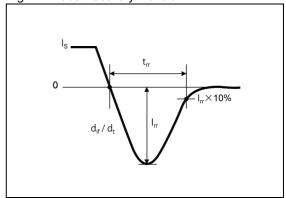
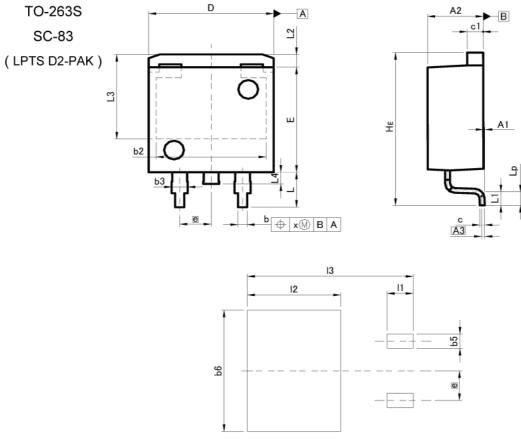


Fig.4-2 Diode Recovery Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

TRA	MILIM	ETERS	INC	HES
IM	MIN	MAX	MIN	MAX
41	0.00	0.30	0.000	0.012
42	4.30	4.70	0.169	0.185
43	0.		0.0	
b	0.68	0.98	0.027	0.039
52		90	0.3	
53	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
:1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
e		54	0.1	
HE	12.80	13.40	0.504	0.528
	2.70	3.30	0.106	0.130
_1	1.	20	0.047	
_2	1.10		0.043	
_3	7.25		0.285	
_4	1.	00	0.0	39
_p	0.90	1.50	0.035	0.059
х	= .	0.25		0.010
	2-28 V	0.23	TNO	

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
bb	=:	1.23	-	0.049
b6	= 0	10.40		0.409
11	23	2.10		0.083
12	77 .4	7.55	1.00	0.297
13	-	13.40	3 	0.528

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
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CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PGA-E Rev.004

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