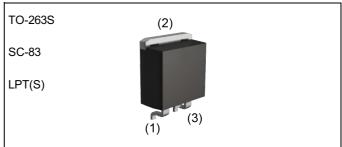
Nch 600V 24A Power MOSFET

V _{DSS}	600V
R _{DS(on)} (Max.)	0.165Ω
I _D	±24A
P _D	245W

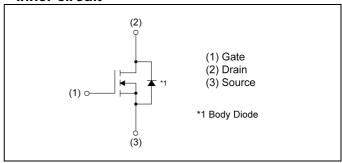
Outline



Features

- 1) Low on-resistance.
- 2) Ultra fast switching speed.
- 3) Parallel use is easy.
- 4) Pb-free lead plating; RoHS compliant

•Inner circuit



Packaging specifications

● I ackaç	Jing specifications	
	Packing	Embossed Tape
	Reel size (mm)	330
Type	Tape width (mm)	24
	Quantity (pcs)	1000
	Taping code	TL
	Marking	R6024KNJ

Application

Switching

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter		Symbol	Value	Unit
Drain - Source voltage		V_{DSS}	600	V
Continuous drain current (T _c = 2	5°C)	I _D *1	±24	А
Pulsed drain current		I _{DP} *2	±72	Α
O-t- O	static	V _{GSS}	±20	V
Gate - Source voltage	AC(f>1Hz)		±30	V
Avalanche current, single pulse		I _{AS}	4.1	Α
Avalanche energy, single pulse		E _{AS} *3	497	mJ
Power dissipation (T _c = 25°C)		P _D	245	W
Junction temperature	T _j	150	°C	
Operating junction and storage to	emperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Davamatav	Cymah al	Values			1.1-:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *4	-	-	0.51	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

• Electrical characteristics $(T_a = 25^{\circ}C)$

Parameter	Cumb al	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	-	V
		V _{DS} = 600V, V _{GS} = 0V				
Zero gate voltage drain current	I _{DSS}	$T_j = 25^{\circ}C$	-	-	100	μΑ
		$T_j = 125^{\circ}C$	-	-	1000	
Gate - Source leakage current	I _{GSS}	V_{GS} = ±20V, V_{DS} = 0V	1	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} = 10V, I_D = 1mA	3	-	5	٧
		V _{GS} = 10V, I _D = 11.3A				
Static drain - source on - state resistance	R _{DS(on)} *6	$T_j = 25^{\circ}C$	-	0.150	0.165	Ω
		$T_j = 125^{\circ}C$	-	0.32	-	
Gate resistance	R_{G}	f = 1MHz, open drain	-	1.9	-	Ω

● Electrical characteristics (T_a = 25°C)

Davanastan	Cy reads ad	Conditions	Values			Lleit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Transfer Admittance	Y _{fs} *6	V _{DS} = 10V, I _D = 12A	6.5	13.0	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	2000	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	1500	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	60	-	
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 300V$, $V_{GS} = 10V$	-	30	-	
Rise time	t _r *6	I _D = 12A	-	50	-	20
Turn - off delay time	t _{d(off)} *6	$R_L \simeq 27.4\Omega$	-	60	1	ns
Fall time	t _f *6	$R_G = 10\Omega$	-	12	-	

● Gate charge characteristics (T_a = 25°C)

Darameter	Cymabal	Conditions	Values			l limit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*6}	V _{DD} ≈ 300V	-	45	-	
Gate - Source charge	Q _{gs} *6	I _D = 24A	-	13	-	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	20	-	
Gate plateau voltage	V _(plateau)	V _{DD} ≈ 300V, I _D = 24A	-	6.8	-	V

^{*1} Limited only by maximum channel temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \doteqdot 70mH, V_{DD}=50V, R_G=25 Ω , STARTING T_i=25 $^{\circ}$ C

^{*4} T_C=25°C

^{*5} Mounted on a epoxy PCB FR4 (25mm x 27mm x 0.8mm)

^{*6} Pulsed

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offit
Continuous forward current	I _S *1	T - 25°C	-	-	24	А
Pulse forward current	I _{SP} *2	T _C = 25°C	-	-	72	А
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = 24A	-	-	1.5	V
Reverse recovery time	t _{rr} *6		-	510	-	ns
Reverse recovery charge	Q _{rr} *6	$I_S = 24A$ di/dt = 100A/µs	-	9.0	-	μC
Peak reverse recovery current	I _{rrm} *6	αναι 100/4μ3	-	32.5	-	Α

Typical transient thermal characteristics

Symbol	Value	Unit
R _{th1}	0.0578	
R _{th2}	0.218	K/W
R _{th3}	0.604	

Symbol	Value	Unit
C _{th1}	0.00248	
C _{th2}	0.00916	Ws/K
C _{th3}	0.209	

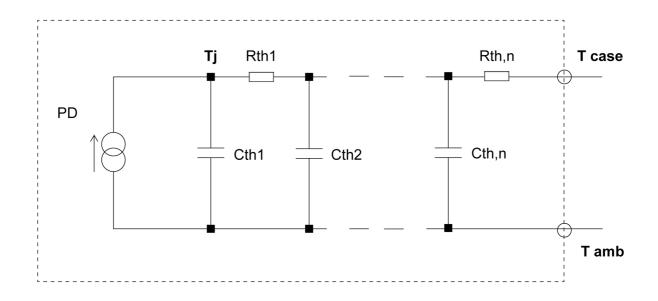


Fig.1 Power Dissipation Derating Curve

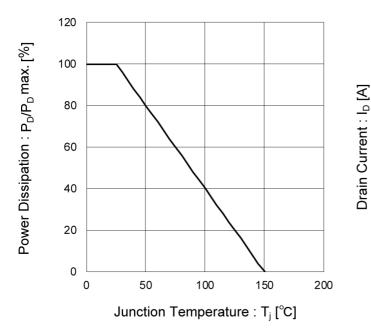


Fig.2 Maximum Safe Operating Area

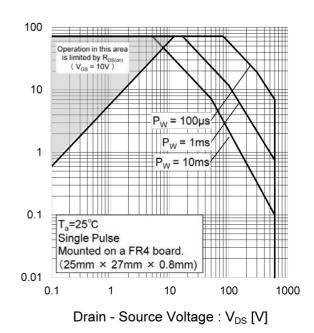


Fig.3 Avalanche Energy Derating
Curve vs. Junction Temperature

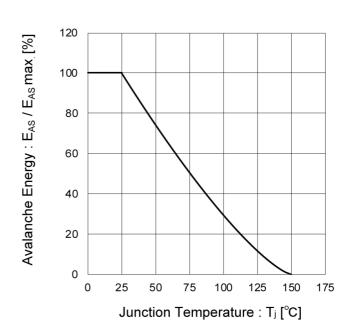


Fig.4 Typical Output Characteristics(I)

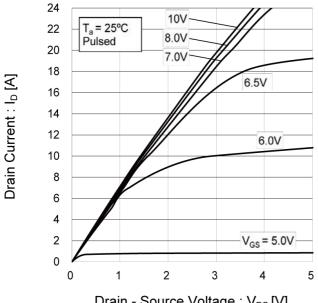
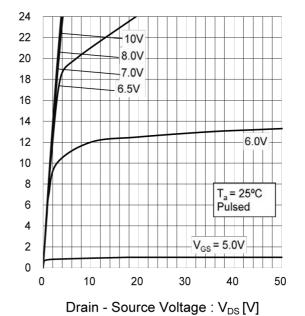


Fig.5 Typical Output Characteristics(II)



Drain Current : I_D [A]



Fig.6 Breakdown Voltage vs.

Junction Temperature

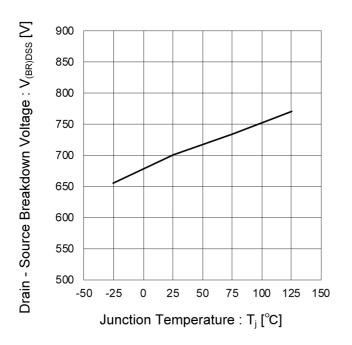


Fig.7 Typical Transfer Characteristics

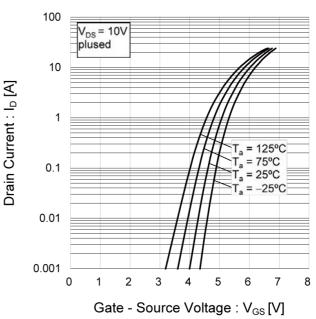


Fig.8 Gate Threshold Voltage vs.
Junction Temperature

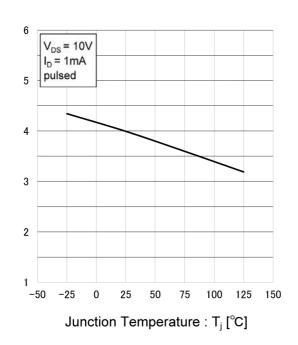
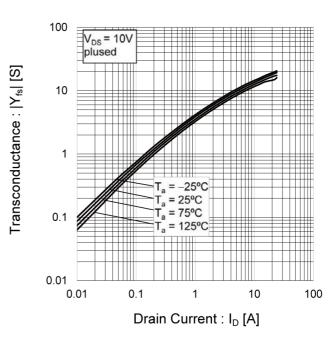


Fig.9 Forward Transfer Admittance vs.

Drain Current



Gate Threshold Voltage: VGS(th) [V]

Fig.10 Static Drain - Source On - State Resistance vs. Gate Source Voltage

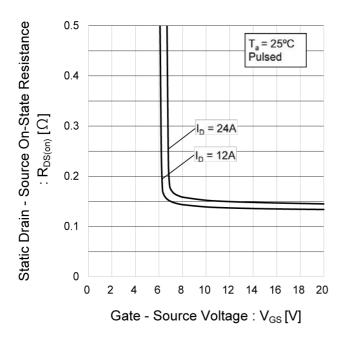


Fig.11 Static Drain - Source On - State Resistance vs. Junction Temperature

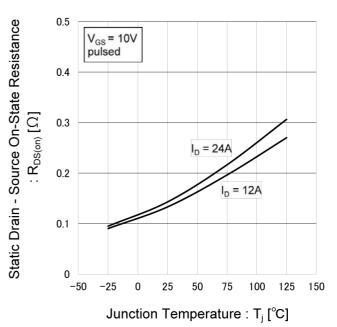
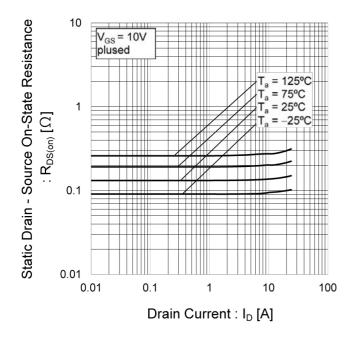


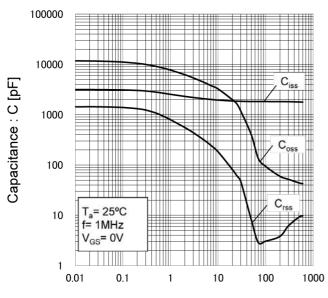
Fig.12 Static Drain - Source On - State Resistance vs. Drain Current(I)



8/12

Fig.13 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage : V_{DS} [V]

Fig.14 Switching Characteristics

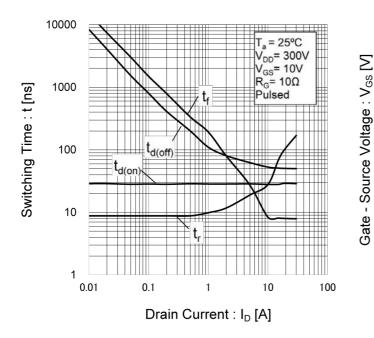


Fig.15 Dynamic Input Characteristics

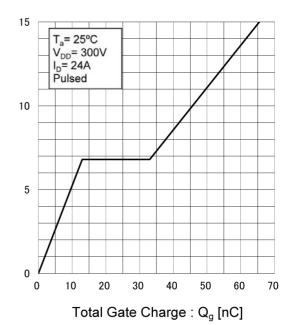
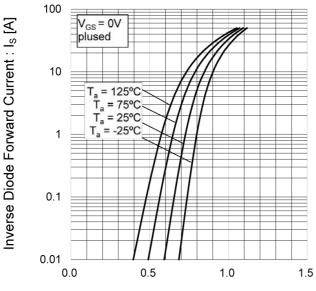
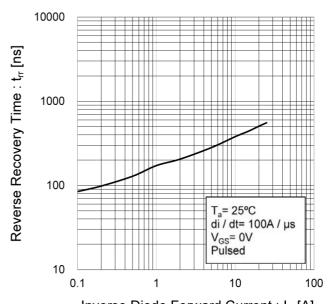


Fig.16 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.17 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

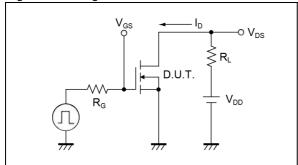


Fig.2-1 Gate Charge Measurement Circuit

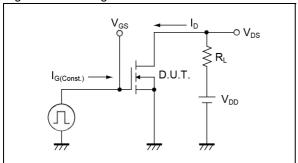


Fig.3-1 Avalanche Measurement Circuit

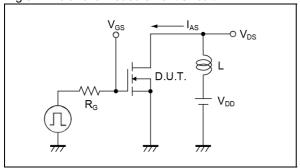


Fig.4-1 dv/dt Measurement Circuit

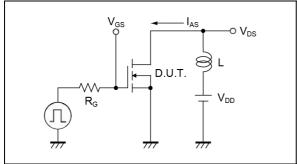


Fig.5-1 di/dt Measurement Circuit

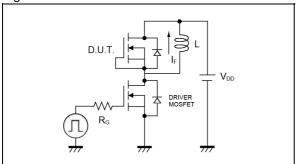


Fig.1-2 Switching Waveforms

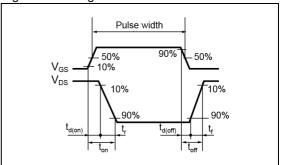


Fig.2-2 Gate Charge Waveform

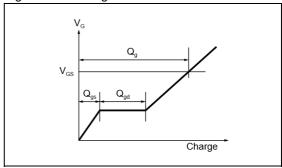


Fig.3-2 Avalanche Waveform

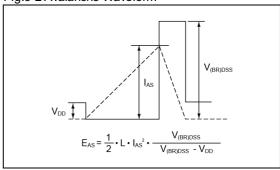


Fig.4-2 dv/dt Waveform

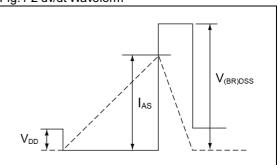
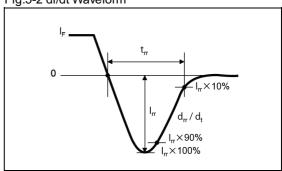
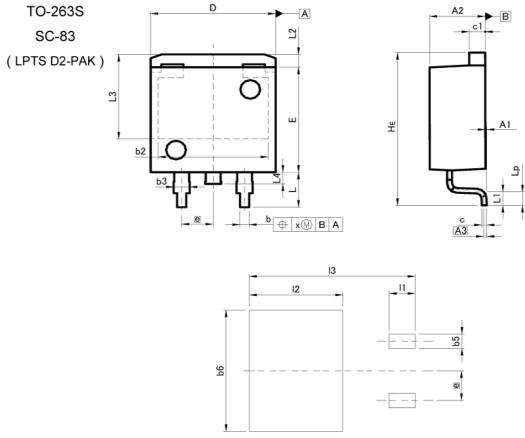


Fig.5-2 di/dt Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.:		0.0	
b	0.68	0.98	0.027	0.039
b2		90	0.3	
ь3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.54		0.1	00
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	1.	20	0.047	
L2	1.	10	0.043	
L3	7.25		0.285	
L4	1.00		0.039	
Lp	0.90	1.50	0.035	0.059
Х	70	0.25	-	0.010
	MIL IMI		INC	1150

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
bb	H(1.23	3-6	0.049	
b6	=(10.40		0.409	
11	23	2.10	, 12	0.083	
12		7.55	100	0.297	
13		13.40	i—	0.528	

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
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CLASSIV		CLASSⅢ	

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 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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