

R6025FNZ1

Nch 600V 25A Power MOSFET

V_{DSS}	600V
R _{DS(on)} (Max.)	0.18Ω
I _D	25A
P_D	446W

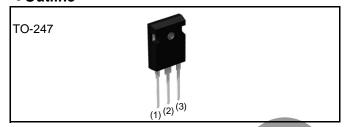
Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Gate-source voltage (V_{GSS}) guaranteed to be $\pm 30V$.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.
- 6) Pb-free lead plating; RoHS compliant

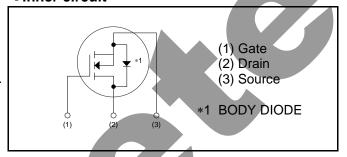
Application

Switching Power Supply

Outline



●Inner circuit



or ackaging specifications						
	Packaging	Tube				
	Reel size (mm)	-				
Type	Tape width (mm)	-				
Type	Basic ordering unit (pcs)	450				
	Taping code	C9				
	Marking	R6025FNZ1				

● Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	600	V
Continuous drain current T _c = 25°C	l _D *1	±25	А
$T_c = 100^{\circ}C$	I _D *1	±12	А
Pulsed drain current	I _{D,pulse} *2	±100	А
Gate - Source voltage	V_{GSS}	±30	V
Avalanche energy, single pulse	E _{AS} *3	42.1	mJ
Avalanche energy, repetitive	E _{AR} *4	9.7	mJ
Avalanche current	I _{AR} *3	12.5	А
Power dissipation (T _c = 25°C)	P_{D}	446	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C
Reverse diode dv/dt	dv/dt *5	15	V/ns

Absolute maximum ratings

Parameter	Symbol	Conditions	Values	Unit
Drain - Source voltage slope	dv/dt	$V_{DS} = 480V, I_{D} = 25A$ $T_{j} = 125^{\circ}C$	50	V/ns

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farameter	Symbol	Min.	Тур.	Max.	Offit
Thermal resistance, junction - case	R_{thJC}	-	-	0.28	°C/W
Thermal resistance, junction - ambient	R_{thJA}	-	-	30	°C/W
Soldering temperature, wavesoldering for 10s	T_{sold}		1	265	°C

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol Conditions		Values			Unit
raiailletei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	600	-	1	V
Drain - Source avalanche breakdown voltage	V _{(BR)DS}	$V_{GS} = 0V, I_D = 12.5A$	-	700	1	V
		$V_{DS} = 600 \text{V}, V_{GS} = 0 \text{V}$				μΑ
Zero gate voltage drain current	I _{DSS}	T _j = 25°C	-	0.1	100	μΑ
		T _j = 125°C	-	-	10	mA
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V$, $I_D = 1mA$	3	-	5	V
		$V_{GS} = 10V, I_D = 12.5A$				
Static drain - source on - state resistance	R _{DS(on)} *6	T _j = 25°C	-	0.14	0.18	Ω
		T _j = 125°C	-	0.28	-	
Gate input resistance	R_{G}	f = 1MHz, open drain	-	3.3	-	Ω

•Electrical characteristics($T_a = 25$ °C)

Parameter	Cumbal	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Transconductance	g _{fs} *6	$V_{DS} = 10V, I_D = 12.5A$	9	18	-	S
Input capacitance	C _{iss}	V _{GS} = 0V	-	3500	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	2200	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	45	-	
Effective output capacitance, energy related	C _{o(er)}	V _{GS} = 0V	-	111		
Effective output capacitance, time related	$C_{o(tr)}$	V _{DS} = 0V to 480V		364	15	pF
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 300 V$, $V_{GS} = 10 V$		57	-	
Rise time	t _r *6	I _D = 12.5A	Y-	115	-	20
Turn - off delay time	t _{d(off)} *6	$R_L = 24\Omega$	(-)	150	300	ns
Fall time	t _f *6	$R_G = 10\Omega$	-	72	144	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol Conditions		Values			Unit
r arameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Qg *6	V _{DD} ≈ 300V	-	85	ı	
Gate - Source charge	Q _{gs} *6	I _D = 25A	-	25	-	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	35	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 300V$, $I_D = 25A$	-	7.1	-	V

^{*1} Limited only by maximum temperature allowed.

^{*2} $P_W \le 10 \mu s$, Duty cycle $\le 1\%$

^{*3} L \simeq 500 μ H, V_{DD} = 50V, R_G = 25 Ω , starting T_j = 25°C

^{*4} L $^{\sim}$ 500 μ H, V_{DD} = 50V, R_G = 25 Ω , starting T_j = 25°C, f = 10kHz

^{*5} Reference measurement circuits Fig.5-1.

^{*6} Pulsed

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai ii etei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _c = 25°C	1	-	25	А
Inverse diode direct current, pulsed	I _{SM} *2	1 _c = 23 0			100	A
Forward voltage	V _{SD} *6	$V_{GS} = 0V, I_{S} = 25A$	-	-	1.5	V
Reverse recovery time	t _{rr} *6		-	120	-	ns
Reverse recovery charge	Q _{rr} *6	$I_S = 25A$ di/dt = 100A/µs	-	0.53	\.	μС
Peak reverse recovery current	I _{rrm} *6			9	-	А
Peak rate of fall of reverse recovery current	di _{rr} /dt	T _j = 25°C		1150		A/μs

● Typical Transient Thermal Characteristics

	Symbol	Value	Unit	
li.	R _{th1}	0.0833		
•	R _{th2}	0.171	K/W	
	R _{th3}	0.579		

Symbol	Value	Unit
C _{th1}	0.0182	
C _{th2}	0.0944	Ws/K
C _{th3}	0.51	

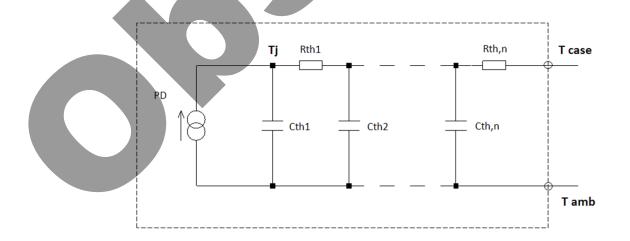
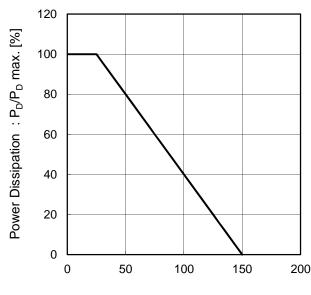
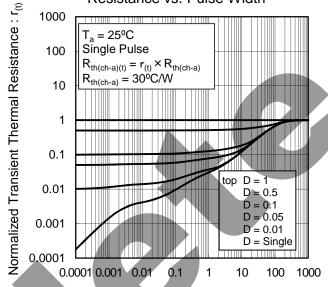


Fig.1 Power Dissipation Derating Curve



Junction Temperature : T_i [°C]

Fig.2 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: P_W [s]



Fig.3 Avalanche Current vs Inductive Load

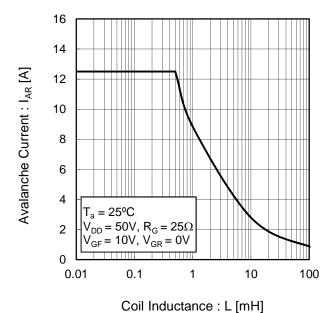


Fig.4 Avalanche Power Losses

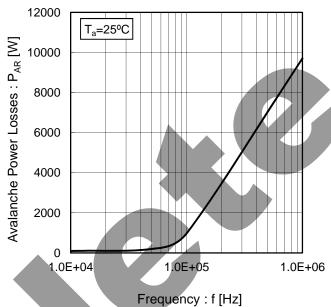
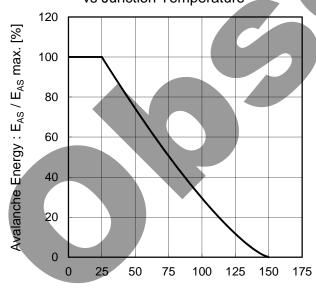
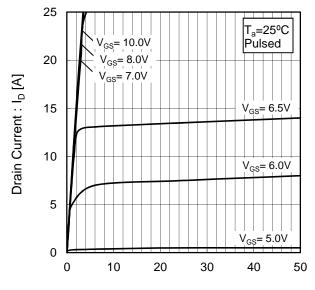


Fig.5 Avalanche Energy Derating Curve vs Junction Temperature



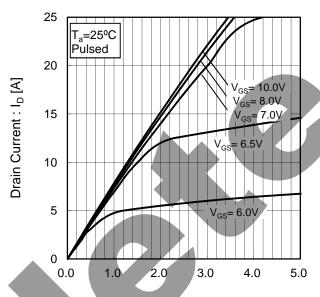
Junction Temperature : T_i [°C]

Fig.6 Typical Output Characteristics(I)



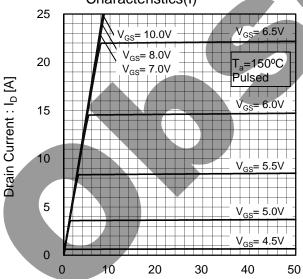
Drain - Source Voltage : V_{DS} [V]

Fig.7 Typical Output Characteristics(II)



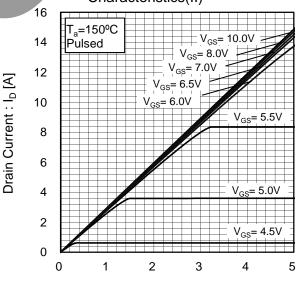
Drain - Source Voltage : V_{DS} [V]

Fig.8 T_j = 150°C Typical Output
Characteristics(I)

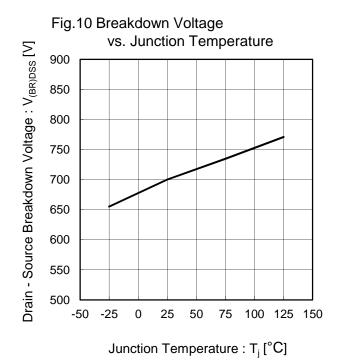


Drain - Source Voltage : V_{DS} [V]

Fig.9 T_j = 150°C Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]



100 V_{DS}= 10V Plused 10 T_a=125°C T_a=75°C T_a=25°C T_a=25°C T_a=25°C

Gate - Source Voltage : V_{GS} [V]

Fig.11 Typical Transfer Characteristics

Fig.12 Gate Threshold Voltage vs. Junction Temperature 8 V_{DS}= 10V Gate Threshold Voltage: V_{GS(th)} [V] 7 $_{D}=1mA$ Plused 6 5 4 3 2 0 -25 25 50 75 100 125 150 -50 Junction Temperature : T_i [°C]

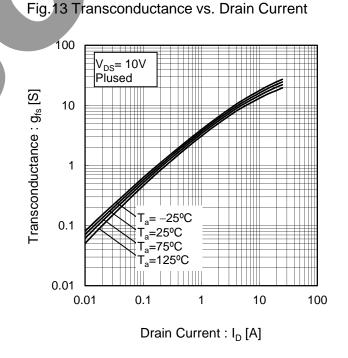
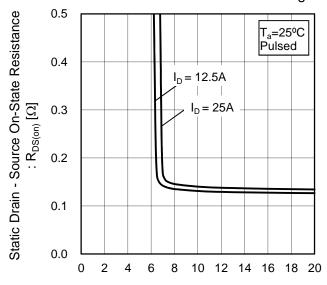
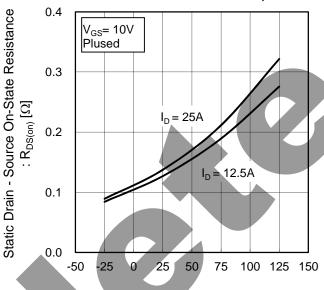


Fig.14 Static Drain - Source On - State Resistance vs. Gate Source Voltage



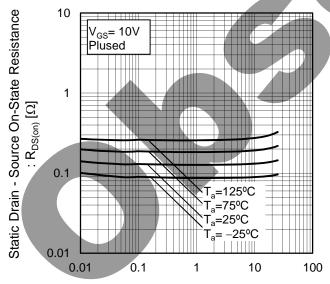
Gate - Source Voltage : V_{GS} [V]

Fig.15 Static Drain - Source On - State Resistance vs. Junction Temperature



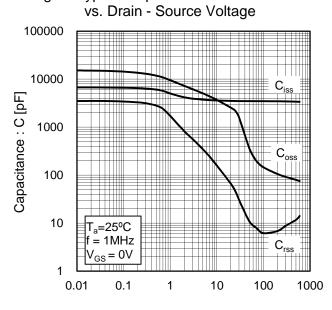
Junction Temperature : T_i [°C]

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current



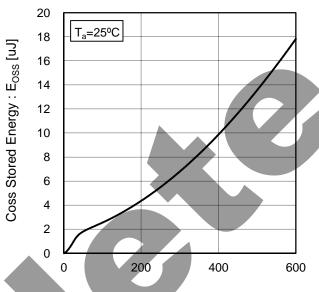
Drain Current : I_D [A]

Fig.17 Typical Capacitance



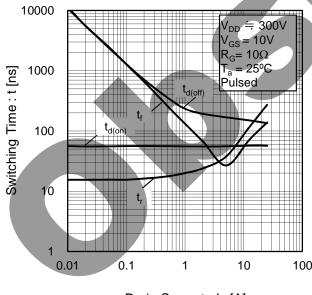
Drain - Source Voltage : V_{DS} [V]

Fig.18 Coss Stored Energy



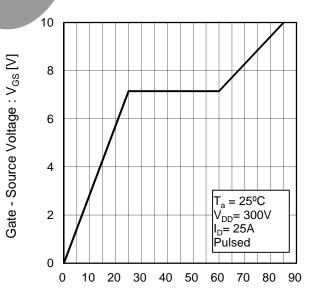
Drain - Source Voltage : V_{DS} [V]

Fig.19 Switching Characteristics



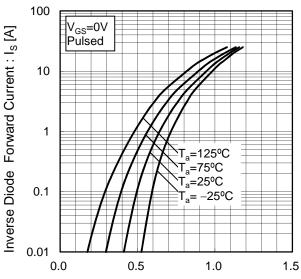
Drain Current : I_D [A]

Fig.20 Dynamic Input Characteristics



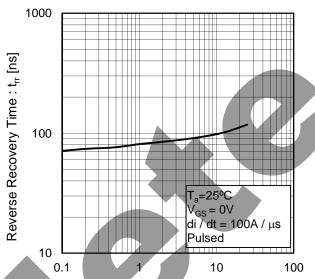
Total Gate Charge : Q_q [nC]

Fig.21 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.22 Reverse Recovery Time vs.Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

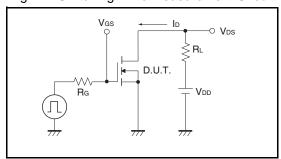


Fig.2-1 Gate Charge Measurement Circuit

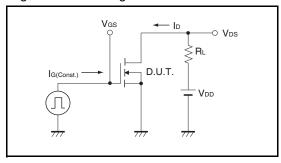


Fig.3-1 Avalanche Measurement Circuit

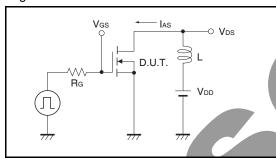


Fig.4-1 dv/dt Measurement Circuit

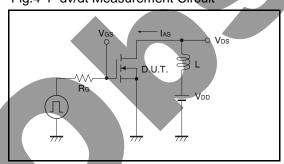


Fig.5-1 di/dt Measurement Circuit

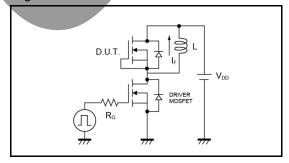


Fig.1-2 Switching Waveforms

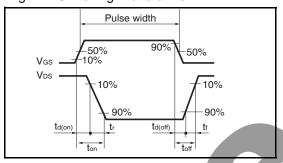


Fig.2-2 Gate Charge Waveform

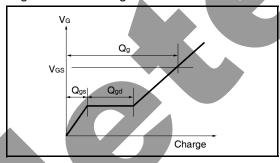


Fig.3-2 Avalanche Waveform

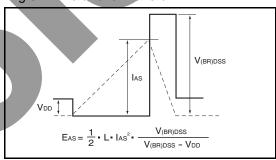


Fig.4-2 dv/dt Waveform

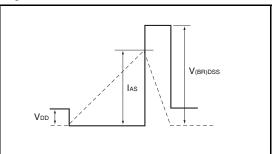
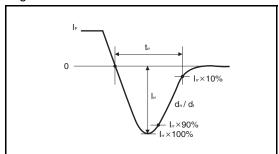
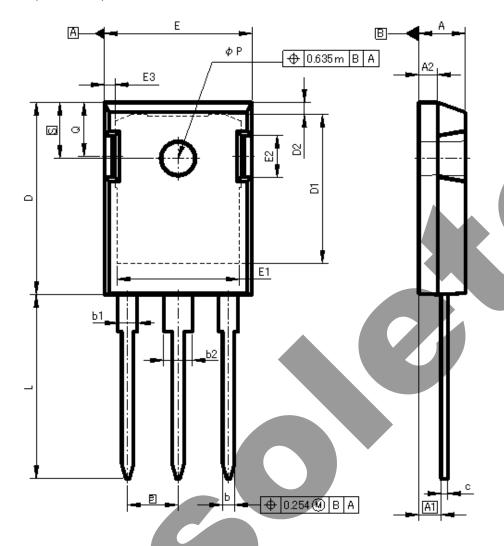


Fig.5-2 di/dt Waveform



●Dimensions (Unit : mm)

TO-247



DIM	MILIM	ETERS	INCHES		
DIIVI	MIN	MAX	MIN	MAX	
A	4.83	5.21	0.190	0.205	
A1	2.29	2.54	0.090	0.100	
A2	1.91	2.16	0.075	0.085	
b	1.14	1.40	0.045	0.055	
b1	1.91	2.20	0.075	0.087	
b2	2.92	3.20	0.115	0.126	
C	0.61	0.80	0.024	0.031	
D	20.80	21.34	0.819	0.840	
D1	17.43	17.83	0.686	0.702	
E	15.75	16.13	0.620	0.635	
е	5.4	45	0.2	15	
N	3.0	00	3.0	00	
L	19.81	20.57	0.780	0.810	
L1	3.81	4.32	0.150	0.170	
ФР	3.55	3.65	0.140	0.144	
Q	5.59	6.20	0.220	0.244	
S	6.	15	0.2	40	

Dimension in mm / inches

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JAPAN	USA	EU	CHINA
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