## Nch 600V 25A Power MOSFET

V <sub>DSS</sub>	600V
R <sub>DS(on)</sub> (Max.)	0.182Ω
I <sub>D</sub>	±25A
P <sub>D</sub>	306W

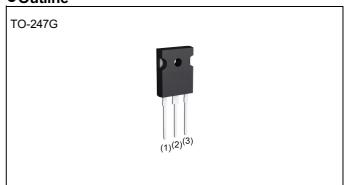
## Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant

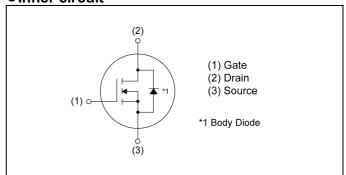
## Application

Switching

## Outline



## •Inner circuit



Packaging specifications

- i dokagiiig opoomodiioiio					
Packing	Tube				
Packing code	C13				
Marking	R6025JNZ4				
Basic ordering unit (pcs)	600				

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{DSS}$	600	V
Continuous drain current (T <sub>c</sub> = 25°C)	I <sub>D</sub> *1	±25	А
Pulsed drain current	I <sub>DP</sub> *2	±75	А
Gate - Source voltage	V <sub>GSS</sub>	±30	V
Avalanche current, single pulse	I <sub>AS</sub> *3	5.0	А
Avalanche energy, single pulse	E <sub>AS</sub> *3	682	mJ
Power dissipation (T <sub>c</sub> = 25°C)	P <sub>D</sub>	306	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Dougnoston	0 1 1	Values			11.36
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	0.41	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	30	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cymah al	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	600	-	-	V	
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 600V, V_{GS} = 0V$ $T_j = 25^{\circ}C$	-	-	100	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 30V$ , $V_{DS} = 0V$	-	-	±100	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 4.5$ mA	5.0	6.0	7.0	V	
Static drain - source on - state resistance	R <sub>DS(on)</sub> *5	$V_{GS} = 15V, I_D = 12.5A$ $T_j = 25^{\circ}C$	-	0.140	0.182	Ω	
Gate resistance	$R_{G}$	f = 1MHz, open drain	-	2.0	-	Ω	

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Doromotor	Cymah al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1900	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 100V	-	115	1	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	2.0	1	_
Effective output capacitance energy related	C <sub>o(er)</sub> *6	V <sub>GS</sub> = 0V	-	85	1	pF
Effective output capacitance time related	C <sub>o(tr)</sub> *7	V <sub>DS</sub> = 0V to 480V	-	340	1	
Turn - on delay time	t <sub>d(on)</sub> *5	V <sub>DD</sub> ≈ 300V, V <sub>GS</sub> = 15V	-	33	1	
Rise time	<b>t</b> <sub>r</sub> *5	I <sub>D</sub> = 12.5A	1	24	1	no
Turn - off delay time	t <sub>d(off)</sub> *5	$R_L \simeq 24.3\Omega$	-	60	1	ns
Fall time	<b>t</b> <sub>f</sub> *5	$R_G = 10\Omega$	-	18	-	

## ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Darameter	Cymaela al	Conditions	Values			l le:4
Parameter	Parameter Symbol Conditions		Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≈ 300V	-	57	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 25A	-	18.5	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 15V	-	20.5	-	
Gate plateau voltage	V <sub>(plateau)</sub>	V <sub>DD</sub> ≈ 300V, I <sub>D</sub> = 25A	-	9.8	-	V

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

<sup>\*3</sup> L  $\simeq$  50mH, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 $\Omega$ , starting T<sub>i</sub> = 25°C

<sup>\*4</sup> Tc=25°C

<sup>\*5</sup> Pulsed

<sup>\*6</sup> Co(er) is a fixed capacitance that gives the same stored energy as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

<sup>\*7</sup> Co(tr) is a fixed capacitance that gives the same charging time as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .

## ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Source current	I <sub>S</sub> *1	T <sub>C</sub> = 25°C	1	-	25	Α	
Pulsed source current	I <sub>SP</sub> *2	1C - 23 C	1	-	75	Α	
Source-Drain voltage	V <sub>SD</sub> *5	$V_{GS} = 0V, I_{S} = 25A$	-	-	1.7	V	
Reverse recovery time	<b>t</b> <sub>rr</sub> *5		-	90	-	ns	
Reverse recovery charge	Q <sub>rr</sub> *5	I <sub>S</sub> = 25A di/dt = 100A/µs	-	350	-	nC	
Peak reverse recovery current	I <sub>rr</sub> *5		-	8.0	-	Α	

Fig.1 Power Dissipation Derating Curve

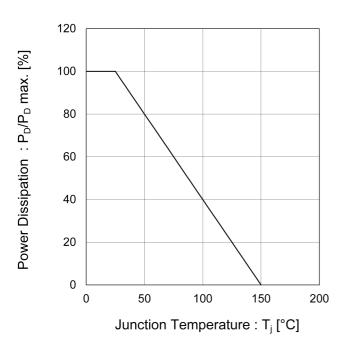


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

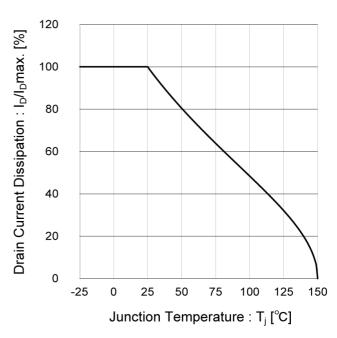


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

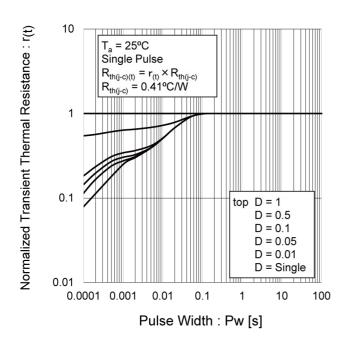


Fig.4 Maximum Safe Operating Area

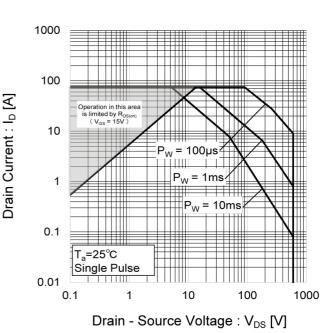


Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

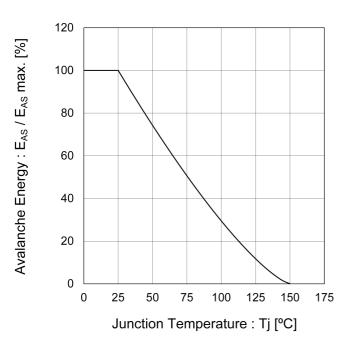


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

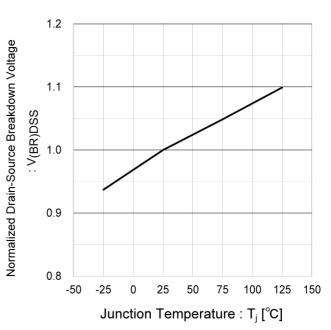


Fig.7 Typical Output Characteristics(I)

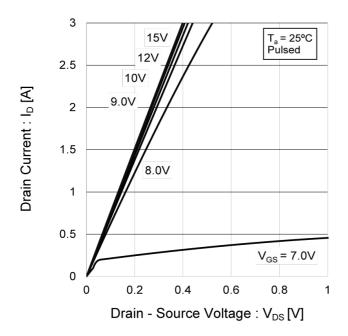
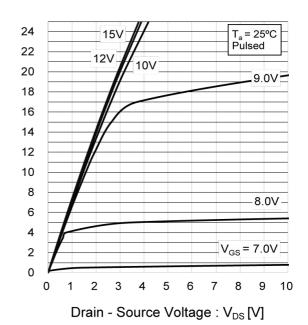


Fig.8 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Fig.9 Typical Transfer Characteristics

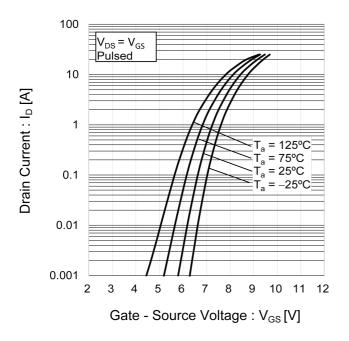


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

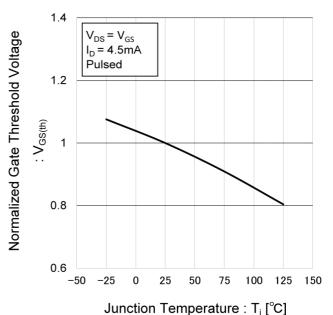


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

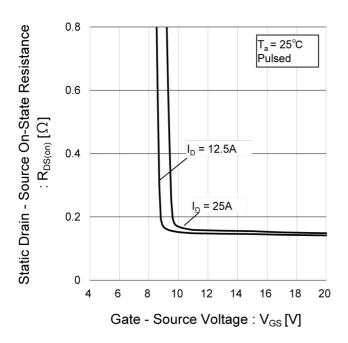


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

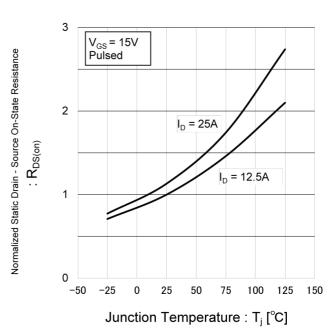


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

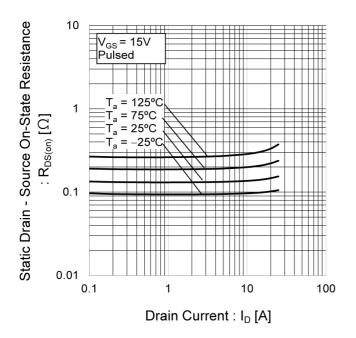


Fig.14 Typical Capacitance vs.
Drain - Source Voltage

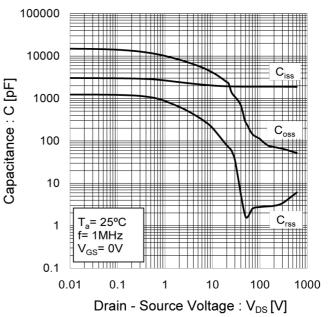


Fig.15 Typical Coss Stored Energy

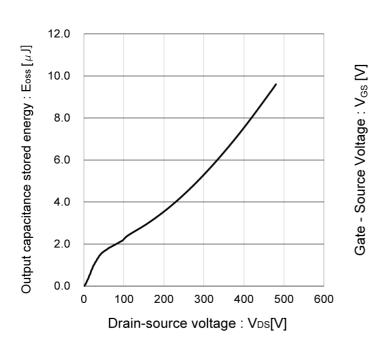


Fig.16 Dynamic Input Characteristics

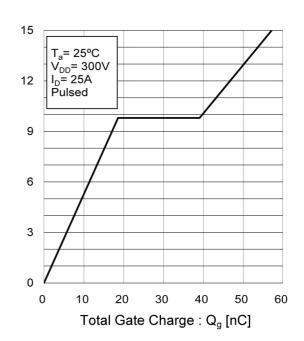
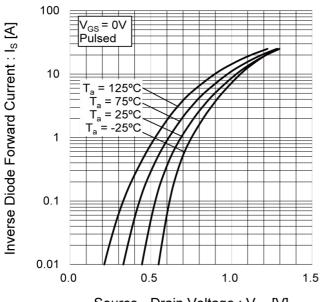
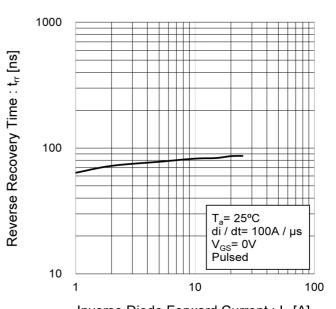


Fig.17 Inverse Diode Forward Current vs. Source - Drain Voltage



Source - Drain Voltage : V<sub>SD</sub> [V]

Fig.18 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current :  $I_S$  [A]

### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

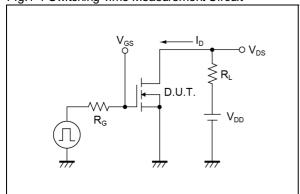


Fig.2-1 Gate Charge Measurement Circuit

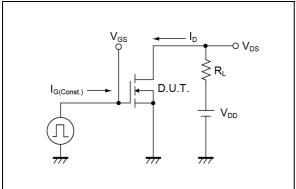


Fig.3-1 Avalanche Measurement Circuit

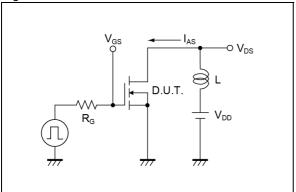


Fig.4-1 Diode Characteristice Measurement Circuit

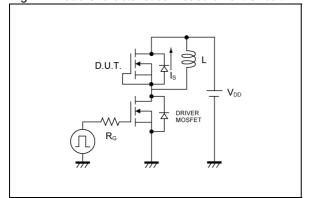


Fig.1-2 Switching Waveforms

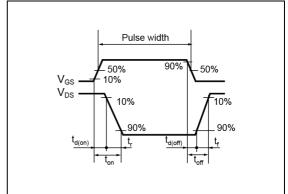


Fig.2-2 Gate Charge Waveform

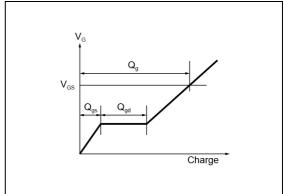


Fig.3-2 Avalanche Waveform

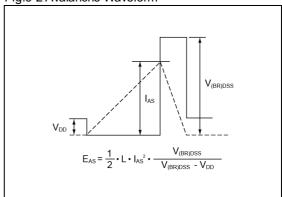
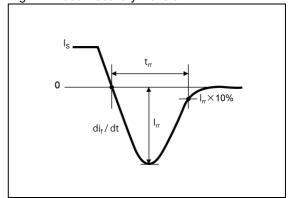
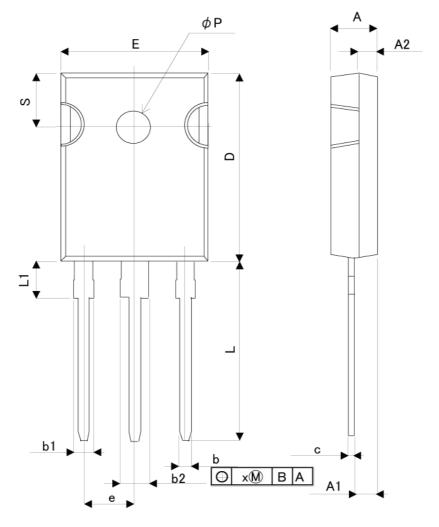


Fig.4-2 Diode Recovery Waveform



## Dimensions

TO-247



DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	4.82	5.22	0.190	0.206
A1	2.11	2.71	0.083	0.107
A2	1.80	2.20	0.071	0.087
b	1.00	1.40	0.039	0.055
b1	1.80	2.20	0.071	0.087
b2	2.80	3.20	0.110	0.126
С	0.45	0.75	0.018	0.030
D	20.65	21.25	0.813	0.837
E	15.64	16.24	0.616	0.639
е	5.44		0.2	14
L	19.77	20.37	0.778	0.802
L1	4.09	4.29	0.161	0.169
Р	3.51	3.71	0.138	0.146
S	5.97	6.37	0.235	0.251

Dimension in mm/inches

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JAPAN	USA	EU	CHINA
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