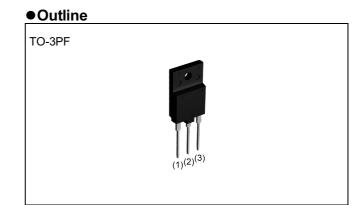
R6077VNZ

Nch 600V 42mohm(typ.) Power MOSFET

V <sub>DSS</sub> (@Tj max.) <sup>*5</sup>	650V
R <sub>DS(on)</sub> (Max.)	0.051Ω
I <sub>DP</sub> *2	±231A
P <sub>D</sub>	113W



# Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating ; RoHS compliant
- 6) Halogen free mold compound

# (2) (1) Gate (2) Drain (3) Source

(3)

Inner circuit

# Application

Switching applications

Marking

R6077VNZ

\*1 Body Diode

## • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	600	V
Continuous drain current ( $T_c = 25^{\circ}C$ )	ا <sub>D</sub> *1	±29	A
Pulsed drain current	<sup>*2</sup>	±231	А
Gate - Source voltage	V <sub>GSS</sub>	±30	V
Avalanche current, single pulse	I <sub>AS</sub> *3	5.5	А
Avalanche energy, single pulse	E <sub>AS</sub> *3	326	mJ
MOSFET dv/dt	dv/dt*4	120	V/ns
Power dissipation $(T_c = 25^{\circ}C)$	P <sub>D</sub>	113	W
Junction temperature	Tj	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## •Thermal resistance

Deremeter	Cumph of	Values			L lucit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub>	-	-	1.09	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub>	-	-	-	°C/W
Soldering temperature, wavesoldering for 10s	T <sub>sold</sub>	-	-	265	°C

# •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Sumpleal	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		600	-	-	V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V	-	-	100	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±30V, $V_{DS}$ = 0V	-	-	±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 1.9 \text{mA}$	4.5	5.5	6.5	V
Static drain - source	D *5	V <sub>GS</sub> = 15V, I <sub>D</sub> = 23A	-	0.042	0.051	Ω
on - state resistance	${R_{DS(on)}}^{*5}$	V <sub>GS</sub> = 10V, I <sub>D</sub> = 23A	-	0.046	0.056	Ω
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain	-	1.3	-	Ω



# •Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Cymab ol	Conditions	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 100V	-	5200	-	
Output capacitance	C <sub>oss</sub>	f = 100kHz	-	165	-	
Effective output capacitance energy related	C <sub>o(er)</sub> *6	V <sub>GS</sub> = 0V	-	150	-	pF
Effective output capacitance time related	C <sub>o(tr)</sub> *7	$V_{DS} = 0V$ to 480V	-	1030	-	
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 300$ V, $V_{GS}$ = 15V	-	55	-	
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 23A	-	62	-	
Turn - off delay time	$t_{d(off)}$ *5	$R_L \simeq 13\Omega$	-	148	-	ns
Fall time	t <sub>f</sub> *5	R <sub>G</sub> = 10Ω	-	53	-	

# • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Cumph of	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*5}$	$V_{DD} \simeq 300V$	-	108	-	
Gate - Source charge	Q <sub>gs</sub> *5	I <sub>D</sub> = 23A	-	43	-	nC
Gate - Drain charge	Q <sub>gd</sub> *5	V <sub>GS</sub> = 10V	-	50	-	
Gate plateau voltage	V <sub>(plateau)</sub>	$V_{DD} \simeq 300$ V, I <sub>D</sub> = 23A	-	8.5	-	V



Body diode electrical characteristics	(Source-Drain) (T <sub>a</sub> = 25°C)
---------------------------------------	--

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Source current	۱ <sub>S</sub> *1	T - 25°C	-	-	29	А
Pulsed source current	$I_{SP}^{*2}$	T <sub>C</sub> = 25°C	-	-	231	А
Source-Drain voltage	$V_{SD}^{*5}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = 23A	-	-	1.5	V
Reverse recovery time	t <sub>rr</sub> *5	V <sub>DD</sub>	-	125	-	ns
Reverse recovery charge	Q <sub>rr</sub> *5	I <sub>S</sub> = 23A	-	705	-	nC
Peak reverse recovery current	۱ <sub>m</sub> *5	di/dt = 100A/µs	-	11	-	А

\*1 Limited only by maximum temperature allowed.

\*2 Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

\*3 L  $\simeq$  20mH, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25 $\Omega$ , starting T<sub>i</sub> = 25°C

- \*4  $V_{DS}$  = 0 to 400V
- \*5 Pulsed
- \*6 Co(er) is a fixed capacitance that gives the same stored energy as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .
- \*7 Co(tr) is a fixed capacitance that gives the same charging time as Coss while  $V_{DS}$  is rising from 0 to 80%  $V_{DSS}$ .



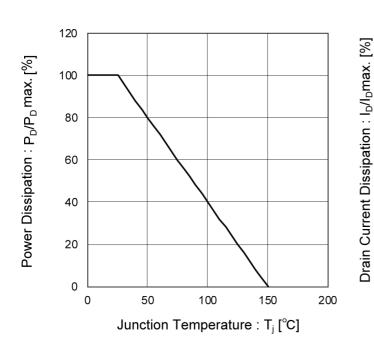


Fig.1 Power Dissipation Derating Curve

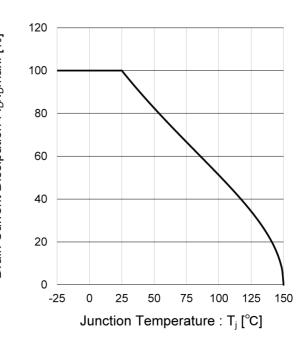


Fig.2 Drain Current Derating Curve

Fig.3 Normalized Transient Thermal
Resistance vs. Pulse Width

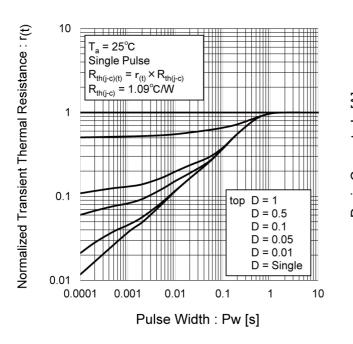
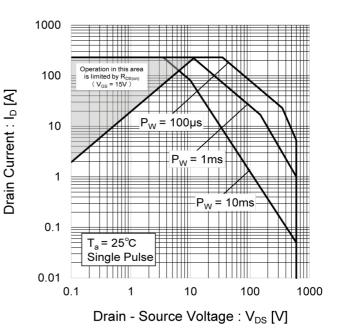


Fig.4 Maximum Safe Operating Area



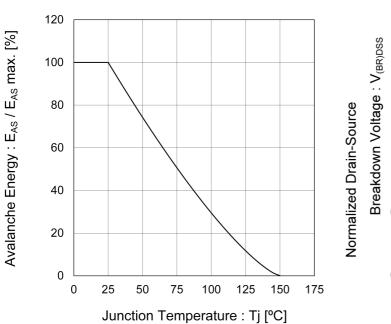


Fig.5 Avalanche Energy Derating Curve

# Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

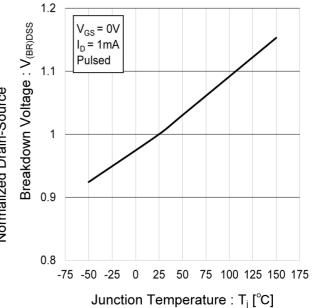
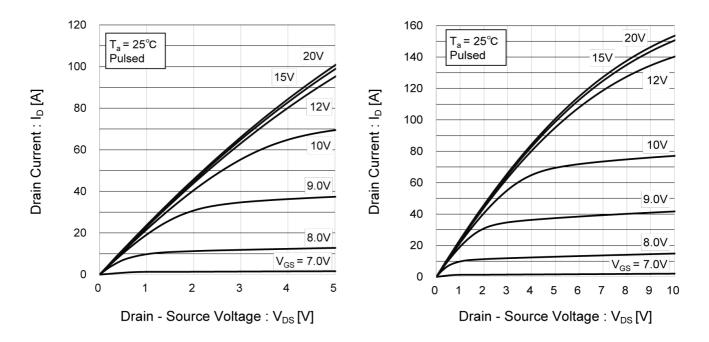
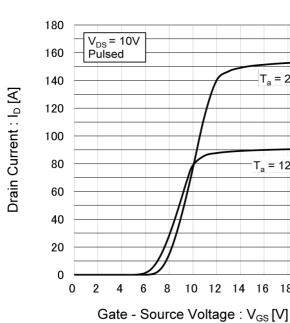


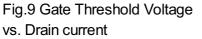
Fig.7 Output Characteristics(I)











 $T_a = 25^{\circ}C_a$ 

T<sub>a</sub> = 125°C

18 20 Fig.10 Normalized Gate Threshold Voltage vs Junction Temperature

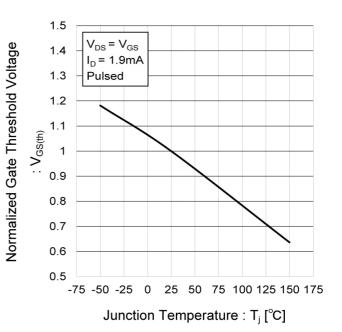
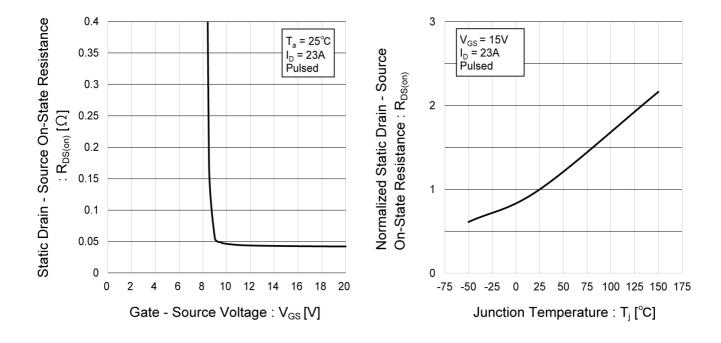


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature



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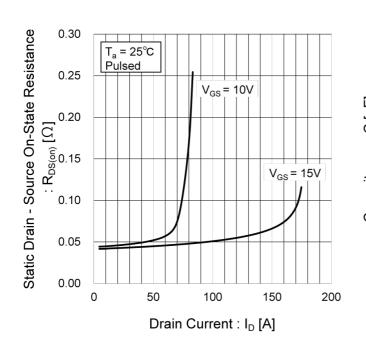


Fig.13 Static Drain - Source On - State

Resistance vs. Drain Current

Fig.14 Capacitances

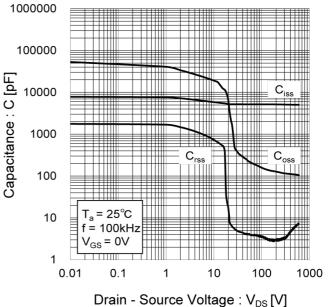
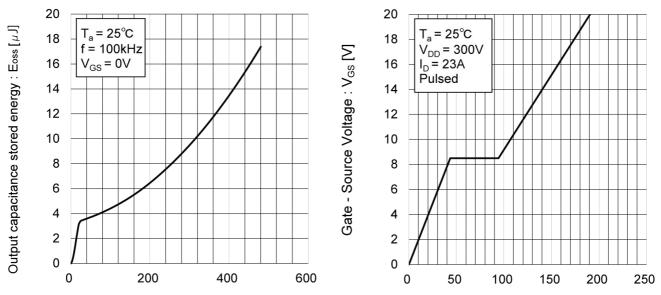


Fig.15 Coss Stored Energy

Fig.16 Gate Charge



Total Gate Charge : Qg [nC]

Drain-source voltage : VDs[V]

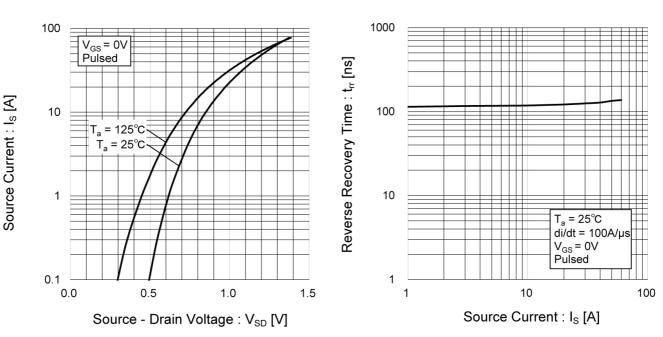


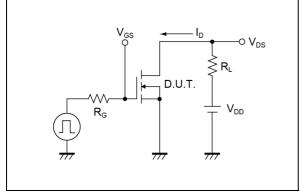
Fig.17 Source Current vs. Source - Drain Voltage

# Fig.18 Reverse Recovery Time vs. Source Current

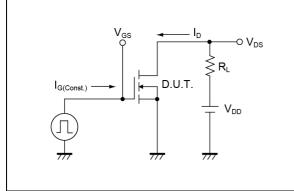


## Measurement circuits

#### Fig.1-1 Switching Time Measurement Circuit



#### Fig.2-1 Gate Charge Measurement Circuit



## Fig.3-1 Avalanche Measurement Circuit

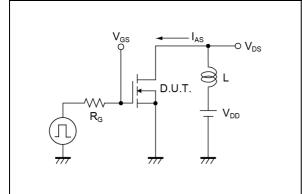
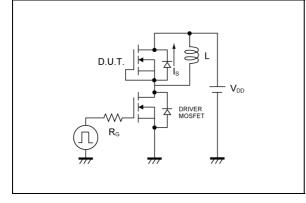
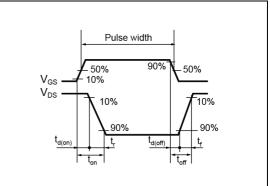


Fig.4-1 Diode Recovery Measurement Circuit

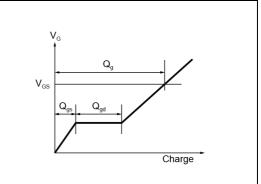


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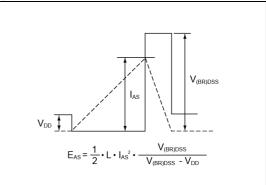
## Fig.1-2 Switching Waveforms



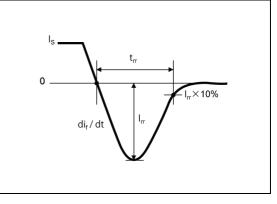
#### Fig.2-2 Gate Charge Waveform



## Fig.3-2 Avalanche Waveform

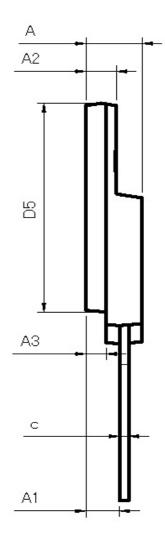


## Fig.4-2 Diode Recovery Waveform





# Dimensions TO-3PF $\phi$ P Е T 0 O S 0 0 $\Box$ 22 8 E2 δ Φ 4 b1 b Ð е e1



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DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A	5.30	5.70	0.209	0.224
A1	3.10	3.50	0.122	0.138
A2	2.80	3.20	0.11	0.126
A3	1.80	2.20	0.071	0.087
b	0.65	0.95	0.026	0.037
b1	1.80	2.20	0.071	0.087
C	0.80	1.10	0.031	0.043
D	26.30	26.70	1.035	1.051
D1	43.60	44.00	1.717	1.732
D2	24.30	24.70	0.957	0.972
D3	1.80	2.20	0.071	0.087
D4	9.80	10.20	0.386	0.402
D5	22.80	23.20	0.898	0.913
E	15.30	15.70	0.602	0.618
e	5.15	5.75	0.203	0.226
e1	10.60	11.20	0.417	0.441
N		3		3
L	14.60	15.00	0.575	0.591
φP	3.40	3.80	0.134	0.15
S	4.30	4.70	0.169	0.185

11/11

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
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CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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