Nch 650V 4A Power MOSFET

V _{DSS}	650V
R _{DS(on)} (Max.)	1.050Ω
I _D	±4.0A
P_D	58W

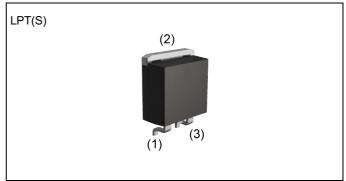
Features

- 1) Low on-resistance
- 2) Ultra fast switching speed
- 3) Parallel use is easy
- 4) Pb-free plating; RoHS compliant

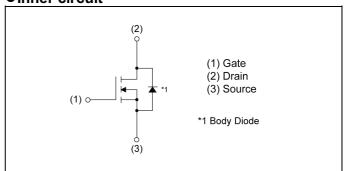
Application

Switching

Outline



•Inner circuit



Packaging specifications

Packing	Embossed Tape
Packing code	TL
Marking	R6504KNJ
Basic ordering unit (pcs)	1000

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V _{DSS}	650	V	
Continuous drain current (T _c = 25	I _D *1	±4.0	А	
Pulsed drain current	I _{DP} *2	±12	А	
0.1.0	Static		±20	V
Gate - Source voltage	AC (f>1Hz)	V_{GSS}	±30	V
Avalanche current, single pulse	·	I _{AS}	0.8	Α
Avalanche energy, single pulse		E _{AS} *3	34.8	mJ
Power dissipation (T _c = 25°C)	P _D	58	W	
Junction temperature	T _j	150	°C	
Operating junction and storage to	emperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Davamatav	Cymah al	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *4	-	-	2.2	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

● Electrical characteristics (T_a = 25°C)

Parameter	Cumb al	Conditions			Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	650	-	-	V	
		$V_{DS} = 650V, V_{GS} = 0V$					
Zero gate voltage drain current	I _{DSS}	$T_j = 25^{\circ}C$	-	-	100	μΑ	
		$T_j = 125^{\circ}C$	-	-	1000		
Gate - Source leakage current	I _{GSS}	V_{GS} = ±20V, V_{DS} = 0V	1	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 130 \mu A$	3	-	5	V	
		V _{GS} = 10V, I _D = 1.5A					
Static drain - source on - state resistance	R _{DS(on)} *6	$T_j = 25^{\circ}C$	-	0.955	1.050	Ω	
		$T_j = 125^{\circ}C$	-	_	-		
Gate resistance	R_{G}	f = 1MHz, open drain	-	3.3	-	Ω	

● Electrical characteristics (T_a = 25°C)

Davamatar	Cymah al	Conditions		Values			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	270	-		
Output capacitance	C _{oss}	V _{DS} = 25V	-	270	-	pF	
Reverse transfer capacitance C _{rss}		f = 1MHz	-	15	-		
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 300V$, $V_{GS} = 10V$	-	16	-		
Rise time	t _r *6	I _D = 2A	-	17	-		
Turn - off delay time ${\mathsf t_{\mathsf d(off)}}^{\star_6}$		R _L ≃ 150Ω	-	30	-	ns	
Fall time	t _f *6	$R_G = 10\Omega$	-	35	-		

● Gate charge characteristics (T_a = 25°C)

Darameter	Symbol Conditions -		Values			l limit
Parameter			Min.	Тур.	Max.	Unit
Total gate charge	Q _g *6	V _{DD} ≃ 300V	-	10	-	
Gate - Source charge	Q _{gs} *6	I _D = 4A	-	2.5	-	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	4.8	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 300V$, $I_D = 4A$	-	6.5	-	V

^{*1} Limited only by maximum channel temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \doteqdot 100mH, V_{DD}=50V, R_G=25 Ω , STARTING T_i=25 $^{\circ}$ C

^{*4} T_C=25°C

^{*5} Mounted on an epoxy PCB FR4 (25mm x 27mm x 0.8mm)

^{*6} Pulsed

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	UTIIL	
Source current	I _S *1	T 05°0	1	-	4.0	Α	
Pulsed source current	sed source current I_{SP}^{*2} $T_C = 25$		1	-	12	Α	
Source-Drain voltage	V _{SD} *6	$V_{GS} = 0V$, $I_S = 4A$	-	-	1.5	V	
Reverse recovery time	t _{rr} *6		-	290	-	ns	
Reverse recovery charge	Q _{rr} *6	I _S = 4A di/dt = 100A/μs	-	1.9	-	μC	
Peak reverse recovery current	_{rr} *6		-	13	-	Α	

Fig.1 Power Dissipation Derating Curve

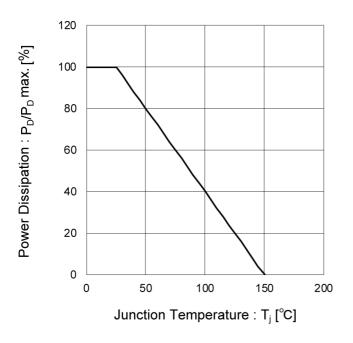


Fig.2 Drain Current Derating Curve

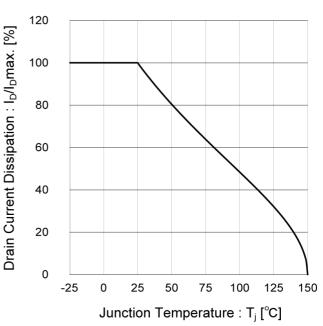


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

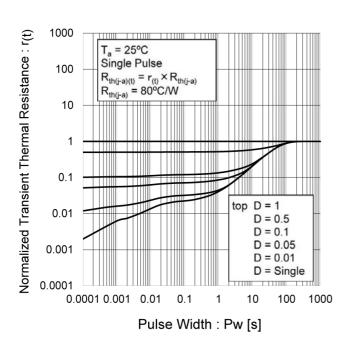


Fig.4 Maximum Safe Operating Area

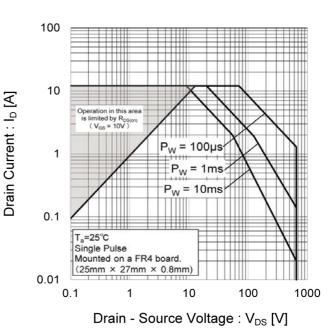


Fig.5 Avalanche Energy Derating Curve

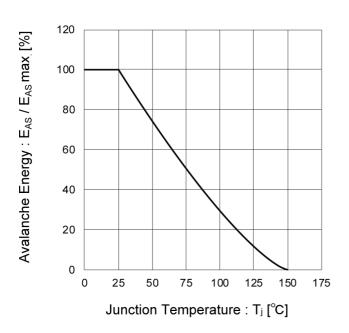


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

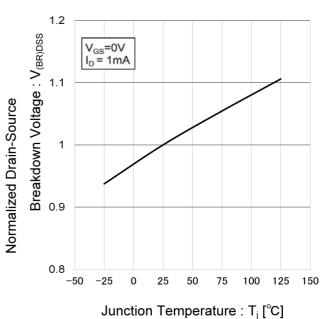


Fig.7 Typical Output Characteristics(I)

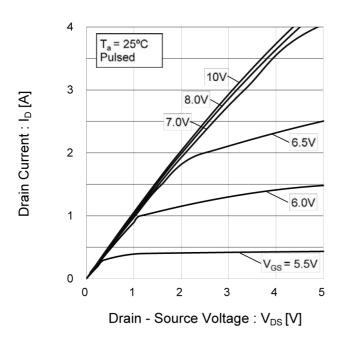
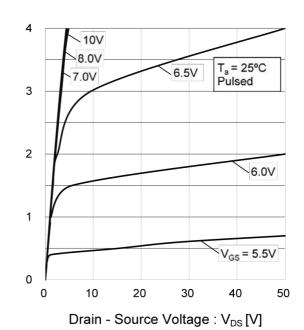


Fig.8 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.9 Typical Transfer Characteristics

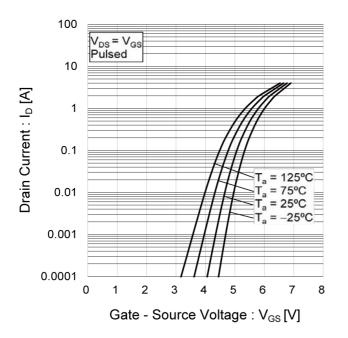


Fig.10 Normalized Gate Threshold

Voltage vs. Junction Temperature

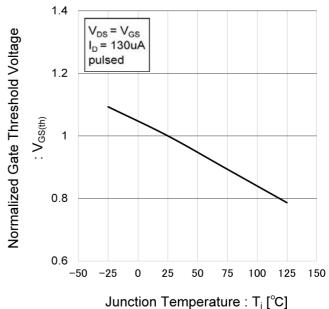


Fig.11 Static Drain - Source On - State Resistance vs. Drain Current

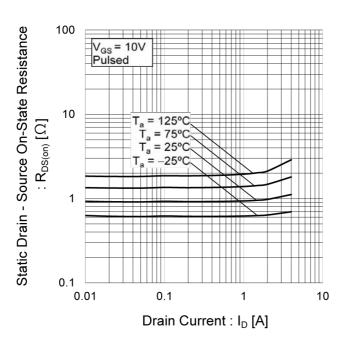


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

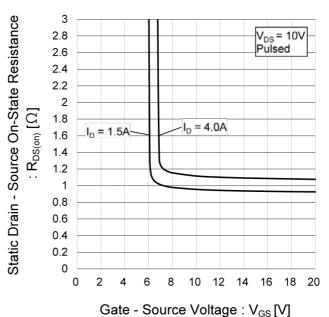


Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

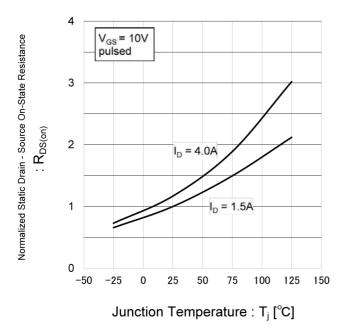
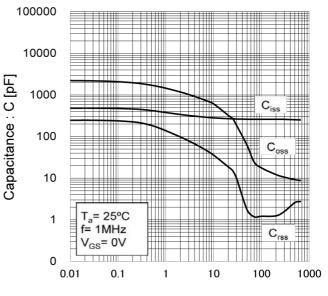


Fig.14 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage: V_{DS}[V]

Fig.15 Switching Characteristics

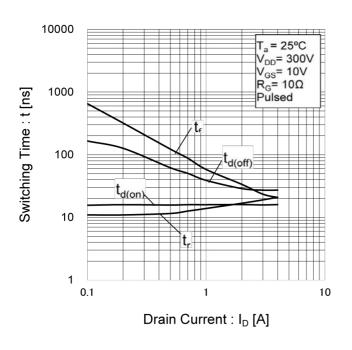
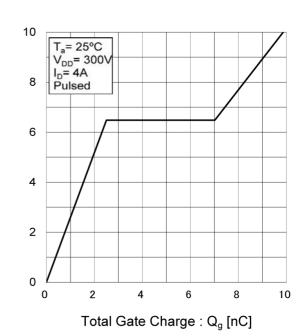


Fig.16 Typical Gate Charge



Gate - Source Voltage : V_{GS} [V]

Fig.17 Source Current vs. Source - Drain Voltage

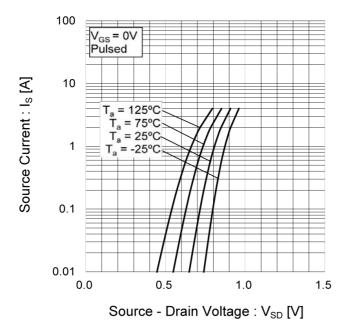
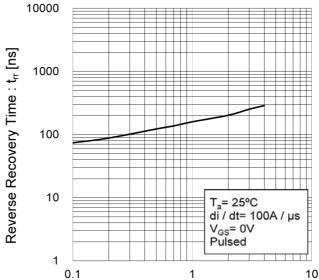


Fig.18 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current : I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

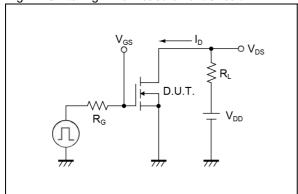


Fig.2-1 Gate Charge Measurement Circuit

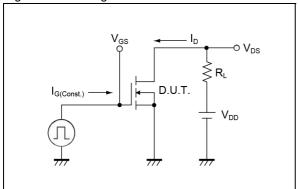


Fig.3-1 Avalanche Measurement Circuit

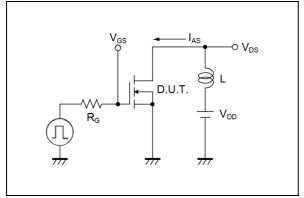


Fig.4-1 trr Measurement Circuit

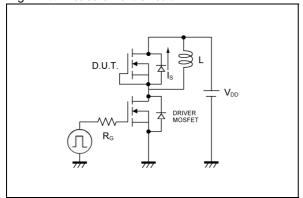


Fig.1-2 Switching Waveforms

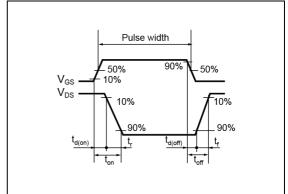


Fig.2-2 Gate Charge Waveform

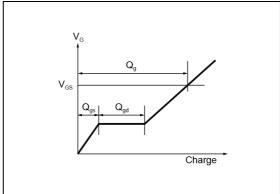


Fig.3-2 Avalanche Waveform

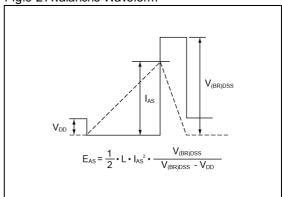
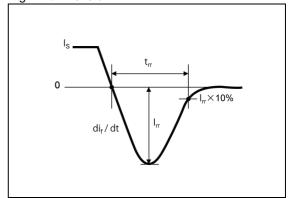
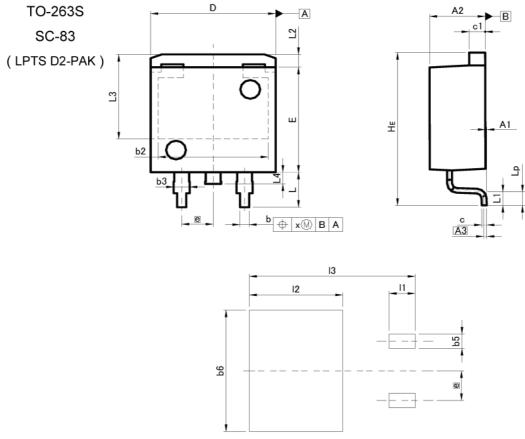


Fig.4-2 trr Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

TRA	MILIM	ETERS	INC	HES
IM	MIN	MAX	MIN	MAX
41	0.00	0.30	0.000	0.012
42	4.30	4.70	0.169	0.185
43	0.		0.0	
b	0.68	0.98	0.027	0.039
52		90	0.3	
53	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
:1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
e		54	0.1	
HE	12.80	13.40	0.504	0.528
	2.70	3.30	0.106	0.130
_1	1.	20	0.0	47
_2	1.	10	0.043	
_3	7.25		0.2	85
_4	1.	00	0.0	39
_p	0.90	1.50	0.035	0.059
х	=,	0.25		0.010
	2-28 V	0.23	INO	

DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
bb	=:	1.23	-	0.049
b6	= 0	10.40		0.409
11	23	2.10		0.083
12	77 .4	7.55	1.00	0.297
13	-	13.40	3 	0.528

Dimension in mm/inches



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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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