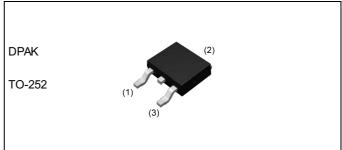


V _{DSS}	800V
R _{DS(on)} (Max.)	4.3Ω
I _D	±2.0A
P _D	69W

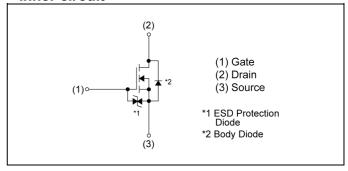
Outline



Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Pb-free plating; RoHS compliant
- 5) AEC-Q101 qualified

●Inner circuit



Packaging specifications

Jing specifications	
Packing	Embossed Tape
Reel size (mm)	330
Tape width (mm)	16
Quantity (pcs)	2500
Taping code	TL
Marking	R8002CND3
	Packing Reel size (mm) Tape width (mm) Quantity (pcs) Taping code

Application

Switching Power Supply

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	800	V
Continuous drain current (T _c = 25°C)	I _D *1	±2.0	А
Pulsed drain current	I _{DP} *2	±8.0	А
Gate - Source voltage	V_{GSS}	±30	V
Avalanche current, single pulse	I _{AS} *3	1.0	А
Avalanche energy, single pulse	E _{AS} *3	0.265	mJ
Power dissipation (T _c = 25°C)	P _D *4	69	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Downwortow	Cymah al	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *4	-	-	1.80	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	100	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

• Electrical characteristics $(T_a = 25^{\circ}C)$

Parameter	Symbol Conditions		Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	800	-	-	V	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 800V, V _{GS} = 0V	-	1	100	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 25V, V_{DS} = 0V$	-	-	±10	μΑ	
Gate threshold voltage	$V_{GS(th)}$	V _{DS} = 10V, I _D = 1mA	3.5	-	5.5	V	
Static drain - source on - state resistance	R _{DS(on)} *6	V _{GS} = 10V, I _D = 1.0A	-	3.3	4.3	Ω	
Gate resistance	R_{G}	f = 1MHz, open drain	-	7.6	-	Ω	

● Electrical characteristics (T_a = 25°C)

Davamatar	Cymah al	Conditions	Values			Lloit	
Parameter	Parameter Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	240	-		
Output capacitance	C _{oss}	V _{DS} = 25V	-	125	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	10	-		
Turn - on delay time	t _{d(on)} *6	V _{DD} ≈ 400V, V _{GS} = 10V	-	19	-		
Rise time	t _r *6	I _D = 1.0A	-	22	-		
Turn - off delay time	t _{d(off)} *6	$R_L \simeq 402\Omega$	-	35	-	ns	
Fall time	t _f *6	$R_G = 10\Omega$	-	67	-		

● Gate charge characteristics (T_a = 25°C)

Darameter	Cymaela a l	Conditions	Values			l limit
Parameter	Symbol	nbol Conditions		Тур.	Max.	Unit
Total gate charge	Q _g *6	V _{DD} ≃ 400V	-	12.1	-	
Gate - Source charge	Q _{gs} *6	I _D = 2.0A	-	3.0	-	nC
Gate - Drain charge	Q _{gd} *6	V _{GS} = 10V	-	6.6	-	
Gate plateau voltage	V _(plateau)	V _{DD} ≃ 400V, I _D = 2.0A	-	7.9	-	V

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*3} L \simeq 500 μ H, V_{DD} = 50V, R_G = 25 Ω , starting T_i = 25°C Fig.3-1,3-2

^{*4} T_c=25°C

^{*5} Mounted on an epoxy PCB FR4 (20×20×0.8mm)

^{*6} Pulsed

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Source current	I _S *1	· T _C = 25°C	1	-	2.0	Α
Pulsed source current	I _{SP} *2	1C - 23 C	1	-	8.0	Α
Source-Drain voltage	V _{SD} *6	$V_{GS} = 0V, I_{S} = 2.0A$	-	-	1.5	V
Reverse recovery time	t _{rr} *6		-	470	-	ns
Reverse recovery charge	Q _{rr} *6	I _S = 2.0A di/dt = 100A/μs	-	2.49	-	μC
Peak reverse recovery current	_{rr} *6		-	10.6	-	А

Fig.1 Power Dissipation Derating Curve

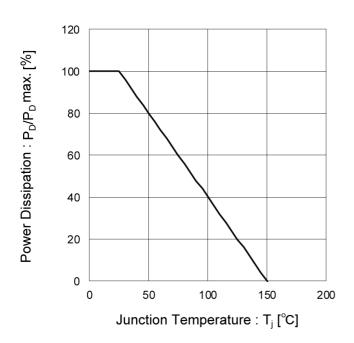


Fig.2 Drain Current Derating
Curve vs. Junction Temperature

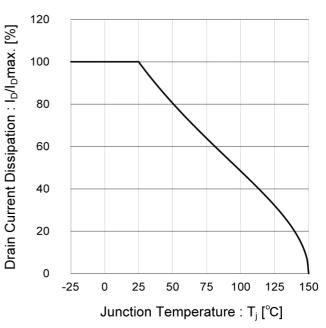


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

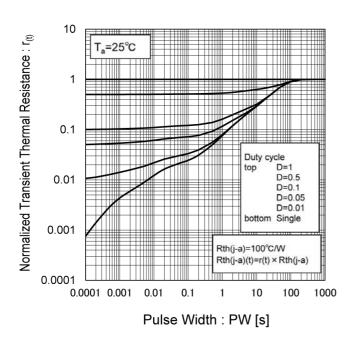


Fig.4 Maximum Safe Operating Area

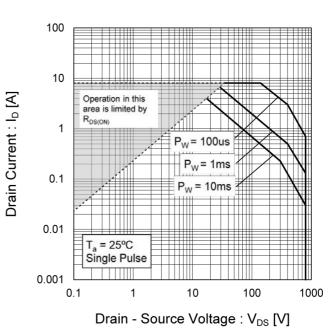


Fig.5 Avalanche Energy Derating
Curve vs. Junction Temperature

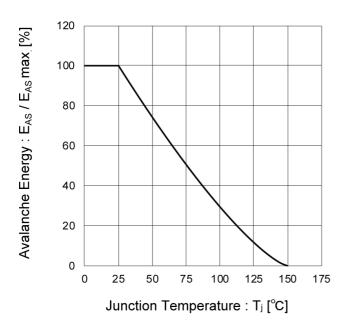


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

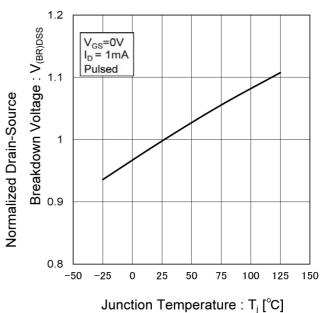


Fig.7 Typical Output Characteristics(I)

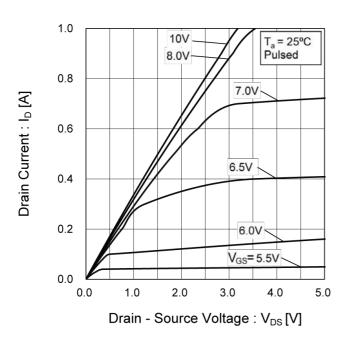


Fig.8 Typical Output Characteristics(II)

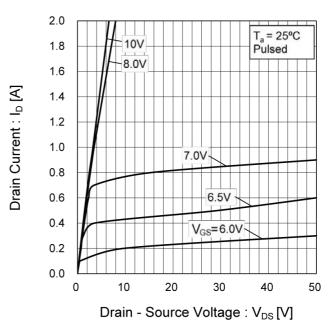


Fig.9 Typical Transfer Characteristics

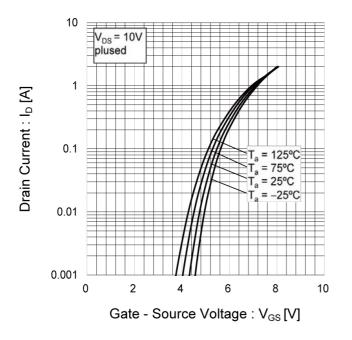
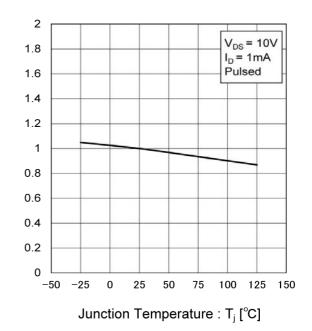


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature



Normalized Gate Threshold Voltage: V_{GS(th)}

Fig.11 Static Drain - Source On - State Resistance vs. Drain Current

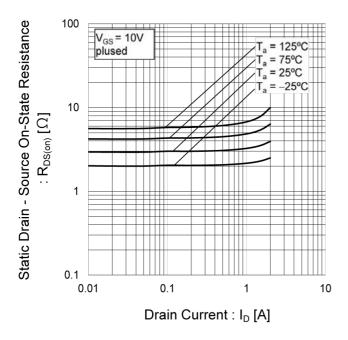


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

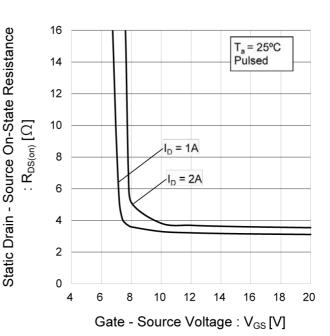


Fig.13 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

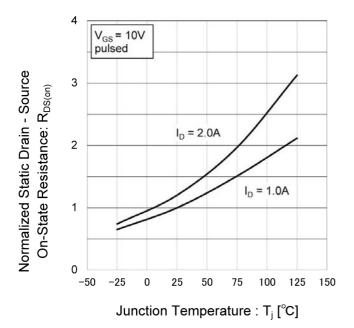


Fig.14 Typical Capacitance vs.
Drain - Source Voltage

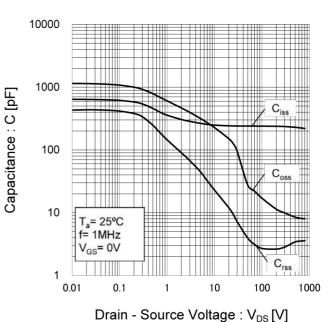


Fig.15 Switching Characteristics

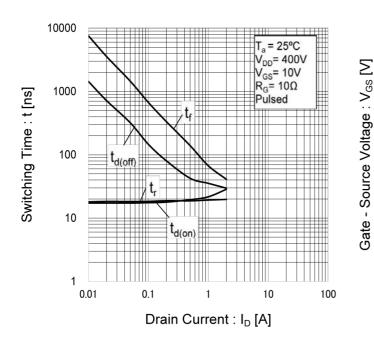


Fig.16 Typical Gate Charge

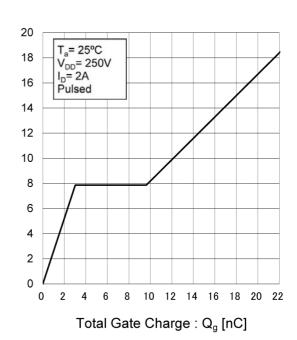


Fig.17 Source Current vs. Source - Drain Voltage

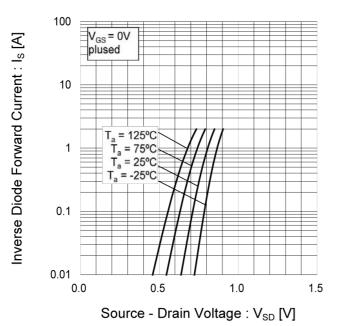
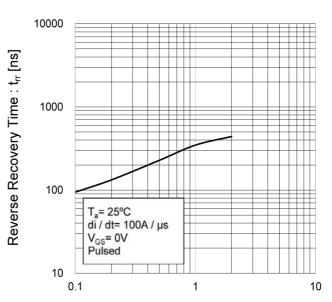


Fig.18 Reverse Recovery Time vs.
Inverse Diode Forward Current



Inverse Diode Forward Current: I_S [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

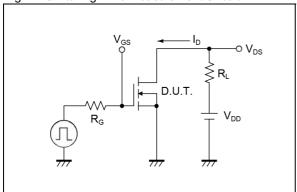


Fig.2-1 Gate Charge Measurement Circuit

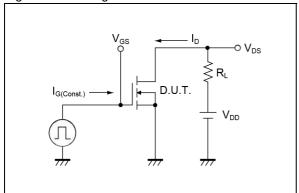


Fig.3-1 Avalanche Measurement Circuit

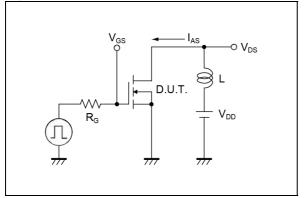


Fig.4-1 trr Measurement Circuit

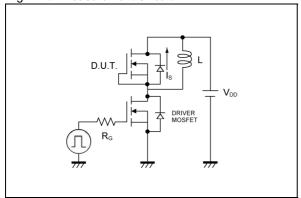


Fig.1-2 Switching Waveforms

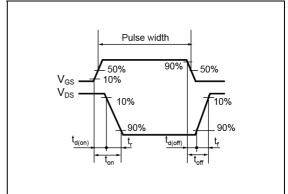


Fig.2-2 Gate Charge Waveform

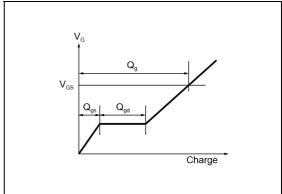


Fig.3-2 Avalanche Waveform

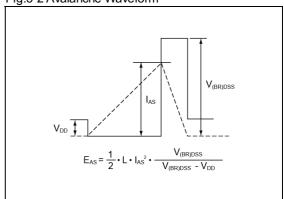
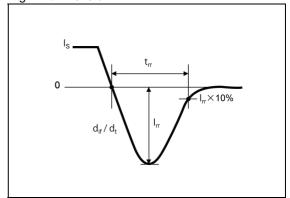
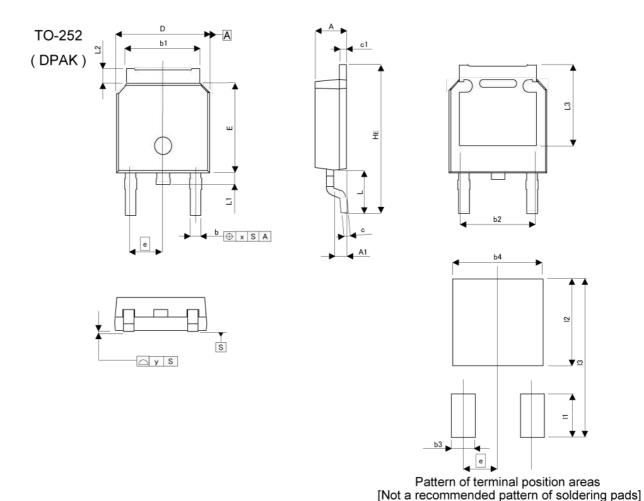


Fig.4-2 trr Waveform



Dimensions



MILIMETERS INCHES DIM MIN MAX MIN MAX 2.10 0.083 0.091 Α 2.30 A1 0.70 1.10 0.028 0.043 b 0.65 0.85 0.026 0.033 0.213 5.10 5.40 b1 0.201 b2 5.10 0.201 0.40 0.60 0.016 0.024 C 0.40 0.60 0.016 0.024 c1 D 6.40 6.80 0.252 0.268 е 0.236 6.00 6.40 0.252 E HE 9.50 10.50 0.374 0.413 0.114 0.028 L1 0.70 0.90 0.035 0.70 0.028 L2 1.30 0.051 L3 0.209 0.10 0.004 X у 0.10 0.004

MILIMETERS **INCHES** DIM MIN MAX MIN MAX b3 1.10 0.043 5.40 0.213 b4 11 2.90 0.114 12 5.50 0.217 13 10.50 0.413

Dimension in mm/inches

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()			
JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSⅢ
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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