

RCJ050N25

Nch 250V 5.0A Power MOSFET

V_{DSS}	250V
R _{DS(on)} (Max.)	1360m Ω
I _D	5.0A
P_D	30W

● Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 4) Parallel use is easy.
- 5) Pb-free lead plating; RoHS compliant
- 6) 100% Avalanche tested

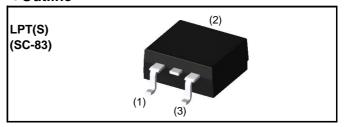
Application

Switching Power Supply

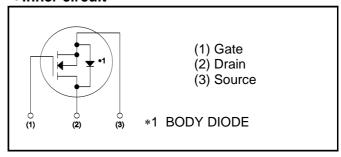
Automotive Motor Drive

Automotive Solenoid Drive

Outline



•Inner circuit



Packaging specifications

	ging opcomoditions	
	Packaging	Taping
	Reel size (mm)	330
Type	Tape width (mm)	24
Туре	Quantity (pcs)	1,000
	Taping code	TL
	Marking	RCJ050N25

•Absolute maximum ratings($T_a = 25$ °C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	250	V
Continuous drain current	T _c = 25°C	I _D *1	±5.0	А
Continuous drain current	T _c = 100°C	I _D *1	±2.7	А
Pulsed drain current	I _{D,pulse} *2	±20	А	
Gate - Source voltage		V_{GSS}	±30	V
Avalanche energy, single pulse		E _{AS} *3	1.82	mJ
Avalanche current		I _{AR} *3	2.5	А
T _c = 25°C		P _D	30	W
Power dissipation $T_a = 25^{\circ}C^{*4}$		P _D	1.56	W
Junction temperature	T _j	150	°C	
Range of storage temperature		T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	-	4.16	°C/W
Thermal resistance, junction - ambient *4	R_{thJA}	-	-	80	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol Conditions		Values			Unit	
- Farameter	Symbol	Conditions	Min.	Тур.	Max.	UIII	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	250	ı	ı	V	
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 250V, V_{GS} = 0V$ $T_j = 25^{\circ}C$	ı	ı	10	μΑ	
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 30V, V_{DS} = 0V$	1	1	±10	nA	
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V$, $I_D = 1mA$	3.5	-	5.5	V	
		$V_{GS} = 10V, I_D = 2.5A$	-	970	1360		
Static drain - source on - state resistance	R _{DS(on)} *5	$V_{GS} = 10V, I_{D} = 2.5A$ $T_{j} = 125^{\circ}C$	-	2100	2950	mΩ	
Forward transfer admittance	g fs	$V_{DS} = 10V, I_{D} = 2.5A$	1.25	2.50	-	S	

●Electrical characteristics(T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	350	-	
Output capacitance	C_{oss}	V _{DS} = 25V	-	30	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	15	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 125V, V_{GS} = 10V$	ı	15	ı	
Rise time	t _r *5	$I_{D} = 2.5A$	-	16	-	no
Turn - off delay time	t _{d(off)} *5	$R_L = 49.9\Omega$	-	18	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	10	-	

● Gate Charge characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol Conditions –		Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*5}	V _{DD} ≃ 125V	-	8.5	-	
Gate - Source charge	Q _{gs} *5	I _D = 5.0A	-	3.5	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 10V	-	3.5	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 125V, I_D = 5.0A$	-	8.0	-	V

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	r Symbol Conditions		Min.	Тур.	Max.	Offic
Continuous source current	l _S *1	T _c = 25°C	-	-	5.0	Α
Pulsed source current	I _{SM} *2	1 _c = 23 C	-	-	20	Α
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = 5.0A$	-	-	1.5	V
Reverse recovery time	t _{rr} *5	I _S = 2.5A	-	90	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	225	-	nC

^{*1} Limited only by maximum temperature allowed.

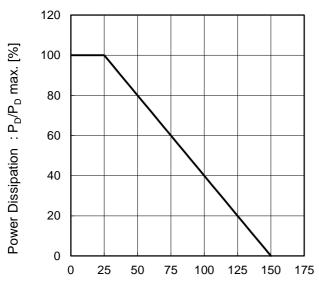
*5 Pulsed

^{*2} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*3} L $^{\simeq}$ 500 μ H, V_{DD} = 50V, Rg = 25 Ω , starting T $_{j}$ = 25°C

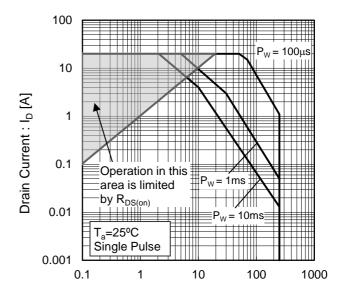
^{*4} Mounted on a epoxy PCB FR4 (25mm x 27mm x 0.8mm)

Fig.1 Power Dissipation Derating Curve



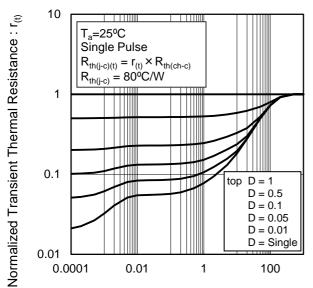
Junction Temperature : T_i [°C]

Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: Pw[s]

Fig.4 Avalanche Current vs Inductive Load

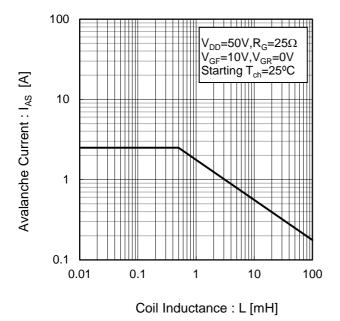
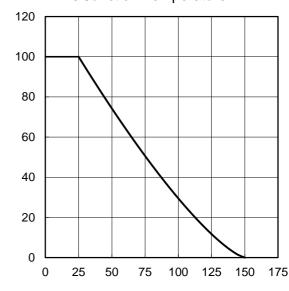


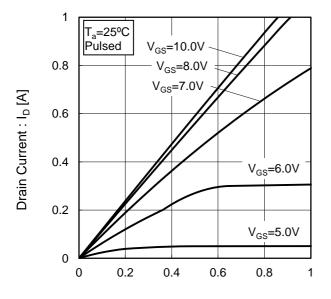
Fig.5 Avalanche Energy Derating Curve vs Junction Temperature



Avalanche Energy : E_{AS} / E_{AS} max. [%]

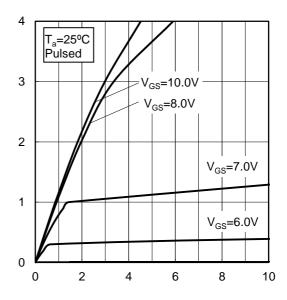
Junction Temperature : T_i [°C]

Fig.6 Typical Output Characteristics(I)



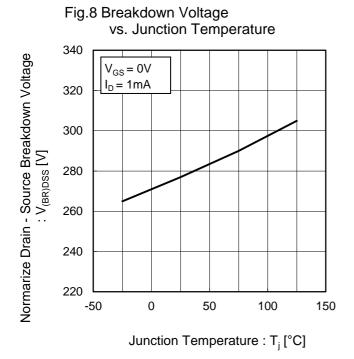
Drain - Source Voltage : V_{DS} [V]

Fig.7 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

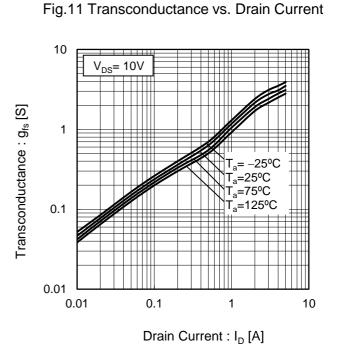
Drain Current : I_D [A]



Gate - Source Voltage : V_{GS} [V]

Fig.9 Typical Transfer Characteristics

Fig.10 Gate Threshold Voltage vs. Junction Temperature 5.5 $V_{DS} = 10V$ $I_D = 1mA$ Gate Threshold Voltage: VGS(th) [V] 5.0 4.5 4.0 3.5 3.0 2.5 -50 -25 25 50 75 100 125 150 Junction Temperature : T_i [°C]

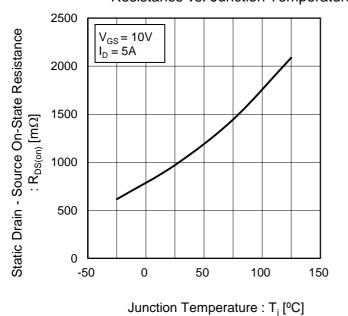


6/12

Fig.12 Static Drain - Source On - State Fig.13 Static Drain - Source On - State Resistance vs. Gate Source Voltage Resistance vs. Drain Current(I) 4000 10000 Static Drain - Source On-State Resistance T_a=25°C Static Drain - Source On-State Resistance T_=25°C 3000 $I_{D} = 5.0A$ $V_{GS} = 10V$ $: R_{DS(on)} [m\Omega]$ $:R_{DS(on)}\left[m\Omega \right]$ $I_{D} = 2.5A$ 2000 1000 1000 100 0 0 5 10 15 20 0.01 0.1 1 10

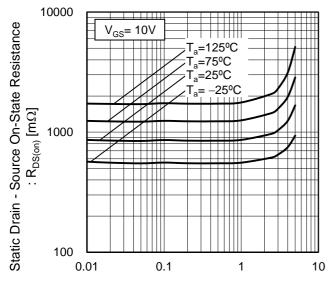
Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature

Gate - Source Voltage : V_{GS} [V]



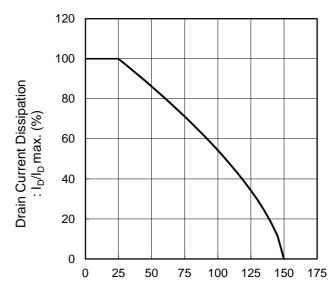
Drain Current: I_D [A]

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)



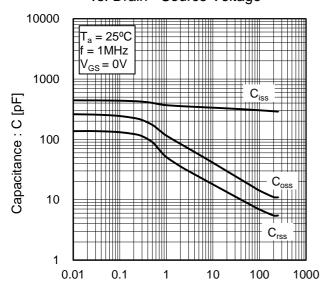
Drain Current : I_D [A]

Fig.16 Drain Current Derating Curve



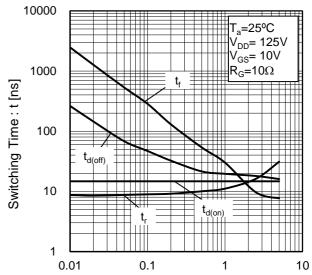
Junction Temperature : T_i [°C]

Fig.17 Typical Capacitance vs. Drain - Source Voltage



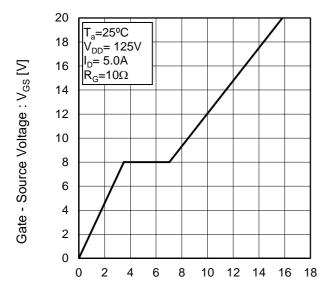
Drain - Source Voltage : V_{DS} [V]

Fig.18 Switching Characteristics



Drain Current: I_D [A]

Fig.19 Dynamic Input Characteristics



Total Gate Charge : Q_g [nC]

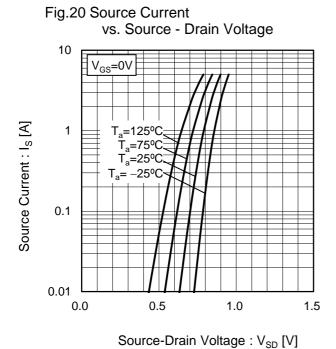


Fig21 Reverse Recovery Time vs.Source Current

1000

Ta=25°C

di / dt = 100A / µs

V_{GS} = 0V

0.1

10

Source Current : I_S [A]

●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

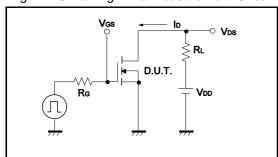


Fig.2-1 Gate Charge Measurement Circuit

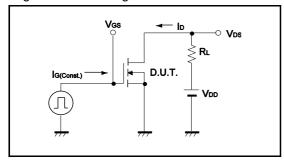


Fig.3-1 Avalanche Measurement Circuit

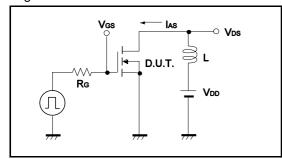


Fig.1-2 Switching Waveforms

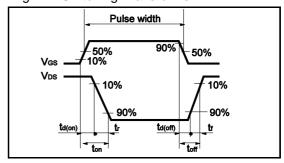


Fig.2-2 Gate Charge Waveform

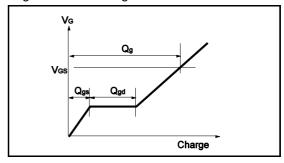
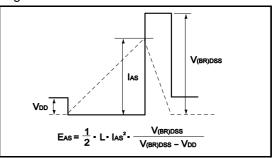
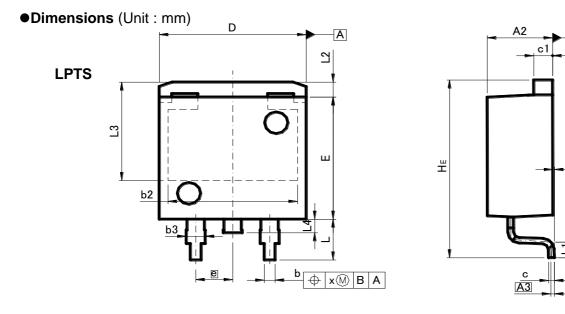
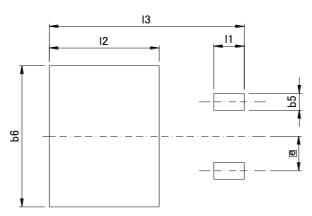


Fig.3-2 Avalanche Waveform







Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0	0.012
A2	4.30	4.70	0.169	0.185
A3	0.:	25	0.	01
b	0.68	0.98	0.027	0.039
b2	8.	90	0.	35
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.	2.54 0.10		10
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.13
L1	0.90	1.50	0.035	0.059
L2	1.	10	0.0)43
L3	7.25		0.285	
L4	1.0	00	0.0)39
Lp	0.90	1.50	0.035	0.059
х	_	0.25	_	0.01

DIM	MILIMETERS II		INC	CHES	
DIM	MIN	MAX	MIN	MAX	
b5	-	1.23	-	0.049	
b6	ı	10.40	ı	0.409	
11	ı	2.10	ı	0.083	
12	-	7.55	-	0.297	
13	-	13.40	-	0.528	

Dimension in mm/inches

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 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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