

RD3G03BBG

Nch 40V 65A Power MOSFET

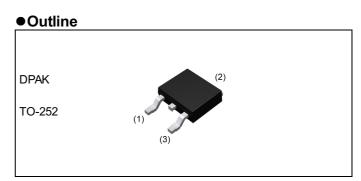
V _{DSS}	40V
R _{DS(on)} (Max.)	6.5mΩ
I _D	±65A
P _D	50W

Features

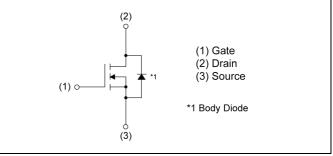
- 1) Low on resistance
- 2) High power package (TO-252)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen free

Application

Switching



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	16
	Quantity (pcs)	2500
	Taping code	TL1
	Marking	RD3G03BBG

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Para	meter	Symbol	Value	Unit	
Drain - Source voltage		V _{DSS}	40	V	
Continuous dusis sumont	Silicon limit (V _{GS} =10V)	۱ _D *1	±65	А	
Continuous drain current	T _a = 25°C (V _{GS} =10V)	I _D *2	±35	А	
Pulsed drain current		I _{DP} *3	±260	А	
Gate - Source voltage		V _{GSS}	±20	V	
Avalanche current, single pulse		I_{AS}^{*4}	17.5	А	
Avalanche energy, single pulse		E_{AS}^{*4}	23	mJ	
Power dissipation		P _D *2	50	W	
Junction temperature		Tj	150	°C	
Operating junction and storage temperature range		T _{stg}	-55 to +150	°C	

•Thermal resistance

Parameter	Symbol	Values			Unit
Falanielei	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}^{*2}	-	-	2.5	°C/W

• Electrical characteristics (T_a = 25°C)

Deremeter	Currence of	Conditions	Values			Lincit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		40	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1 \text{mA}$ referenced to 25°C		-	28.9	-	mV/°C	
Zero gate voltage drain current	I_{DSS} V_{DS} = 40V, V_{GS} = 0V		-	-	1	μA	
Gate - Source leakage current	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 1mA$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$			-4.6	-	mV/°C	
Static drain - source	D *5	V _{GS} = 10V, I _D = 35A	-	5.0	6.5		
on - state resistance	${\sf R}_{\sf DS(on)}^{*5}$	V _{GS} = 4.5V, I _D = 17.5A	-	7.6	10.6	mΩ	
Gate resistance	R _G -		-	2.1	-	Ω	
Forward Transfer Admittance	Y _{fs} * ⁵	V _{DS} = 5V, I _D = 17.5A	10	-	-	S	

*1 Limited by silicon chip capability.

*2 T_c=25°C, Limited only by maximum temperature allowed.

*3 Pw \leq 10µs , Duty cycle \leq 1%

*4 L \simeq 0.1mH, V_{DD} = 20V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2

*5 Pulsed





•Electrical characteristics (T_a = 25°C)

Deremeter	Cumph of	Conditions	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	1170	-		
Output capacitance	C _{oss}	V _{DS} = 20V	-	540	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	65	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 20V, V_{GS} = 10V$	-	12	-		
Rise time	t _r *5	I _D = 17.5A	-	14	-	-	
Turn - off delay time	t _{d(off)} *5	R _L ≃ 1.14Ω	-	42	-	ns	
Fall time	t _f *5	R _G = 10Ω	-	16	-		

• Gate charge characteristics (T_a = 25°C)

Deremeter	O: make al	Symbol Conditions		Values			1 1	
Parameter	Зупрог			Min.	Тур.	Max.	Unit	
T () ()	O *5		V _{GS} = 10V	-	18.2	-		
Total gate charge	Q_g^{*5}		$V_{DD} \simeq 20V$		-	9.3	-	nC
Gate - Source charge	Q_{gs}^{*5}	I _D = 35A	V _{GS} = 4.5V	-	2.9	-	nc	
Gate - Drain charge	Q _{gd} *5			-	3.4	-		

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Deremeter	Sumbol	Conditions		Values		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I _S	T _a = 25℃	-	-	35	А
Pulse forward current	I_{SP}^{*3}	$T_a = 25 C$	-	-	260	А
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 35A	-	-	1.2	V
Reverse recovery time	t _{rr} *5	I _S = 35A, V _{GS} =0V	-	34	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/µs	-	33	-	nC



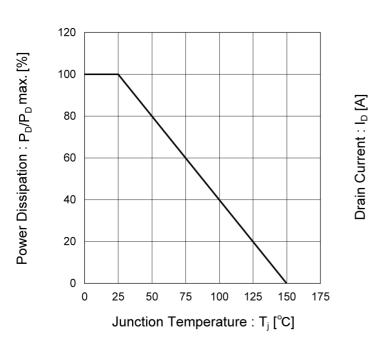


Fig.1 Power Dissipation Derating Curve

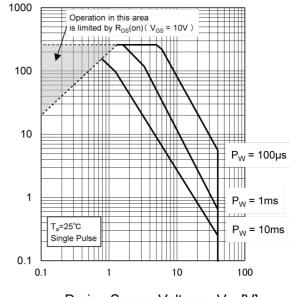


Fig.2 Maximum Safe Operating Area

Drain - Source Voltage : V_{DS} [V]

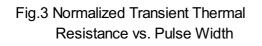
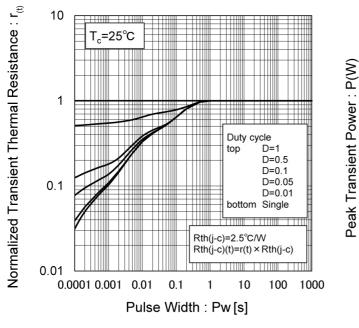
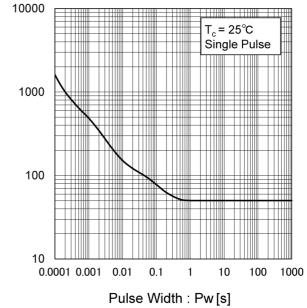


Fig.4 Single Pulse Maximum Power Dissipation







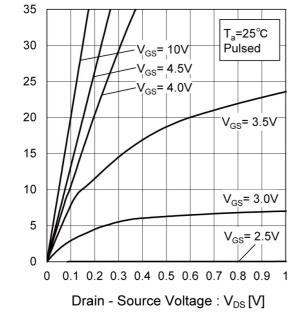
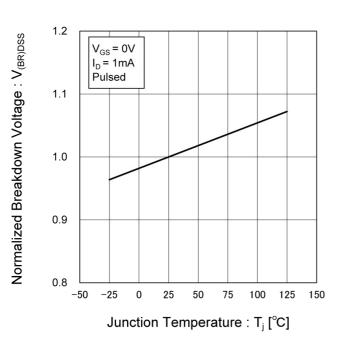
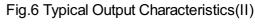


Fig.7 Normalized Breakdown Voltage vs. Junction Temperature





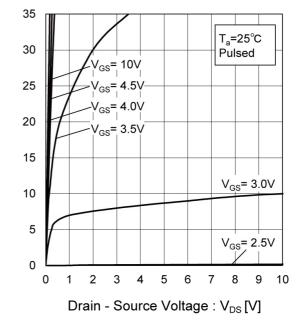
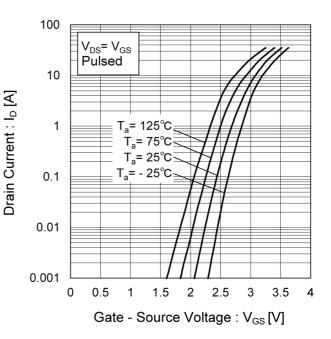


Fig.8 Typical Transfer Characteristics



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Fig.5 Typical Output Characteristics(I) Fig.5

Drain Current : I_D [A]

• Electrical characteristic curves

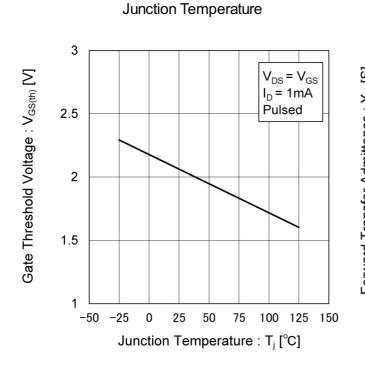


Fig.9 Gate Threshold Voltage vs.

Fig.10 Forward Transfer Admittance vs. Drain Current

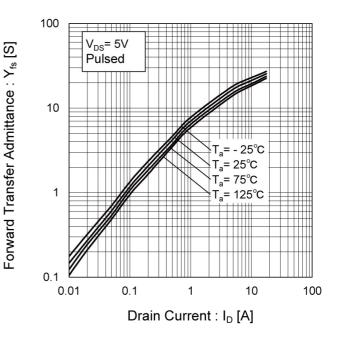
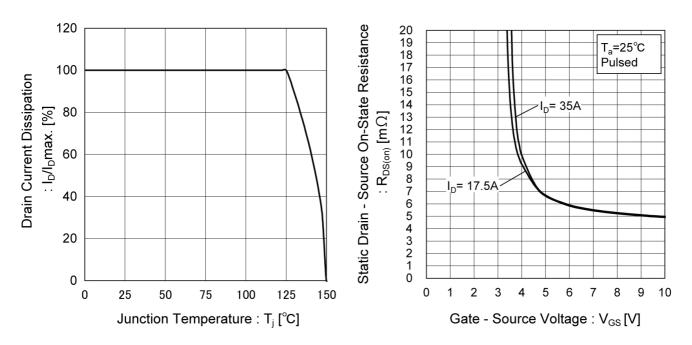


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage





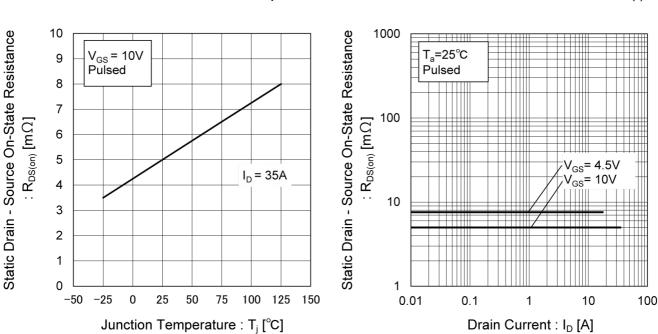


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

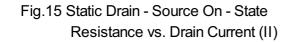
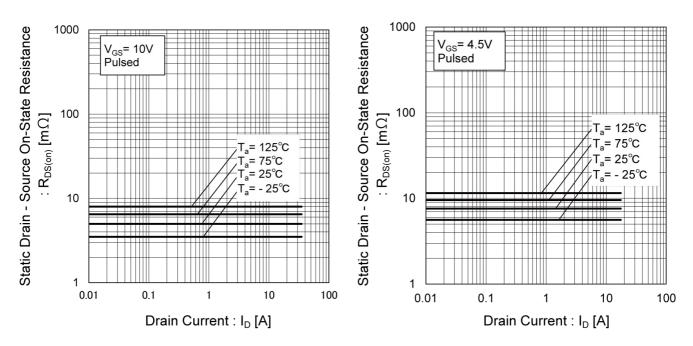


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



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•Electrical characteristic curves

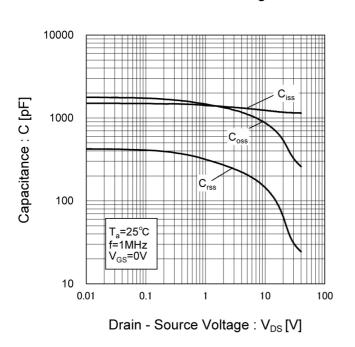


Fig.17 Typical Capacitances vs. Drain - Source Voltage

Fig.18 Switching Characteristics

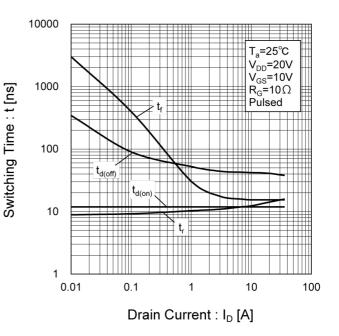


Fig.19 Typical Gate Charge

Gate - Source Voltage : V_{GS} [V]

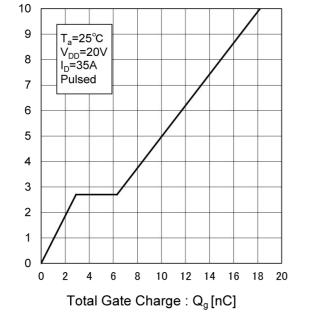
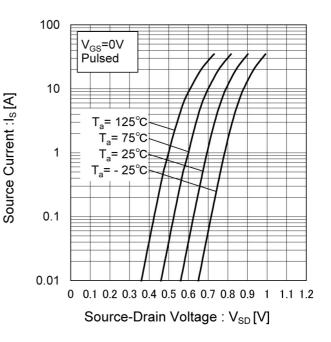


Fig.20 Source Current vs. Source Drain Voltage



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Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

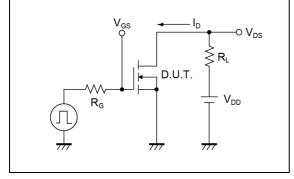


Fig.2-1 Gate Charge Measurement Circuit

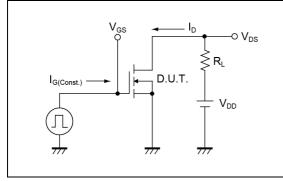


Fig.3-1 Avalanche Measurement Circuit

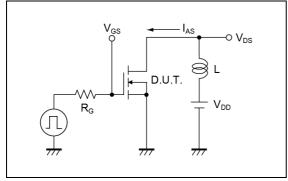


Fig.1-2 Switching Waveforms

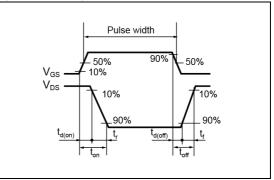


Fig.2-2 Gate Charge Waveform

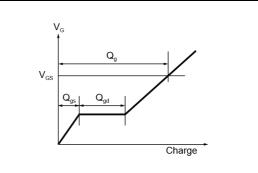
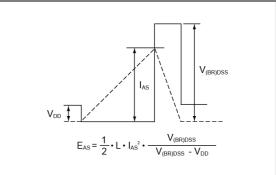
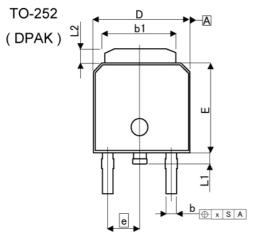


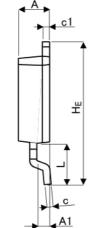
Fig.3-2 Avalanche Waveform

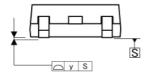


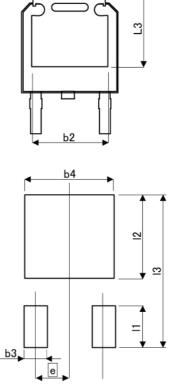


Dimensions









Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A	2.20	2.40	0.087	0.094
A1	0.70	1.10	0.028	0.043
b	0.60	0.90	0.024	0.035
b1	5.20	5.50	0.205	0.217
b2	4.	80	0.1	89
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.0)91
E	6.00	6.40	0.236	0.252
HE	9.40	10.40	0.370	0.409
L	2.	90	0.1	14
L1	0.60	1.00	0.024	0.039
L2	0.70	1.30	0.028	0.051
L3	5.	30	0.2	209
x	-	0.25	-	0.010
у	-	0.10	-	0.004
DIM	MILIME	TERS	INCHES	
DIM	MIN	MAX	MIN	MAX
b3	-	1.15	- 1	0.045
b4	-	5.55	-	0.219
1	-	2.77	-	0.109
12	-	5.50		0.217
13	-	10.40	-	0.409

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
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CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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