## Nch 40V 50A Power MOSFET

V <sub>DSS</sub>	40V
R <sub>DS(on)</sub> (Max.)	4.9mΩ
I <sub>D</sub>	±50A
P <sub>D</sub>	35W

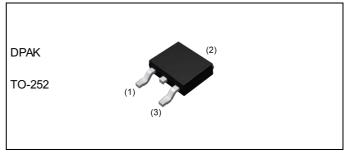
# ● Features

- 1) Low on resistance
- 2) High power package (TO-252)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen free

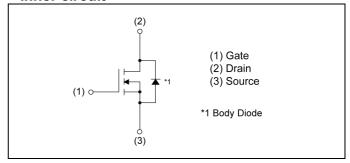
# Application

Switching

## Outline



## Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Type	Tape width (mm)	16
	Quantity (pcs)	2500
	Taping code	TL
	Marking	RD3G500GN

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	40	V
Continuous drain current	I <sub>D</sub> *1	±50	Α
Pulsed drain current	I <sub>DP</sub> *2	±100	Α
Gate - Source voltage	V <sub>GSS</sub>	±20	V
Avalanche current, single pulse	I <sub>AS</sub> *3	50	Α
Avalanche energy, single pulse	E <sub>AS</sub> *3	94	mJ
Power dissipation	P <sub>D</sub> *1	35	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Parameter Thermal resistance, junction - case	Symbol	Values			1.1
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *1	-	-	3.5	°C/W

## ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatav	Cymah al	Conditions	Values			Unit	
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Uriil	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		40	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j} I_D = 1 \text{mA}$ referenced to 25°C		26.2	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V	1	1	1	μA	
Gate - Source leakage current	$I_{GSS}$ $V_{GS} = \pm 20V$ , $V_{DS} = 0V$		ı	ı	±500	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 1mA$	1.0	1	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-4.9	-	mV/°C	
Static drain - source	D *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 50A	-	3.9	4.9	mΩ	
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 50A	-	4.7	6.3	11122	
Gate resistance	$R_G$	f = 1MHz, open drain	1	2.1	1	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 5V, I <sub>D</sub> = 25A	25	-	-	S	

<sup>\*1</sup> Tc=25°C, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw $\leq$ 10 $\mu$ s , Duty cycle $\leq$ 1%

<sup>\*3</sup> L  $\simeq$  0.05mH, V<sub>DD</sub> = 20V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Doromotor	Symbol	Conditions	Values			Unit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Offic	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2280	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 20V	ı	370	1	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	ı	100	1		
Turn - on delay time	$t_{d(on)}^{*4}$	$V_{DD} \simeq 20V, V_{GS} = 10V$	1	12.5	1		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 50A	1	7.5	ı	no	
Turn - off delay time	$t_{d(off)}^{*4}$	$R_L \simeq 0.4\Omega$	1	65	1	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	10	-		

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

Davamatav	Cymahal	Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gate above	O *4		V <sub>GS</sub> = 10V	-	31	-	
Total gate charge	Q <sub>g</sub> *4	$V_{DD} \simeq 20V$		-	16	-	<b>"</b> C
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 50A	V <sub>GS</sub> = 4.5V	-	5.9	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4			-	4.5	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Symbol	Conditions	Values			Unit
Parameter	Parameter Symbol Conditions		Min.	Тур.	Max.	Offic
Continuous forward current	I <sub>S</sub>	T = 25°C	-	-	29	Α
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25°C	-	-	100	Α
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 29A	-	-	1.2	V

#### Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

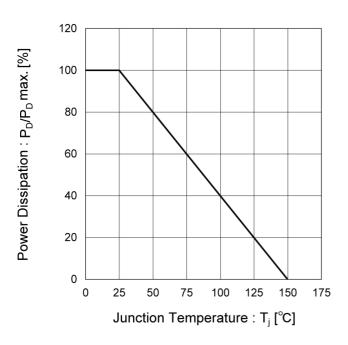
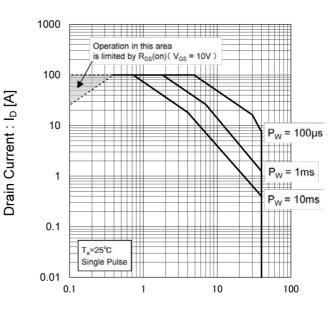


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage :  $V_{DS}[V]$ 

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

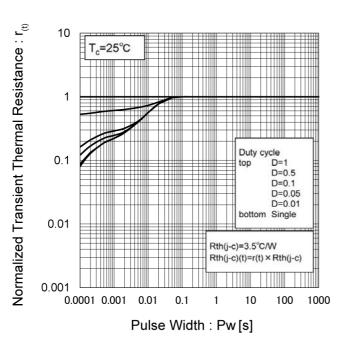
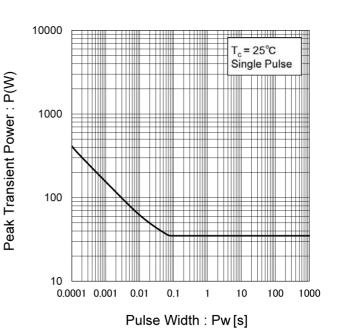


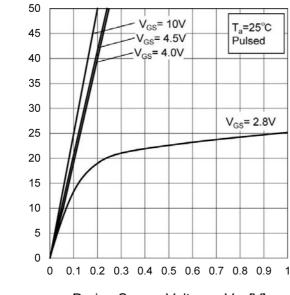
Fig.4 Single Pulse Maximum Power dissipation



Drain Current : I<sub>D</sub> [A]

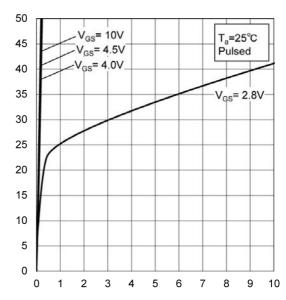
## • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V<sub>DS</sub> [V]

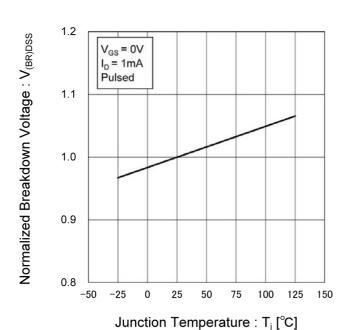
Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Normalized Breakdown Voltage vs. Junction Temperature



## • Electrical characteristic curves

Fig.8 Typical Transfer Characteristics

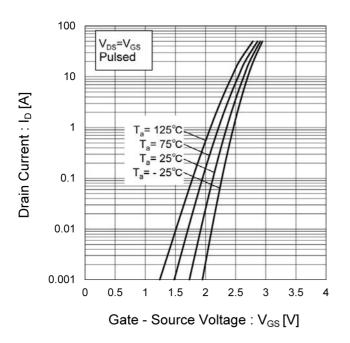


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

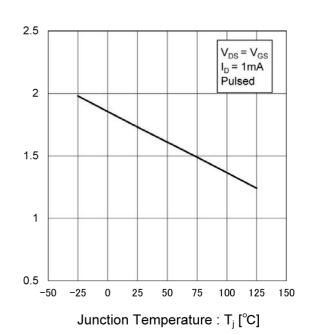
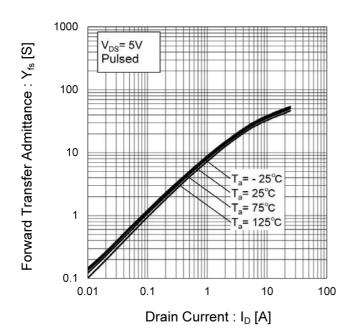


Fig.10 Forward Transfer Admittance vs.
Drain Current



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Gate Threshold Voltage :  $V_{GS(th)}\left[V\right]$ 

## • Electrical characteristic curves

Fig.11 Drain Current Derating Curve

Drain Current Dissipation

O 25 50 75 100 125 150

Junction Temperature : T<sub>i</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

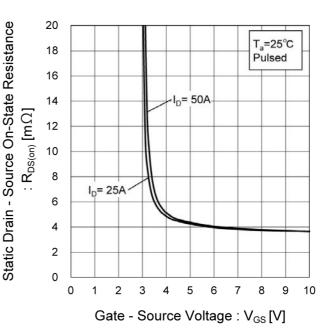
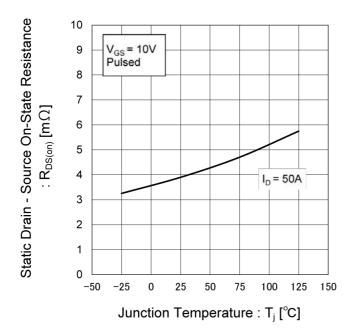


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



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## • Electrical characteristic curves

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

Static Drain Current :  $I_D$  [A]

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

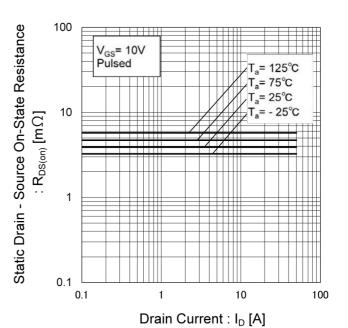
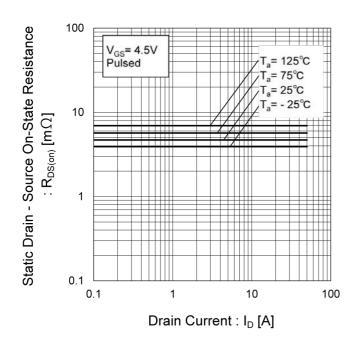


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



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## Electrical characteristic curves

Fig.17 Typical Capacitance vs. Drain - Source Voltage

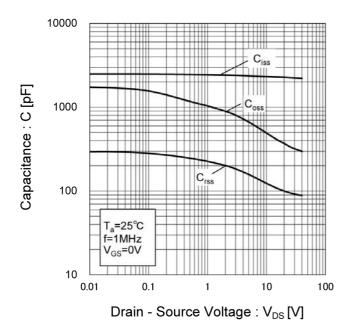


Fig.18 Switching Characteristics

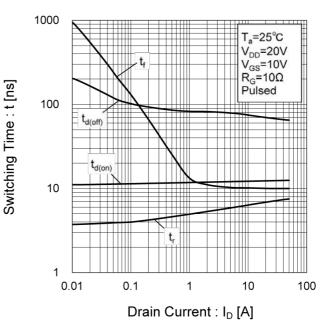


Fig.19 Dynamic Input Characteristics

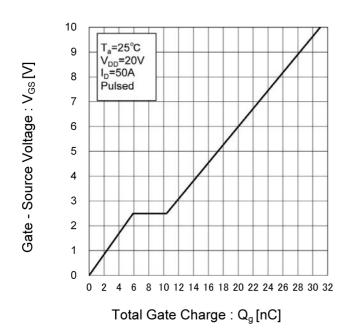
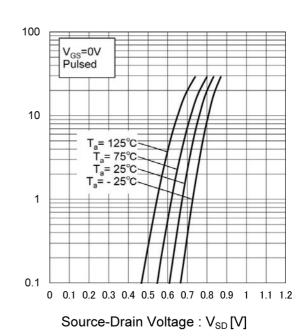


Fig.20 Source Current vs. Source Drain Voltage



Source Current : Is [A]

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## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

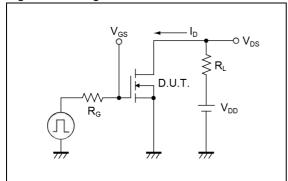


Fig.2-1 Gate Charge Measurement Circuit

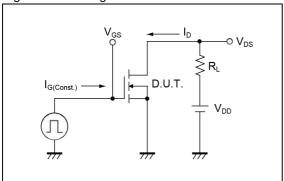


Fig.3-1 Avalanche Measurement Circuit

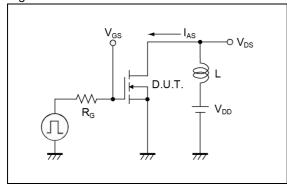


Fig.1-2 Switching Waveforms

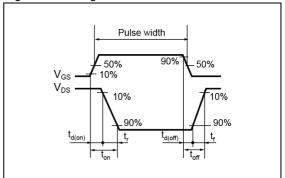


Fig.2-2 Gate Charge Waveform

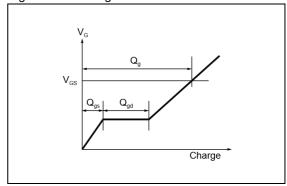
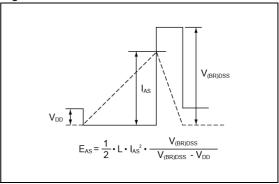
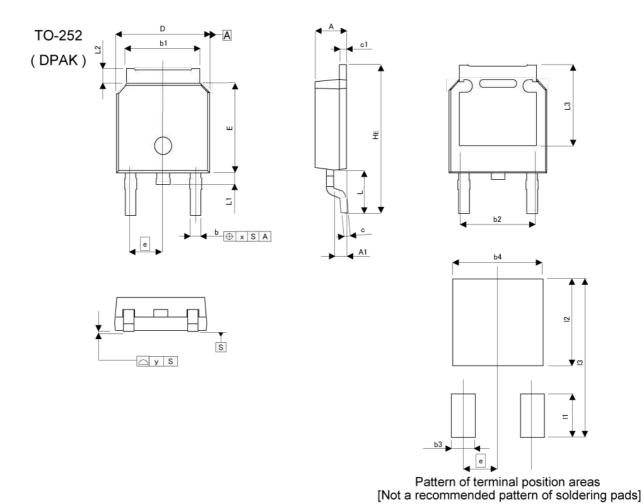


Fig.3-2 Avalanche Waveform



## Dimensions



DIM -	MILIME	ETERS	INC	HES	
DIIVI	MIN	MAX	MIN	MAX	
Α	2.10	2.30	0.083	0.091	
A1	0.70	1.10	0.028	0.043	
b	0.65	0.85	0.026	0.033	
b1	5.10	5.40	0.201	0.213	
b2	5.	10	0.2	201	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.	30	0.091		
E	6.00	6.40	0.236	0.252	
HE	9.50	10.50	0.374	0.413	
L	2.	90	0.1	114	
L1	0.70	0.90	0.028	0.035	
L2	0.70	1.30	0.028	0.051	
L3	L3 5.30		0.2	209	
х	-	0.10	160	0.004	
у	-	0.10	-	0.004	

DIM	MILIME	MILIMETERS		HES
DIM L	MIN	MAX	MIN	MAX
b3	2	1.10	623	0.043
b4	*	5.40	5,41	0.213
I1 .	<u> </u>	2.90	72	0.114
12	-	5.50	5.00	0.217
13	<u>(4</u> )	10.50	V21	0.413

Dimension in mm/inches



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- 8. Confirm that operation temperature is within the specified range described in the product specification.
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  - [c] the Products are exposed to direct sunshine or condensation
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