Pch -45V -8A Power MOSFET

V _{DSS}	-45V
R _{DS(on)} (Max.)	91mΩ
I _D	±8A
P_D	15W

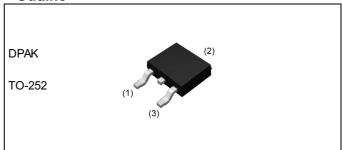
● Features

- 1) Low on resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Parallel use is easy
- 5) Pb-free lead plating; RoHS compliant

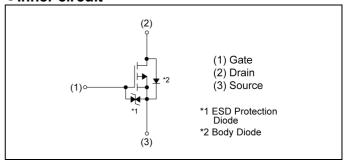
Application

Switching

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
_	Tape width (mm)	16
Type	Basic ordering unit (pcs)	2500
	Taning and	TL
	Taping code	TL1
	Marking	RD3H080SP

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	-45	V
Continuous drain current	I _D *1	±8	А
Pulsed drain current	I _{DP} *2	±16	А
Gate - Source voltage	V _{GSS}	±20	V
Power dissipation	P _D *3	15	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter Thermal resistance, junction - case	Symbol	Values			l le:4
		Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *3	-	-	8.33	°C/W

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = -1mA$	-45	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	-	-50	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = -45V, V _{GS} = 0V	-	-	-1	μA	
Gate - Source leakage current I _{GSS}		$V_{GS} = \pm 20V, V_{DS} = 0V$	1	1	±10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = -10V$, $I_{D} = -1mA$	-1.0	1	-3.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	-	3.3	-	mV/°C	
		V _{GS} = -10V, I _D = -8A	-	65	91		
Static drain - source on - state resistance	R _{DS(on)} *4	V _{GS} = -4.5V, I _D = -8A	-	95	133	mΩ	
on state resistance		V _{GS} = -4.0V, I _D = -8A	-	105	147		
Gate resistance	R _G f = 1MHz, open drain		1	8.8	-	Ω	
Forward Transfer Y		V _{DS} = -10V, I _D = -8A	6.0	-	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*3} T_C=25°C

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

Daramatar	Cymahal	Conditions	Values			l lait	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	1000	-		
Output capacitance	C _{oss}	v _{DS} = -10V		160	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz -		80	-		
Turn - on delay time ${t_{d(on)}}^{*4}$		$V_{DD} \simeq -25V, V_{GS} = -10V$	-	12	-		
Rise time	t _r *4	I _D = -4A	-	15	-	no	
Turn - off delay time	t _{d(off)} *4	R _L ≃ 6.25Ω	-	50	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	20	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ a	,				
Parameter	Cymahal	Conditions	Values			l lm:t
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg*4	V _{DD} ≃ - 25V.	-	9.0	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \simeq -25V$, $I_D = -8A$, $V_{GS} = -5V$	-	4.0	-	nC
Gate - Drain charge	Q _{gd} *4		-	3.0	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Daramatar	Symbol	Conditions	Values			l lait
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I _S *1	T - 25°C	-	-	-8	Α
Pulse forward current	I _{SP} *2	T _a = 25°C	-	-	-16	Α
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = -8A	-	-	-1.2	V

Fig.1 Power Dissipation Derating Curve

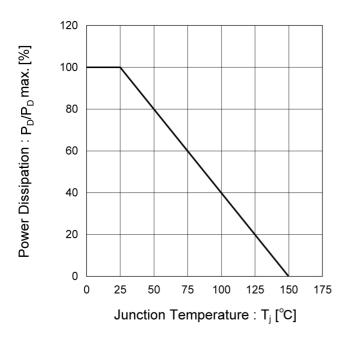


Fig.2 Maximum Safe Operating Area

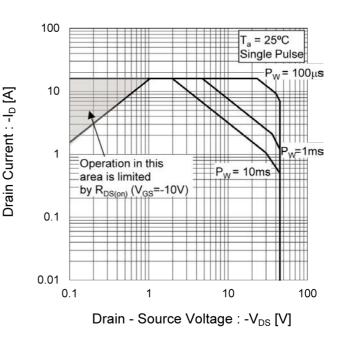


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

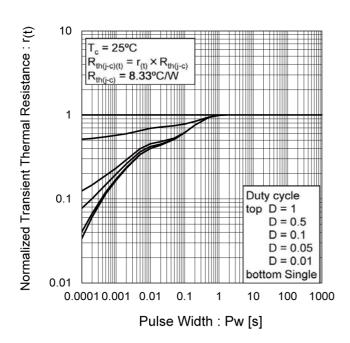


Fig.4 Single Pulse Maximum Power dissipation

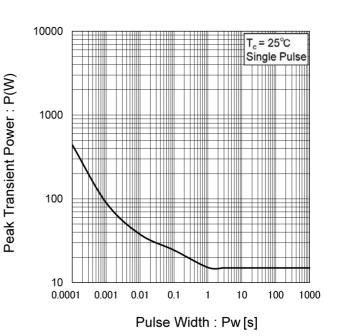


Fig.5 Typical Output Characteristics(I)

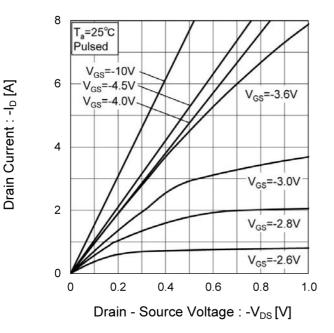
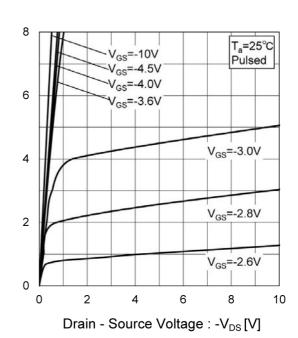


Fig.6 Typical Output Characteristics(II)



Drain Current : -I_D [A]

Fig.7 Breakdown Voltage vs.

Junction Temperature

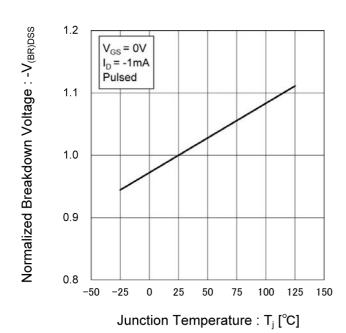


Fig.8 Typical Transfer Characteristics

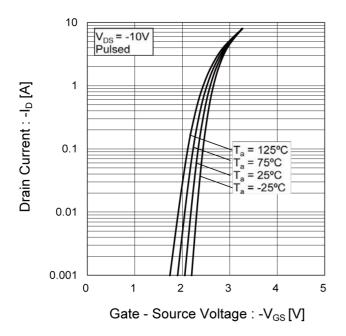


Fig.9 Gate Threshold Voltage vs.

Junction Temperature

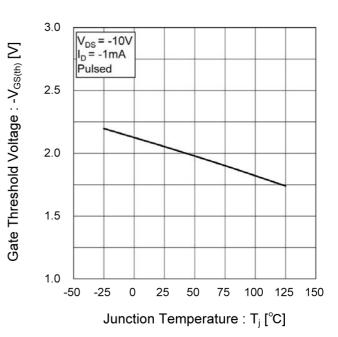
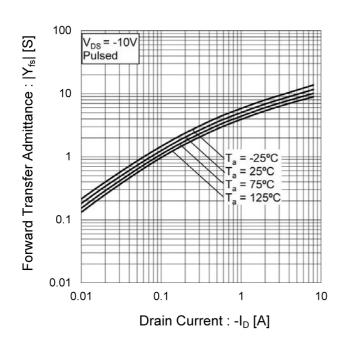


Fig.10 Forward Transfer Admittance vs.
Drain Current



6/12

Fig.11 Drain Current Derating Curve

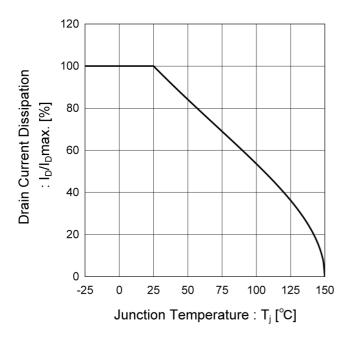


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

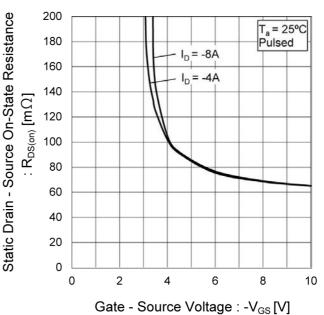


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

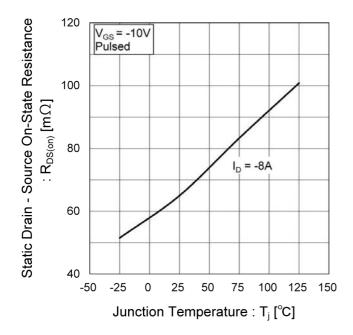


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

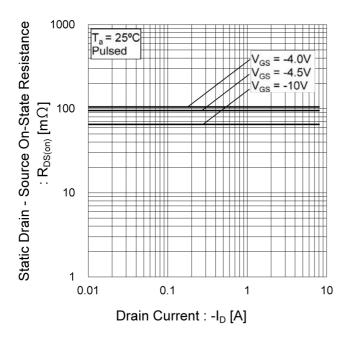


Fig.15 Static Drain - Source On - State
Resistance vs. Drain Current(II)

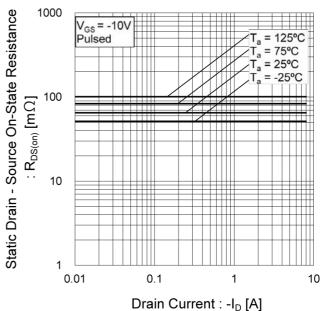


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

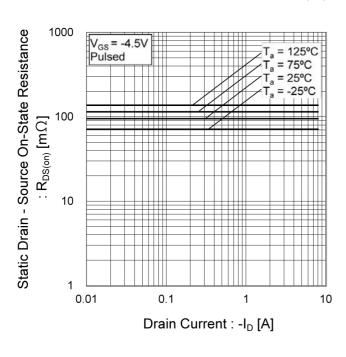


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

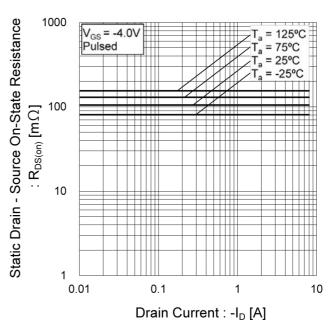


Fig.18 Typical Capacitance vs.

Drain - Source Voltage

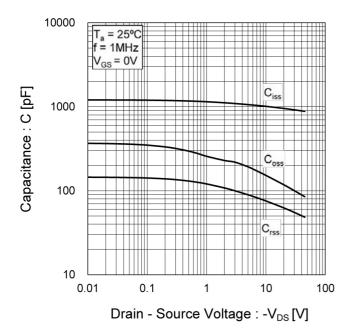


Fig.19 Switching Characteristics

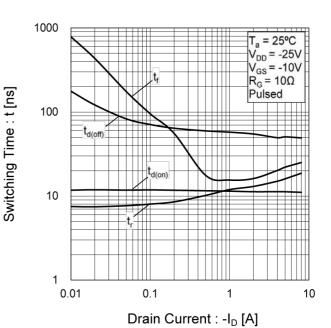


Fig.20 Dynamic Input Characteristics

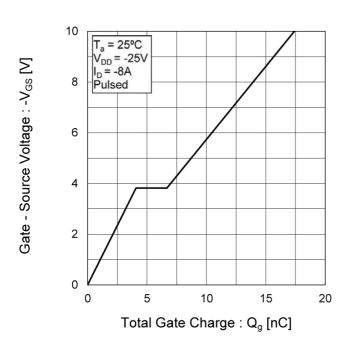
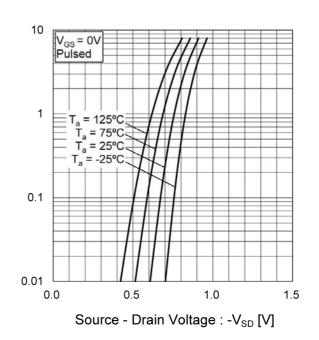


Fig.21 Source Current vs.

Source Drain Voltage



Source Current : -I_s [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

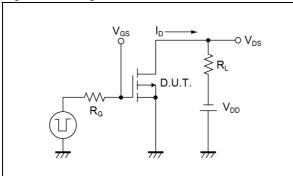


Fig.2-1 Gate Charge Measurement Circuit

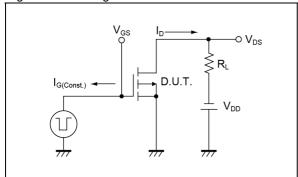


Fig.1-2 Switching Waveforms

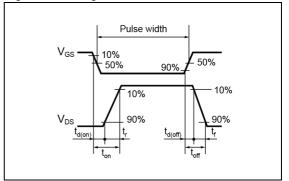
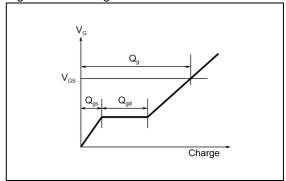
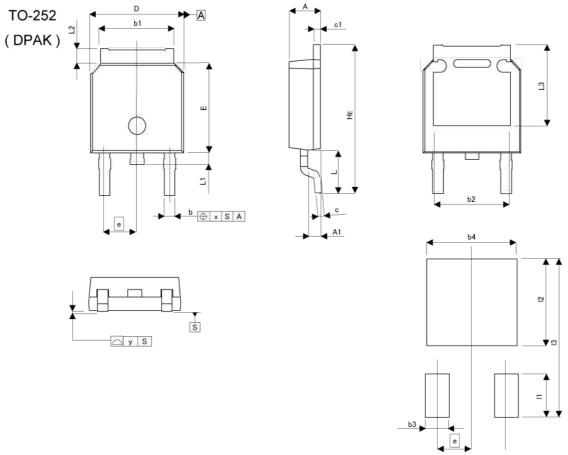


Fig.2-2 Gate Charge Waveform



ullet Dimensions (TL)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

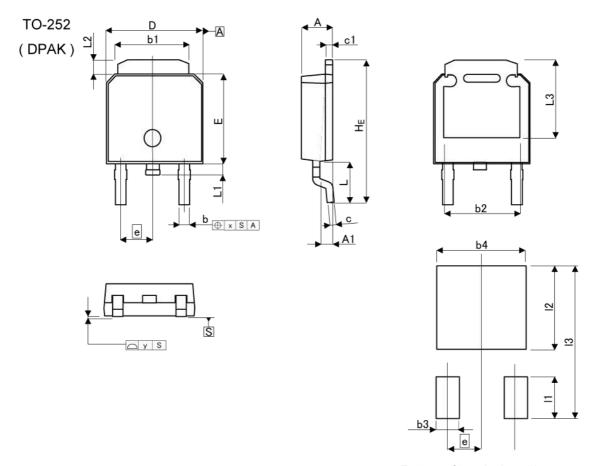
DIM -	MILIME	ETERS	INC	HES	
DIIVI	MIN	MAX	MIN	MAX	
Α	2.10	2.30	0.083	0.091	
A1	0.70	1.10	0.028	0.043	
b	0.65	0.85	0.026	0.033	
b1	5.10	5.40	0.201	0.213	
b2	5.	10	0.2	201	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.	30	0.091		
E	6.00	6.40	0.236	0.252	
HE	9.50	10.50	0.374	0.413	
L	2.	90	0.114		
L1	0.70	0.90	0.028	0.035	
L2	0.70	1.30	0.028	0.051	
L3	5.30		0.209		
х	-	0.10	161	0.004	
у	-	0.10	-	0.004	

DIM	MILIM	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
b3	₽	1.10	622	0.043
b4	*	5.40	5 .4 5	0.213
I1 .	<u> </u>	2.90	72	0.114
12	*	5.50	5.0	0.217
13	<u>s</u>	10.50	V21	0.413

Dimension in mm/inches



● Dimensions (TL1)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIME	ETERS	INCI	HES	
ואווט	MIN	MAX	MIN	MAX	
Α	2.20	2.40	0.087	0.094	
A1	0.70	1.10	0.028	0.043	
b	0.60	0.90	0.024	0.035	
b1	5.20	5.50	0.205	0.217	
b2	4.	80	0.1	89	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.30		0.091		
E	6.00	6.40	0.236	0.252	
HE	9.40	10.40	0.370	0.409	
L	2.	90	0.114		
L1	0.60	1.00	0.024	0.039	
L2	0.70	1.30	0.028	0.051	
L3	5.	30	0.209		
Х		0.25	S (#)	0.010	
у	8	0.10	(5)	0.004	
- T	MILIME	ETERS	INCI	HES	
DIM	MIN	MAX	MIN	MAX	
b3	-	1.15	948	0.045	
b4		5.55	0.50	0.219	
11	-	2.77	S (#1)	0.109	
12	7.	5.50	(5)(0.217	
13	#	10.40	7E0	0.409	

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
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CLASSIV	CLASSIII	CLASSIII	CLASSIII

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
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 - [h] Use of the Products in places subject to dew condensation
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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