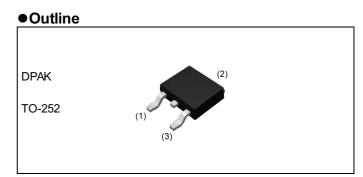


RD3L050SN

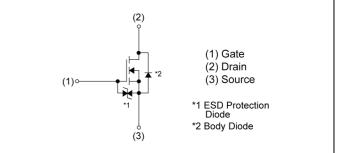
V <sub>DSS</sub>	60V
R <sub>DS(on)</sub> (Max.)	109mΩ
Ι <sub>D</sub>	±5.0A
P <sub>D</sub>	15W

## Features

- 1) Low on resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Parallel use is easy
- 5) Pb-free lead plating ; RoHS compliant



#### ●Inner circuit



# Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
_	Tape width (mm)	16
Туре	Basic ordering unit (pcs)	2500
	Taning and a	TL
	Taping code	TL1
	Marking	RD3L050SN

## Switching

Application

## • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	60	V
Continuous drain current	I <sub>D</sub> *1	±5.0	А
Pulsed drain current	I <sub>DP</sub> *2	±15	А
Gate - Source voltage	V <sub>GSS</sub>	±20	V
Power dissipation	P <sub>D</sub> *3	15	W
Junction temperature	Tj	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## •Thermal resistance

Deremeter	Sumbol		Values		Unit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}^{*3}$	-	-	8.33	°C/W

## •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Currente e l	Canditiana	Values			Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	63.7	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	-	-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$	V <sub>DS</sub> = 10V , I <sub>D</sub> = 1mA	1.0	-	3.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-4.4	-	mV/°C	
		V <sub>GS</sub> = 10V, I <sub>D</sub> = 5.0A	-	78	109		
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 5.0A	-	94	131	mΩ	
		V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 5.0A	-	100	140		
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain	-	6.3	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>   <sup>*4</sup>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 5.0A	3.5	-	-	S	

\*1 Limited only by maximum temperature allowed.

\*2 Pw $\leq$ 10µs , Duty cycle $\leq$ 1%

\*3 T<sub>C</sub>=25°C

\*4 Pulsed



# • Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Sumphal	Conditions		Values		Lincit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	290	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10V	-	90	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	35	-	
Turn - on delay time	$t_{d(on)}^{*4}$	$V_{DD} \simeq 30V, V_{GS}$ = 10V	-	8	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 2.5A	-	17	-	20
Turn - off delay time	$t_{d(off)}^{*4}$	$R_L \simeq 12\Omega$	-	26	-	ns
Fall time	$t_{f}^{*4}$	R <sub>G</sub> = 10Ω	-	8	-	

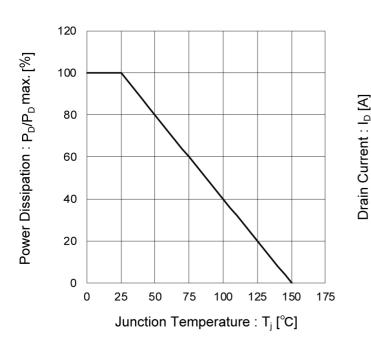
## • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Symbol	Conditions		Values		Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*4}$	V <sub>DD</sub> ≃ 30V,	-	8.0	-	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 5.0A,	-	1.4	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	V <sub>GS</sub> = 10V	-	1.4	-	

# •Body diode electrical characteristics (Source-Drain) ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	Conditions	Values			Unit
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ <sub>S</sub> *1	$T = 25^{\circ}$	-	-	5.0	А
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25°C	-	-	15	А
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 5.0A	-	-	1.2	V





## Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

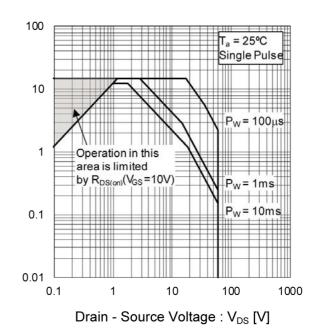
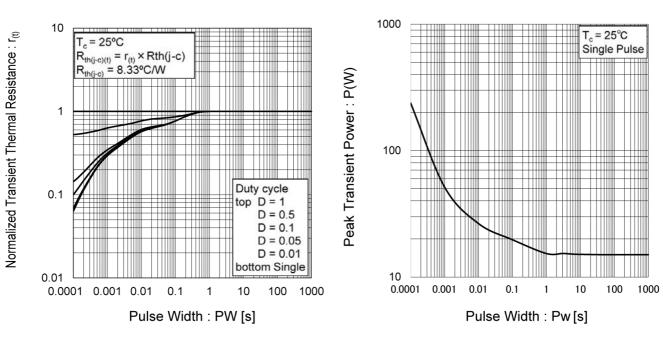


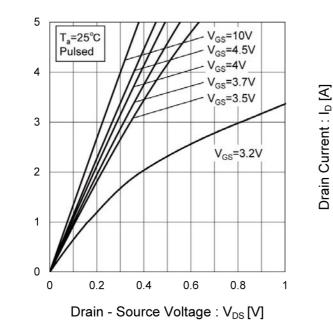
Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power dissipation





Drain Current : I<sub>D</sub> [A]



## Fig.5 Typical Output Characteristics(I)

Fig.6 Typical Output Characteristics(II)

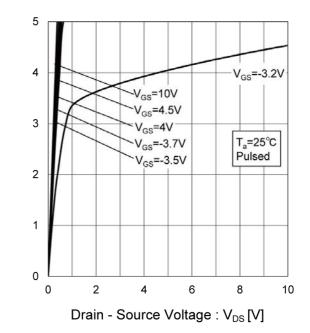


Fig.7 Breakdown Voltage vs. Junction Temperature

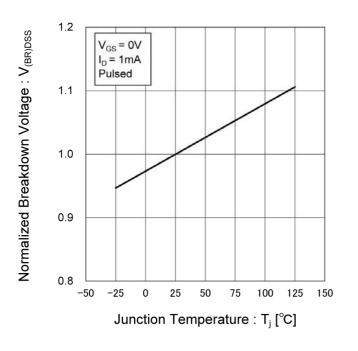
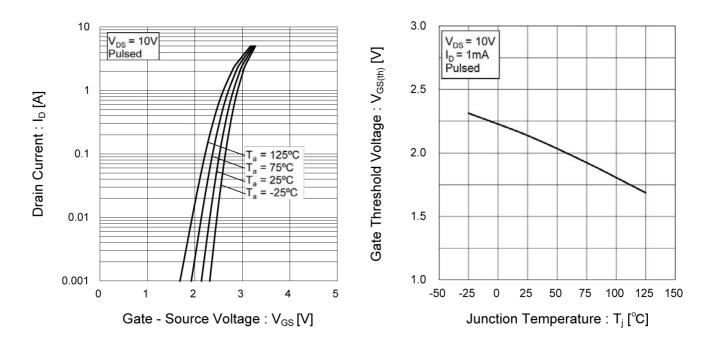


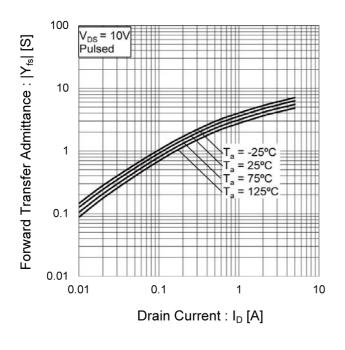




Fig.9 Gate Threshold Voltage vs. Junction Temperature



## Fig.10 Forward Transfer Admittance vs. Drain Current





## • Electrical characteristic curves

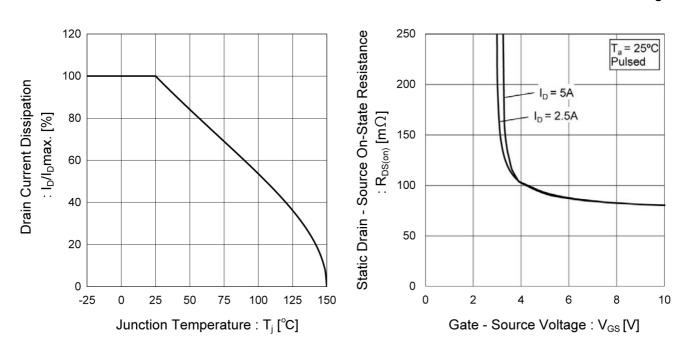
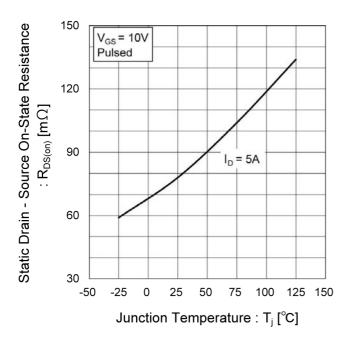


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature





## Electrical characteristic curves

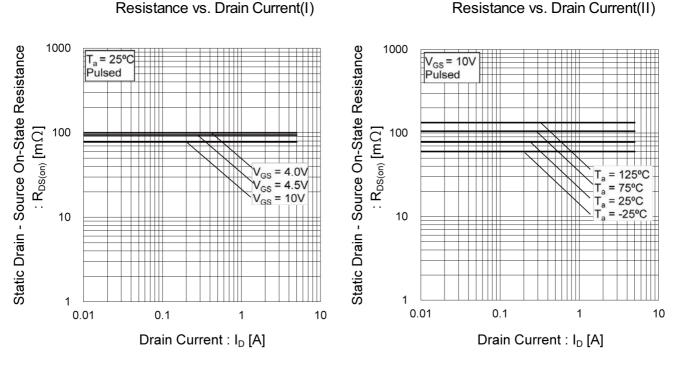


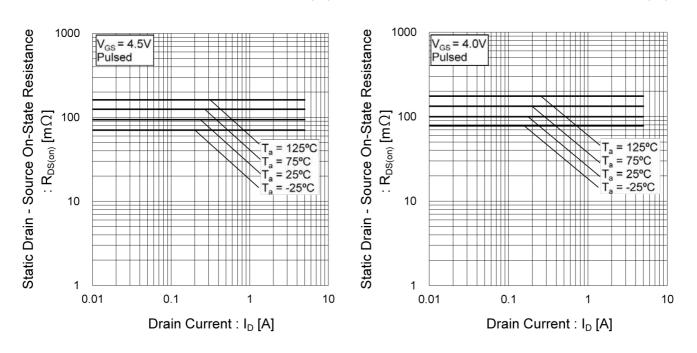
Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

Fig.16 Static Drain - Source On - State

Resistance vs. Drain Current(III)

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

Fig.15 Static Drain - Source On - State



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## • Electrical characteristic curves

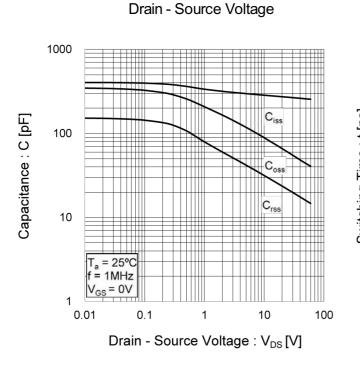


Fig.18 Typical Capacitance vs.

#### Fig.19 Switching Characteristics

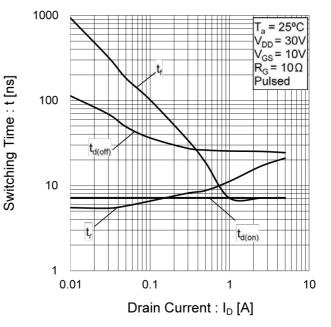


Fig.20 Dynamic Input Characteristics

Gate - Source Voltage : V<sub>GS</sub> [V]

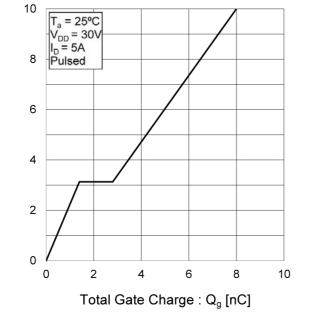
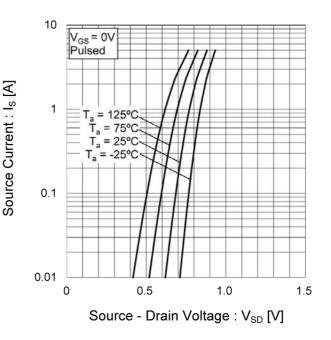


Fig.21 Source Current vs. Source Drain Voltage





#### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

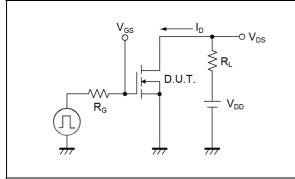


Fig.2-1 Gate Charge Measurement Circuit

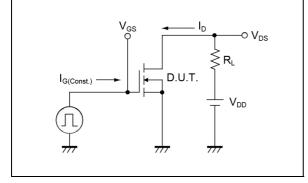
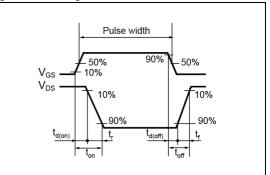
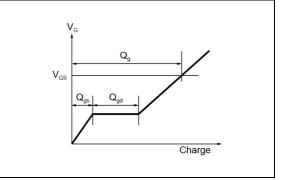


Fig.1-2 Switching Waveforms

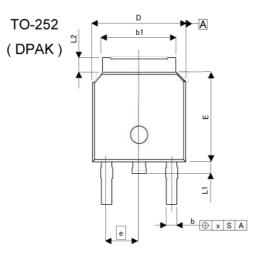


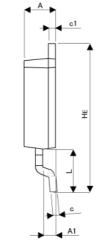


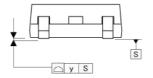


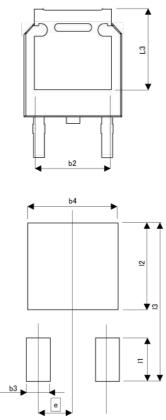


## • Dimensions (TL)









Pattern of terminal position areas [Not a recommended pattern of soldering pads]

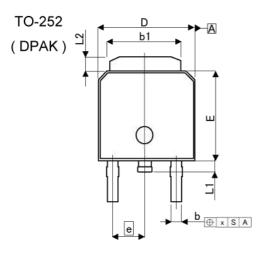
DIM	MILIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX
A	2.10	2.30	0.083	0.091
A1	0.70	1.10	0.028	0.043
b	0.65	0.85	0.026	0.033
b1	5.10	5.40	0.201	0.213
b2	5.	10	0.2	201
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.091	
E	6.00	6.40	0.236	0.252
HE	9.50	10.50	0.374	0.413
L	2.	90	0.114	
L1	0.70	0.90	0.028	0.035
L2	0.70	1.30	0.028	0.051
L3	5.	30	0.2	209
х	-	0.10	14	0.004
y	-	0.10		0.004

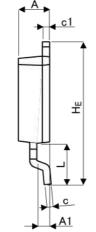
DIM -	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b3	<u>8</u>	1.10	64 <u>28</u>	0.043
b4	π.	5.40	1. <del></del>	0.213
11	<u>2</u>	2.90	7 <u>6</u> 1	0.114
12		5.50	(. <del></del> )	0.217
13	<u>15</u>	10.50	021	0.413

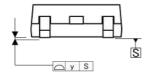
Dimension in mm/inches

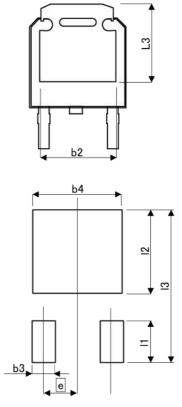


## • Dimensions (TL1)









Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	ETERS	INC	HES
	MIN	MAX	MIN	MAX
A	2.20	2.40	0.087	0.094
A1	0.70	1.10	0.028	0.043
b	0.60	0.90	0.024	0.035
b1	5.20	5.50	0.205	0.217
b2	4.	80	0.1	89
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
e	2.	30	0.0	)91
E	6.00	6.40	0.236	0.252
HE	9.40	10.40	0.370	0.409
L	2.	90	0.114	
L1	0.60	1.00	0.024	0.039
L2	0.70	1.30	0.028	0.051
L3	5.	30	0.209	
x	¥ )	0.25		0.010
у		0.10	(7)	0.004
DIM -	MILIME	TERS	INCHES	
	MIN	MAX	MIN	MAX
b3	× j	1.15	(#4)	0.045
b4	-	5.55	6751	0.219
11	¥ )	2.77	100 (NR)	0.109
12	5	5.50	(E))	0.217
13	÷: []	10.40	260	0.409

Dimension in mm/inches



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  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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