## Nch 60V 80A Power MOSFET

V <sub>DSS</sub>	60V
R <sub>DS(on)</sub> (Max.)	5.5mΩ
I <sub>D</sub>	±80A
P <sub>D</sub>	119W

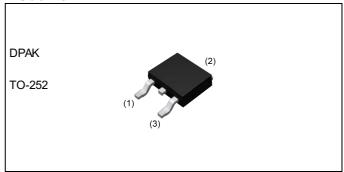
# ● Features

- 1) Low on resistance
- 2) High power small mold package (TO-252)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen free
- 5) 100% Rg and UIS tested

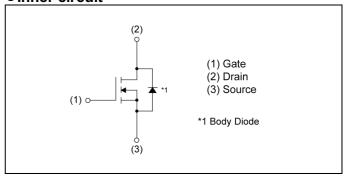
## Application

Switching

### Outline



## •Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Type	Tape width (mm)	16
-	Quantity (pcs)	2500
	Taping code	TL
	Marking	RD3L08BGN

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V <sub>DSS</sub>	60	V
Continuous drain current	V <sub>GS</sub> = 10V	I <sub>D</sub> *1	±80	Α
Pulsed drain current	I <sub>DP</sub> *2	±160	Α	
Gate - Source voltage	V <sub>GSS</sub>	±20	V	
Avalanche current, single pulse	I <sub>AS</sub> *3	40	Α	
Avalanche energy, single pulse	E <sub>AS</sub> *3	60	mJ	
Power dissipation	P <sub>D</sub> *1	119	W	
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage tel	T <sub>stg</sub>	-55 to +150	°C	

## ●Thermal resistance

Parameter	Symbol	Values			Lloit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *1	-	-	1.05	°C/W

## ● Electrical characteristics (T<sub>a</sub> = 25°C)

Doromotor	Symbol	Conditions		Values			
Parameter	Symbol	Symbol		Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_i} I_D = 1 \text{mA}$ referenced to 25°C		60	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 60V, V <sub>GS</sub> = 0V	-	-	10	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$	1	-	±500	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 100 \mu A$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-5.6	-	mV/°C	
Static drain - source	D *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 80A	-	4.2	5.5	m0	
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 40A	-	5.7	8.1	mΩ	
Gate resistance	$R_G$	f = 1MHz, open drain	ı	1.6	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 5V, I <sub>D</sub> = 40A	30	-	-	S	

<sup>\*1</sup> T<sub>c</sub> =25°C, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw≦10µs , Duty cycle≦1%

<sup>\*3</sup> L  $\simeq$  0.05mH, V<sub>DD</sub> = 30V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Dorameter	Cumbal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	3620	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 30V	-	710	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	1	180	1		
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 30V, V_{GS} = 10V$	1	28	1		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 40A	-	36	-		
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L \simeq 0.75\Omega$	-	99	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	150	-		

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

Daramatar	Cymahal	Canditions		Values			l limit
Parameter	Symbol Conditions		ions	Min.	Тур.	Max.	Unit
Total gate charge	O *4		V <sub>GS</sub> = 10V	-	71.0	-	
Total gate charge	Q <sub>g</sub> *4	$V_{DD} \simeq 30V$		-	38.0	-	~C
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = 40A	V <sub>GS</sub> = 4.5V	-	15.3	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4			-	13.6	-	

## ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Cumbal	Conditions	Values			Unit	
Parameter			Min.	Тур.	Max.	Offic	
Continuous forward current	I <sub>S</sub>	T = 25°C	1	-	80	Α	
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25°C	1	-	160	Α	
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 80A	-	-	1.2	V	
Reverse recovery time	t <sub>rr</sub> *4	I <sub>S</sub> = 50A, V <sub>GS</sub> =0V	-	48	-	ns	
Reverse recovery charge	Q <sub>rr</sub> *4	di/dt = 100A/μs	-	68	-	nC	

Fig.1 Power Dissipation Derating Curve

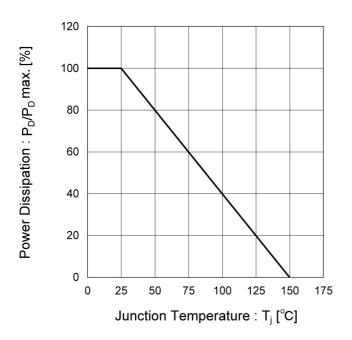
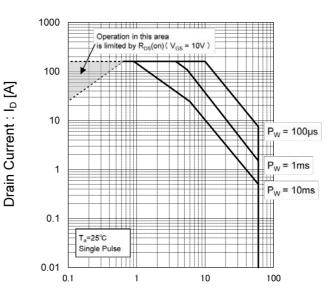


Fig.2 Maximum Safe Operating Area



Drain - Source Voltage: V<sub>DS</sub>[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

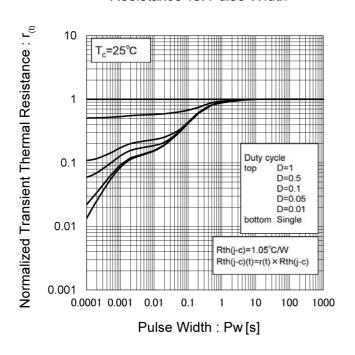


Fig.4 Single Pulse Maximum Power dissipation

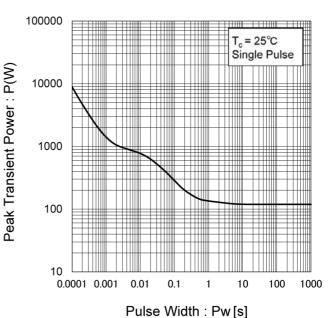
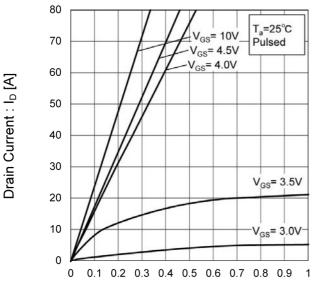
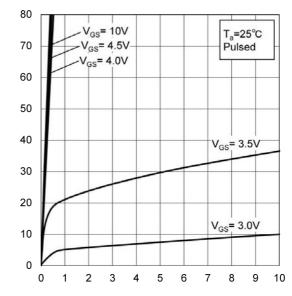


Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs.

Junction Temperature

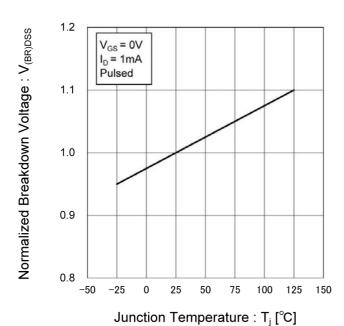


Fig.8 Typical Transfer Characteristics

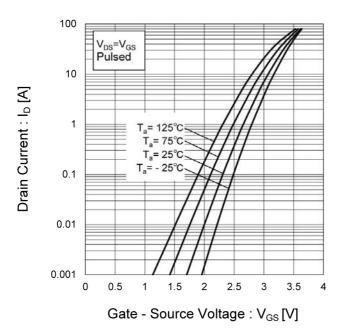


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

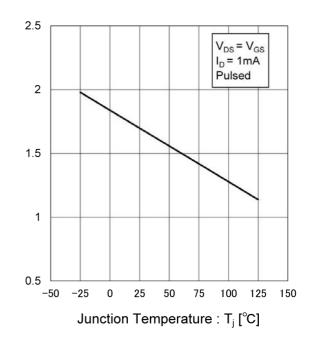
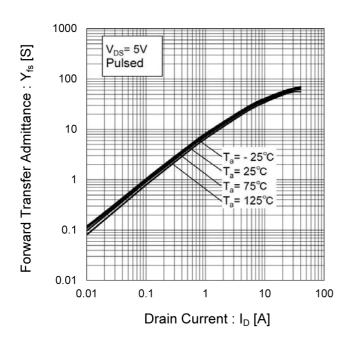


Fig.10 Forward Transfer Admittance vs.
Drain Current



Gate Threshold Voltage: V<sub>GS(th)</sub> [V]

Fig.11 Drain Current Derating Curve

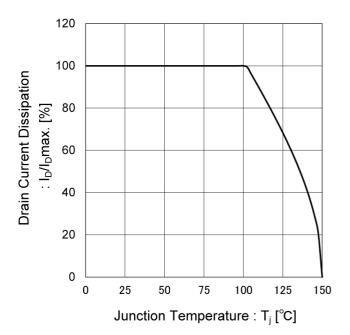


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

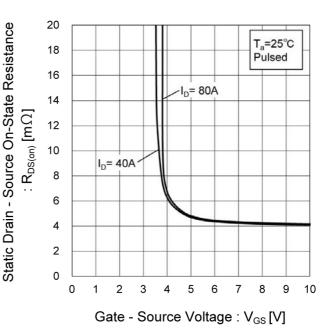


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

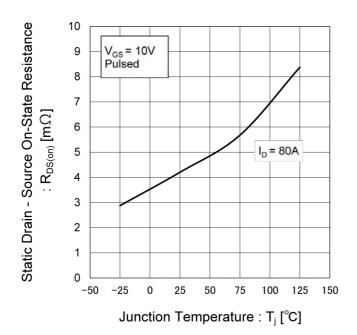


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

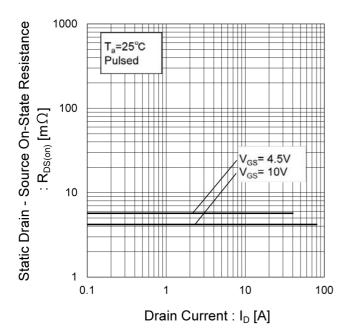


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

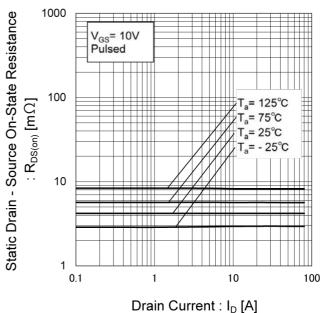


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

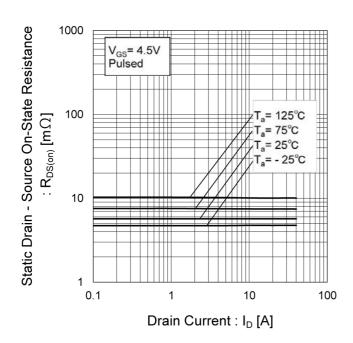


Fig.17 Typical Capacitance vs.

Drain - Source Voltage

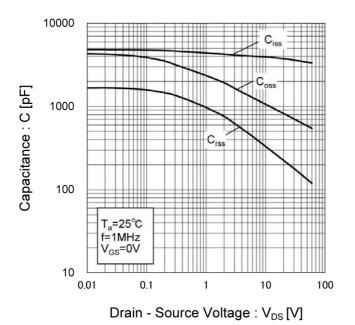


Fig.18 Switching Characteristics

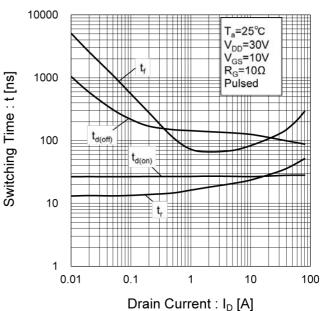


Fig.19 Dynamic Input Characteristics

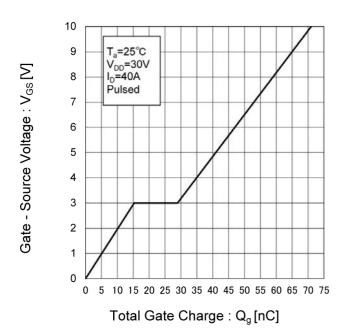
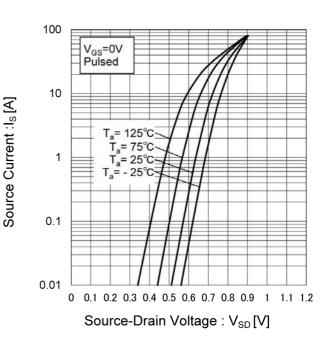


Fig.20 Source Current vs.

Source Drain Voltage



## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

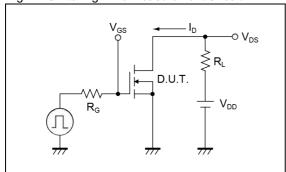


Fig.2-1 Gate Charge Measurement Circuit

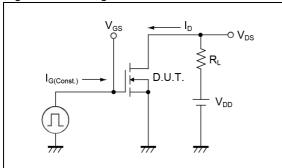


Fig.3-1 Avalanche Measurement Circuit

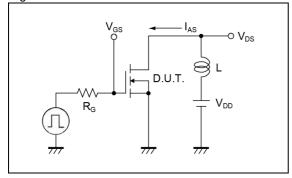


Fig.1-2 Switching Waveforms

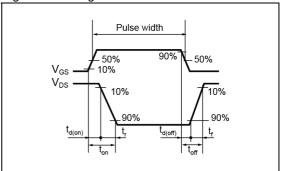


Fig.2-2 Gate Charge Waveform

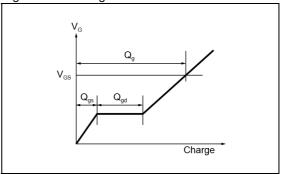
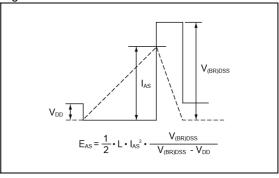
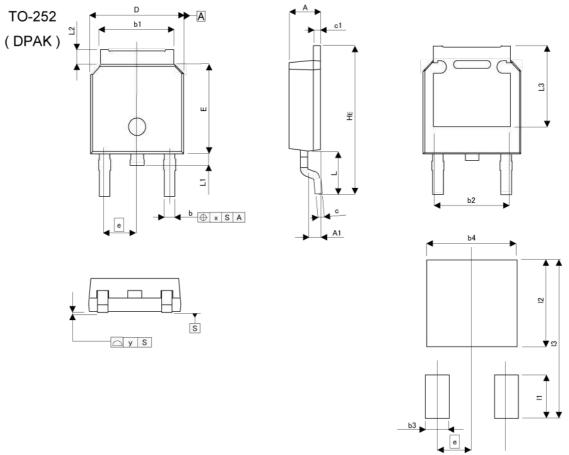


Fig.3-2 Avalanche Waveform



## Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	2.10	2.30	0.083	0.091
A1	0.70	1.10	0.028	0.043
b	0.65	0.85	0.026	0.033
b1	5.10	5.40	0.201	0.213
b2	5.	10	0.2	201
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.091	
E	6.00	6.40	0.236	0.252
HE	9.50	10.50	0.374	0.413
L	2.	90	0.1	14
L1	0.70	0.90	0.028	0.035
L2	0.70	1.30	0.028	0.051
L3	5.	30	0.2	209
х	-	0.10	160	0.004
у	-	0.10		0.004

DIM MILIMETERS		ETERS	INC	INCHES	
DIIVI	MIN	MAX	MIN	MAX	
b3	<i>≦</i>	1.10	623	0.043	
b4	*	5.40	5,41	0.213	
11	2:	2.90	72	0.114	
12	*	5.50	5.00	0.217	
13	<b>≅</b>	10.50	021	0.413	

Dimension in mm/inches



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