Nch 100V 17.5A Power MOSFET

V _{DSS}	100V
R _{DS(on)} (Max.)	105mΩ
I _D	±17.5A
P_D	20W

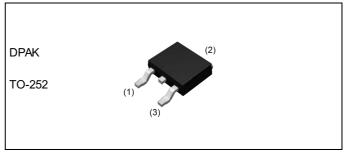
● Features

- 1) Low on resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Parallel use is easy
- 5) Pb-free lead plating; RoHS compliant

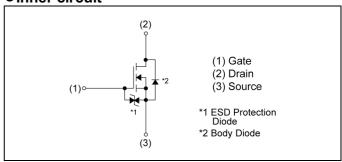
Application

Switching

Outline



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
_	Tape width (mm)	16
Туре	Basic ordering unit (pcs)	2500
	Taning and	TL
	Taping code	TL1
	Marking	RD3P175SN

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	100	V
Continuous drain current	I _D *1	±17.5	А
Pulsed drain current	I _{DP} *2	±35	Α
Gate - Source voltage	V _{GSS}	±20	V
Power dissipation	P _D *3	20	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Cymbal	Values			l leit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *3	-	-	6.25	°C/W

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	100	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	116.9	-	mV/°C
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	-	1	±10	μA
Gate threshold voltage	$V_{GS(th)}$	V _{DS} = 10V , I _D = 1mA	1.0	1	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	-3.6	-	mV/°C
		V _{GS} = 10V, I _D = 8.8A	-	75	105	
Static drain - source on - state resistance	R _{DS(on)} *4	$V_{GS} = 4.5V, I_D = 8.8A$	-	80	112	mΩ
		$V_{GS} = 4V, I_D = 8.8A$	-	85	119	
Gate resistance R _G f = 1MHz, open drain		-	6.0	1	Ω	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 10V, I _D = 8.8A	5	-	-	S

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*3} T_C=25°C

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

Daramatar	Cymah al	Conditions	Values			l lm:t	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	950	-		
Output capacitance	C _{oss}	V _{DS} = 25V	-	85	-	pF	
Reverse transfer capacitance C _{rss}		f = 1MHz	-	55	-		
Turn - on delay time	t _{d(on)} *4	V _{DD} ≈ 50V,V _{GS} = 10V	-	10	-		
Rise time	t _r *4	I _D = 8.8A	-	25	-	no	
Turn - off delay time	t _{d(off)} *4	$R_L \simeq 5.7\Omega$	-	60	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	50	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ u	,				
Darameter	Symbol	Conditions	Values			l leit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg*4	V _{DD} ≃ 50V.	-	24	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \simeq 50V$, $I_D = 17.5A$,	-	3	-	nC
Gate - Drain charge	Q _{gd} *4	V _{GS} = 10V	-	6	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Darameter	Symbol Conditions		Values			l leit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	I _S *1	T = 25°C	-	-	17.5	Α	
Pulse forward current	I _{SP} *2	T _a = 25°C	-	-	35	Α	
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 17.5A	-	-	1.5	V	

Fig.1 Power Dissipation Derating Curve

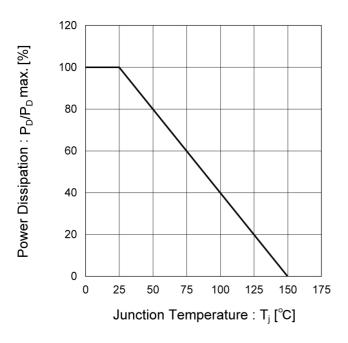


Fig.2 Maximum Safe Operating Area

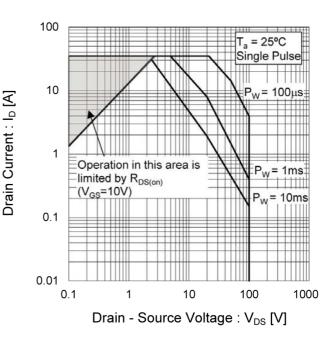


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

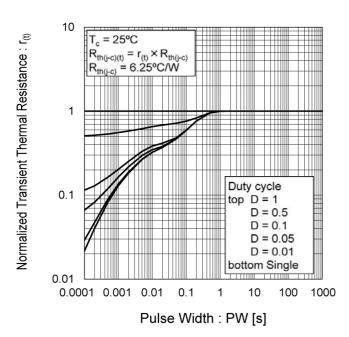


Fig.4 Single Pulse Maximum Power dissipation

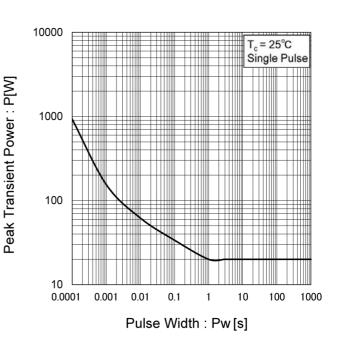
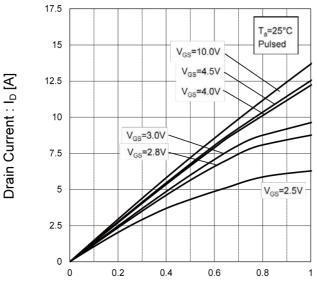
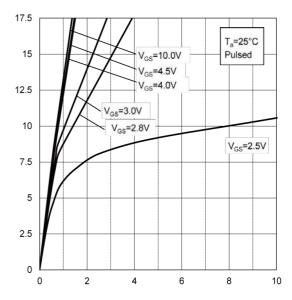


Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage: V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

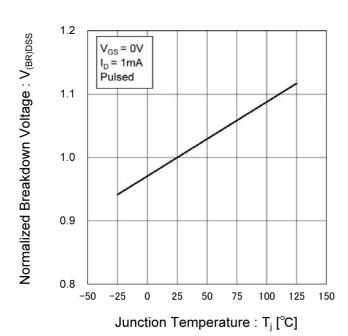


Fig.8 Typical Transfer Characteristics

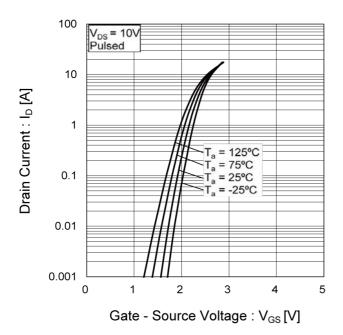


Fig.9 Gate Threshold Voltage vs.

Junction Temperature

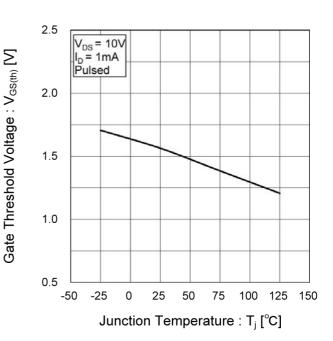
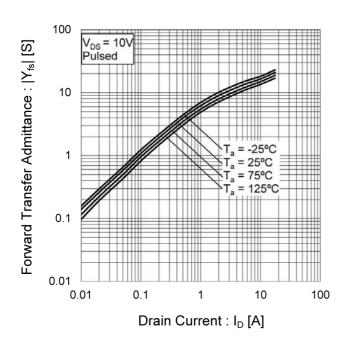


Fig.10 Forward Transfer Admittance vs.
Drain Current



6/12

Fig.11 Drain Current Derating Curve

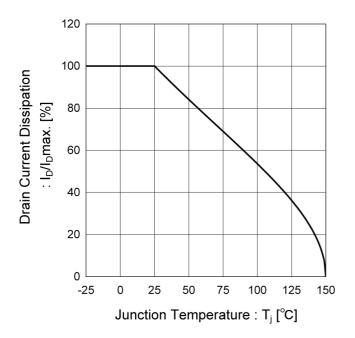


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

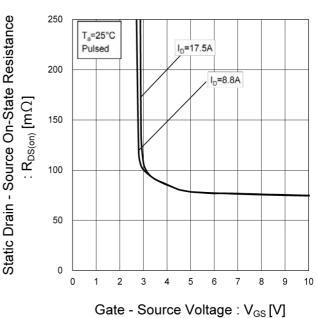
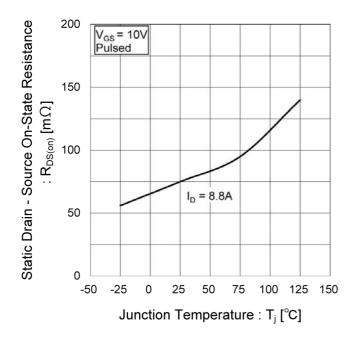


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



4/12

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

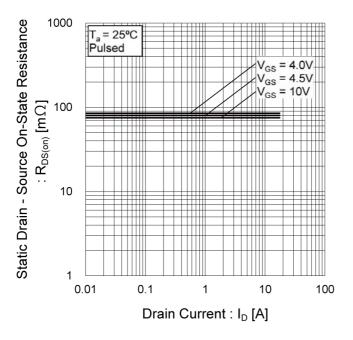


Fig.15 Static Drain - Source On - State
Resistance vs. Drain Current(II)

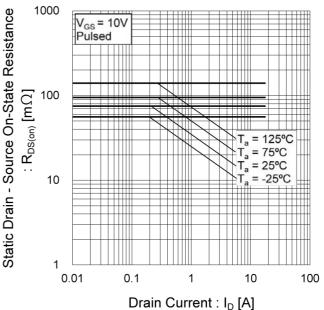


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

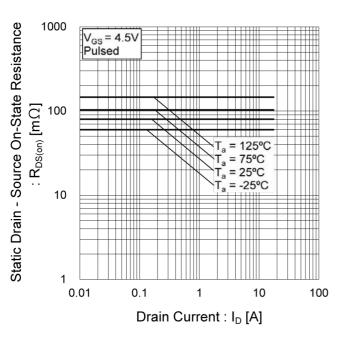


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

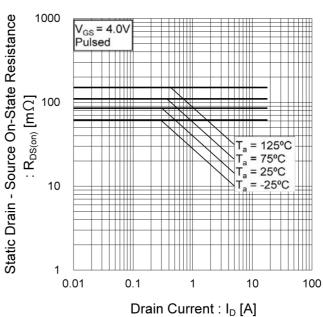
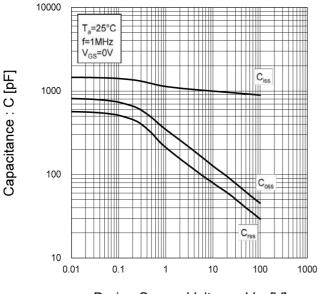


Fig.18 Typical Capacitance vs.

Drain - Source Voltage



Drain - Source Voltage : V_{DS} [V]

Fig.19 Switching Characteristics

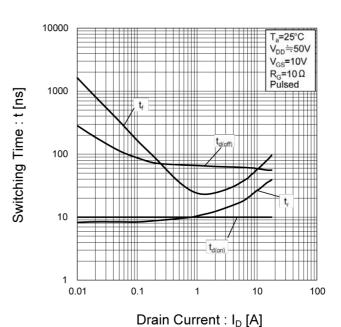
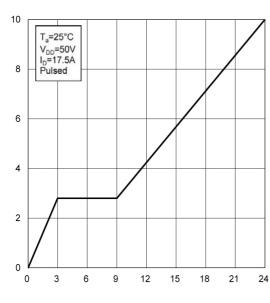


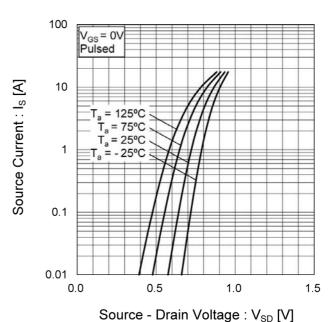
Fig.20 Dynamic Input Characteristics



Total Gate Charge: Qq [nC]

Fig.21 Source Current vs.

Source Drain Voltage



Gate - Source Voltage : V_{GS} [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

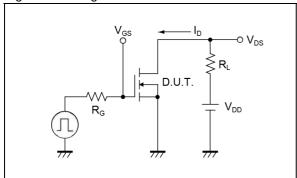


Fig.2-1 Gate Charge Measurement Circuit

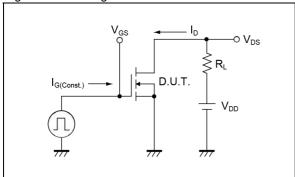


Fig.1-2 Switching Waveforms

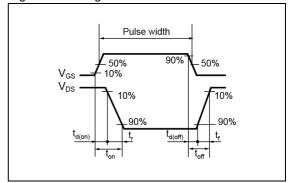
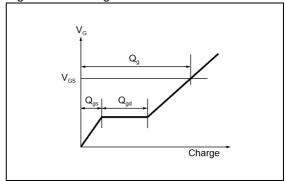
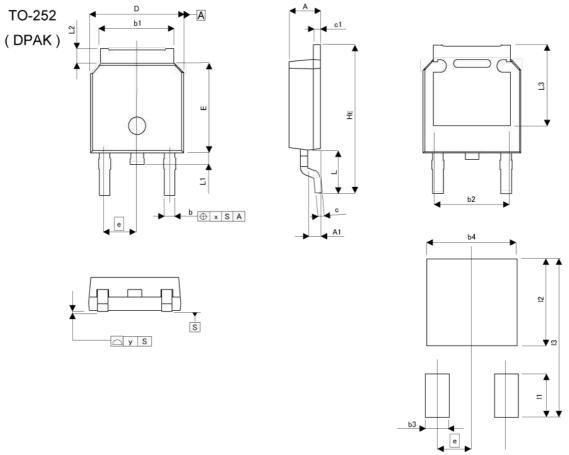


Fig.2-2 Gate Charge Waveform



ullet Dimensions (TL)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

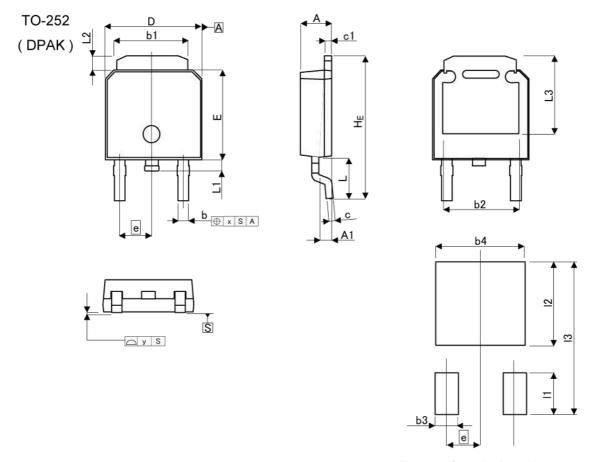
DIM -	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	2.10	2.30	0.083	0.091
A1	0.70	1.10	0.028	0.043
b	0.65	0.85	0.026	0.033
b1	5.10	5.40	0.201	0.213
b2	5.	10	0.2	201
С	0.40	0.60	0.016	0.024
c1	0.40	0.60	0.016	0.024
D	6.40	6.80	0.252	0.268
е	2.	30	0.091	
E	6.00	6.40	0.236	0.252
HE	9.50	10.50	0.374	0.413
L	2.	90	0.114	
L1	0.70	0.90	0.028	0.035
L2	0.70	1.30	0.028	0.051
L3	5.30		0.209	
Х	-	0.10	160	0.004
У	-	0.10	-	0.004

DIM	MILIM	ETERS	INCHES	
DIIVI	MIN	MAX	MIN	MAX
b3	₽	1.10	623	0.043
b4	*	5.40	5.41	0.213
I1 .	<u> </u>	2.90	72	0.114
12	*	5.50	5.00	0.217
13	2	10.50	021	0.413

Dimension in mm/inches



● Dimensions (TL1)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIME	ETERS	INCHES		
ן ואוט	MIN	MAX	MIN	MAX	
Α	2.20	2.40	0.087	0.094	
A1	0.70	1.10	0.028	0.043	
b	0.60	0.90	0.024	0.035	
b1	5.20	5.50	0.205	0.217	
b2	4.	80	0.1	89	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.30		0.0	91	
E	6.00	6.40	0.236	0.252	
HE	9.40	10.40	0.370	0.409	
L	2.	90	0.114		
L1	0.60	1.00	0.024	0.039	
L2	0.70	1.30	0.028	0.051	
L3	5.	30	0.209		
Х		0.25		0.010	
у	8	0.10	(5)	0.004	
DIA .	MILIME	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
b3	-	1.15	S#40	0.045	
b4	-	5.55	0.530	0.219	
11	=)	2.77	S (#3)	0.109	
12	8	5.50	(8)	0.217	
13	2	10.40	2 4 0	0.409	

Dimension in mm/inches



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CLASSIV	CLASSIII	CLASSIII	CLASSIII

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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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