

Nch 250V 6A Power MOSFET

V_{DSS}	250V
R _{DS(on)} (Max.)	530mΩ
I _D	±6A
P _D	52W

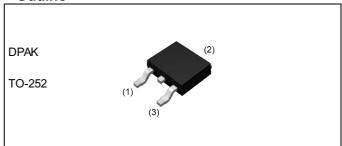
●Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Drive circuits can be simple
- 4) Parallel use is easy
- 5) Pb-free plating; RoHS compliant

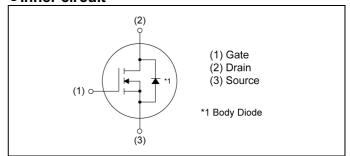
Application

Switching Power Supply

Outline



•Inner circuit



Packaging specifications

Туре	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	16
	Quantity (pcs)	2500
	Taping code	TL1
	Marking	RD3U060CN

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

	==	· · · · · · · · · · · · · · · · · · ·		
Parameter	Symbol	Value	Unit	
Drain - Source voltage		V_{DSS}	250	V
Caratina carra dunina accumant	T _c = 25°C	I _D *1	±6	Α
Continuous drain current	T _c = 100°C	I _D *1	±3.3	Α
Pulsed drain current	I _{DP} *2	±24	Α	
Gate - Source voltage		V_{GSS}	±30	V
Avalanche energy, single pulse		E _{AS} *3	2.62	mJ
Avalanche current, single pulse		I _{AS} *3	3	Α
Power dissipation (T _c = 25°C)		P _D	52	W
Junction temperature		T _j	150	°C
Operating junction and storage te	T _{stg}	-55 to +150	°C	

●Thermal resistance

Doromotor	Cymab al	Values			l limit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	2.36	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymah al	Canditions	Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	250	-	-	V	
Zero gate voltage drain current	I _{DSS}	I_{DSS} $V_{DS} = 250V, V_{GS} = 0V$ $T_j = 25^{\circ}C$		-	10	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	V _{DS} = 10V, I _D = 1mA	3.0	1	5.0	V	
Static drain - source on - state resistance	R _{DS(on)} *4	V _{GS} = 10V, I _D = 3A	-	410	530	mΩ	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 10V, I _D = 3.75A	2.2	4.4	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10µs, Duty cycle \leq 1%

^{*3} L \simeq 500 μ H, V_{DD} = 50V, R_G = 25 Ω , starting T_j = 25°C

^{*4} Pulsed

● Electrical characteristics (T_a = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Parameter Symbol Conditions		Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	840	-	
Output capacitance	C _{oss}	V _{DS} = 25V	-	50	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	25	-	
Turn - on delay time	$t_{d(on)}^{*4}$ $V_{DD} \approx 100V, V_{GS} = 10V$		-	22	-	
Rise time	t _r *4	I _D = 3A	-	20	-	no
Turn - off delay time	t _{d(off)} *4	R _L ≃ 41.67Ω	-	30	-	ns
Fall time	t _f *4	$R_G = 10\Omega$	_	13	-	

● Gate charge characteristics (T_a = 25°C)

Davamatar	Cymah al	Conditions	Values			Unit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*4}	V _{DD} ≃ 125V	-	15	-	
Gate - Source charge	Q _{gs} *4	I _D = 6A	-	6	-	nC
Gate - Drain charge	Q _{gd} *4	V _{GS} = 10V	-	6	-	
Gate plateau voltage	V _(plateau)	V _{DD} ≈ 125V, I _D = 6A	-	7.2	-	V

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol Conditions		Min.	Тур.	Max.	Offic	
Continuous forward current	I _S *1	T = 25°C	-	-	6	Α	
Pulse forward current	I _{SP} *2	T _C = 25°C	1	1	24	Α	
Forward voltage	V _{SD} *4	$V_{GS} = 0V, I_{S} = 6A$	ı	ı	1.5	V	
Reverse recovery time	t _{rr} *4	I _S = 3.8A	ı	70	1	ns	
Reverse recovery charge	Q _{rr} *4	di/dt = 100A/μs	ı	180	ı	nC	

Fig.1 Power Dissipation Derating Curve

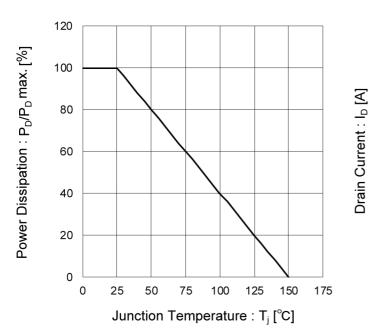


Fig.2 Maximum Safe Operating Area

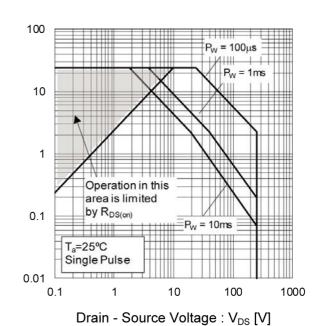
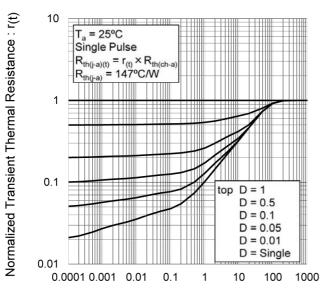


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: Pw [s]

Fig.4 Avalanche Current vs. Inductive Load

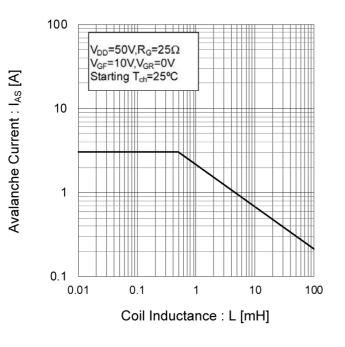


Fig.5 Avalanche Energy Derating Curve vs. Junction Temperature

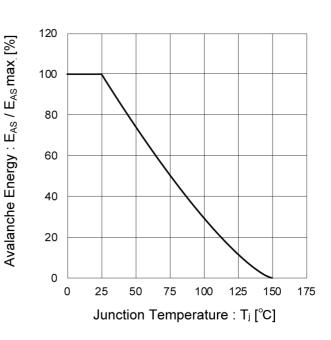
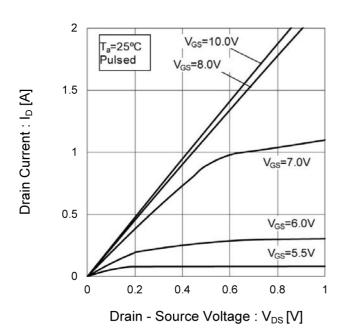


Fig.6 Typical Output Characteristics(I)



Drain Current : I_D [A]

6 T_a=25℃ Pulsed V_{GS}=10.0V 5 V_{GS}=8.0V 4 3 2 V_{GS}=7.0V 1 V_{GS}=6.0V V_{GS}=5.5V 0 2 6 10 Drain - Source Voltage: V_{DS}[V]

Fig.7 Typical Output Characteristics(II)

Fig.8 Breakdown Voltage vs. Junction Temperature

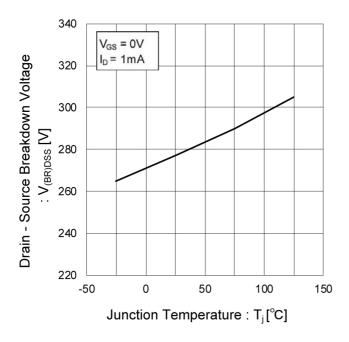


Fig.9 Typical Transfer Characteristics

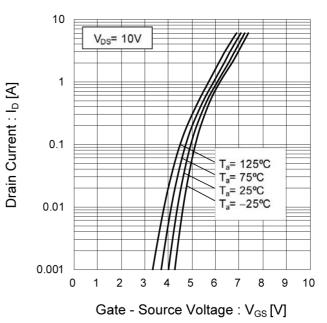


Fig.10 Gate Threshold Voltage vs. Junction Temperature

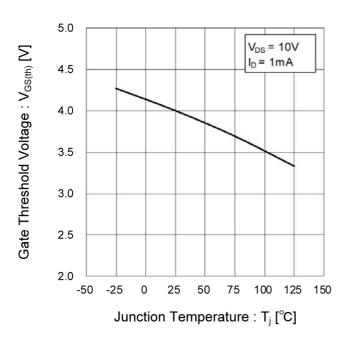


Fig.11 Transconductance vs. Drain Current

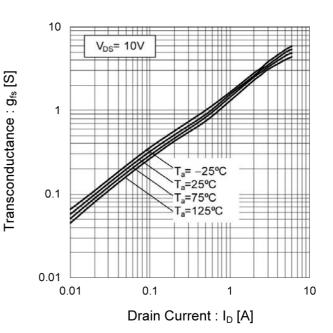


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

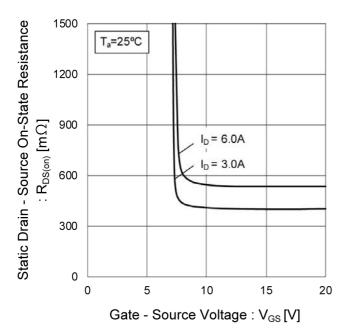


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

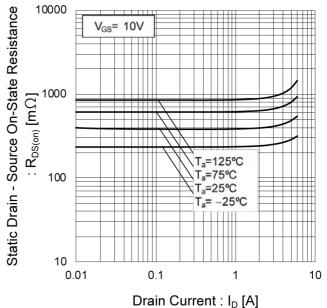


Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature

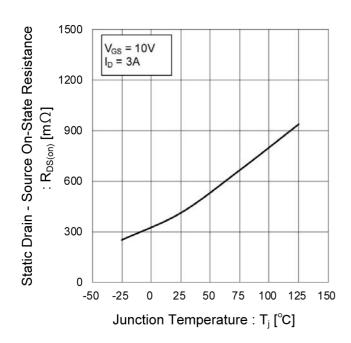


Fig.15 Typical Capacitance vs. Drain - Source Voltage

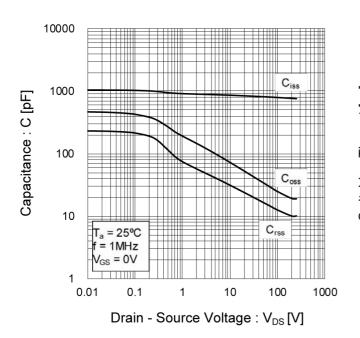


Fig.16 Switching Characteristics

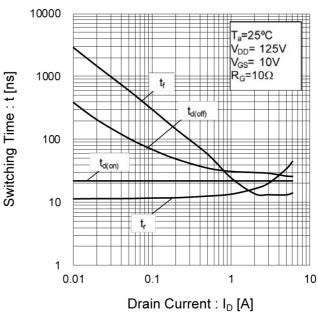
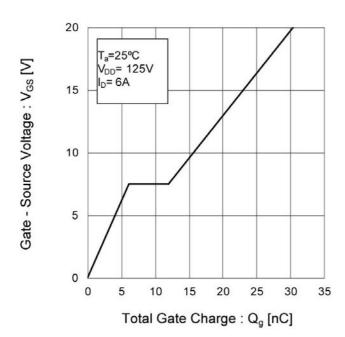


Fig.17 Dynamic Input Characteristics



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Fig.18 Source Current vs. Source-Drain Voltage

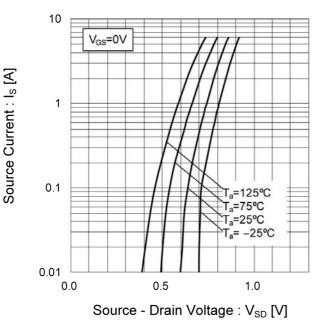
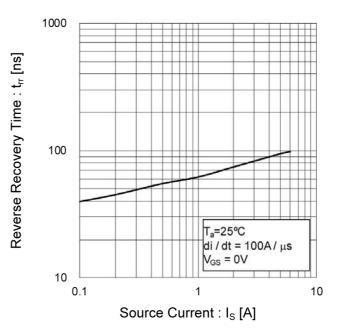


Fig.19 Source Current vs. Reverse Recovery Time



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

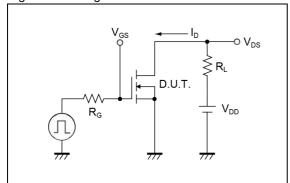


Fig.2-1 Gate Charge Measurement Circuit

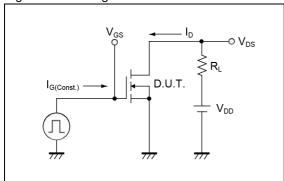


Fig.3-1 Avalanche Measurement Circuit

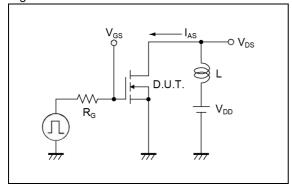


Fig.1-2 Switching Waveforms

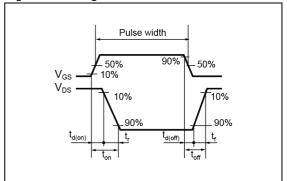


Fig.2-2 Gate Charge Waveform

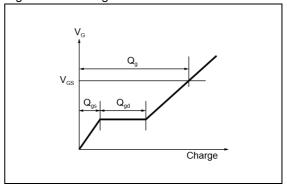
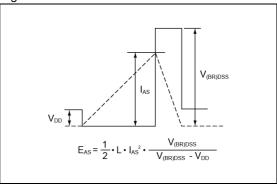
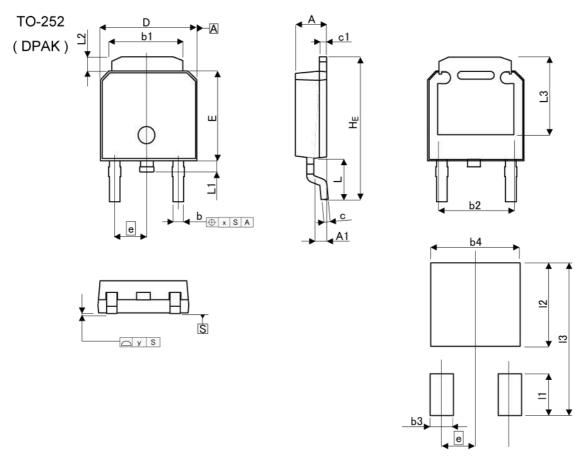


Fig.3-2 Avalanche Waveform



Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM -	MILIME	ETERS	INCHES		
	MIN	MAX	MIN	MAX	
Α	2.20	2.40	0.087	0.094	
A1	0.70	1.10	0.028	0.043	
b	0.60	0.90	0.024	0.035	
b1	5.20	5.50	0.205	0.217	
b2	4.	80	0.1	89	
С	0.40	0.60	0.016	0.024	
c1	0.40	0.60	0.016	0.024	
D	6.40	6.80	0.252	0.268	
е	2.	30	0.0	91	
E	6.00	6.40	0.236	0.252	
HE	9.40	10.40	0.370	0.409	
L	2.	90	0.1	14	
L1	0.60	1.00	0.024	0.039	
L2	0.70	1.30	0.028	0.051	
L3	5.	30	0.209		
Х		0.25		0.010	
у	8	0.10	(5)	0.004	
DIA	MILIME	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
b3	¥	1.15	S#6	0.045	
b4		5.55	0.50	0.219	
11	-	2.77	S 1960	0.109	
12	8	5.50	(5)	0.217	
13	#	10.40	2 <u>26</u> 0	0.409	

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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