## Pch -20V -10A Middle Power MOSFET

V <sub>DSS</sub>	-20V
R <sub>DS(on)</sub> (Max.)	26mΩ
I <sub>D</sub>	±10A
$P_D$	2W

## Features

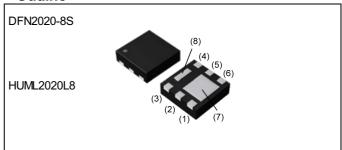
- 1) Low on resistance.
- 2) High Power small mold Package (HUML2020L8).
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

# Application

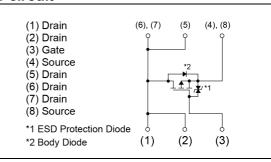
Switching

Load switch

## Outline



## ●Inner circuit



Packaging specifications

	Packing	Embossed Tape
Type	Reel size (mm)	180
	Tape width (mm)	8
-	Quantity (pcs)	3000
	Taping code	TR
	Marking	SJ

## ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-20	V
Continuous drain current	I <sub>D</sub>	±10	А
Pulsed drain current	I <sub>DP</sub> *1	±20	Α
Gate - Source voltage	$V_{GSS}$	0~-8	V
Power dissipation	P <sub>D</sub> *2	2	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

## ●Thermal resistance

Parameter	Cumb of	Values			Lloit
- Farameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R <sub>thJA</sub> *2	-	62.5	-	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Darameter	Cymah al	Canditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = -1mA$	-20	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = -1 \text{mA}$ referenced to 25°C		-9.3	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V	-	-	-10	μΑ	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = -8V, $V_{DS}$ = 0V	-	-	-10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = -10V, I_{D} = -1mA$	-0.3	-	-1.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I <sub>D</sub> = -1mA referenced to 25°C	-	1.8	-	mV/°C	
		$V_{GS} = -4.5V, I_D = -5.0A$	-	18	26		
Static drain - source	D *3	$V_{GS}$ = -2.5V, $I_{D}$ = -2.5A	-	22	31	mΩ	
on - state resistance	R <sub>DS(on)</sub> *3	$V_{GS} = -1.8V, I_D = -2.5A$	1	27	45	11122	
		$V_{GS} = -1.5V, I_D = -1.0A$	-	32	65		
Forward Transfer Admittance	Y <sub>fs</sub>  *3	V <sub>DS</sub> = -10V, I <sub>D</sub> = -5.0A	8	-	-	S	

<sup>\*1</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*2</sup> MOUNTED ON 40mm×40mm Cu BOARD

<sup>\*3</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

Daramatar	Symbol	Conditions	Values			Lloit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	5500	-	_
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	230	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	210	-	
Turn - on delay time	t <sub>d(on)</sub> *3	$V_{DD} \simeq -10V, V_{GS} = -4.5V$	-	16	1	
Rise time	t <sub>r</sub> *3	I <sub>D</sub> = -2.5A	-	16	-	no
Turn - off delay time	t <sub>d(off)</sub> *3	$R_L \simeq 4.02\Omega$	-	580	-	ns
Fall time	t <sub>f</sub> *3	$R_G = 10\Omega$	-	160	-	

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ a	,				
Parameter	Cymbal	Conditions	Values			Unit
raiametei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q <sub>g</sub> *3	V <sub>DD</sub> ≃ -10V.	-	55	-	
Gate - Source charge	Q <sub>gs</sub> *3	$V_{DD} \simeq -10V,$ $I_{D} = -5.0A,$ $V_{GS} = -4.5V$	-	6.4	-	nC
Gate - Drain charge	Q <sub>gd</sub> *3	$V_{GS} = -4.5V$	-	8.4	-	

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Darameter	Symbol	Conditions	Values			Linit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	I <sub>S</sub>	T - 25°C	-	-	-1.6	Α	
Pulse forward current	I <sub>SP</sub> *1	T <sub>a</sub> = 25°C	-	-	-20	Α	
Forward voltage	V <sub>SD</sub> *3	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1.6A	-	-	-1.2	V	

Fig.1 Power Dissipation Derating Curve

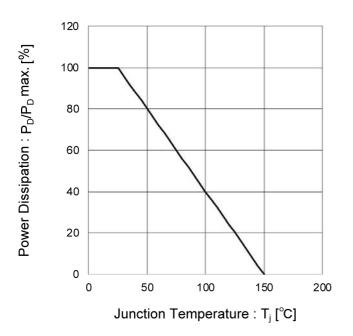
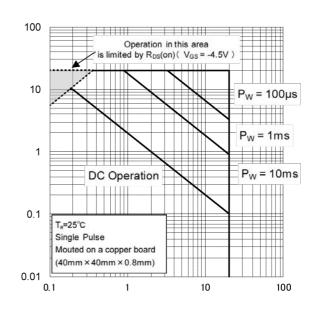


Fig.2 Maximum Safe Operating Area



Drain Current: -l<sub>D</sub> [A]

Drain - Source Voltage : -V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

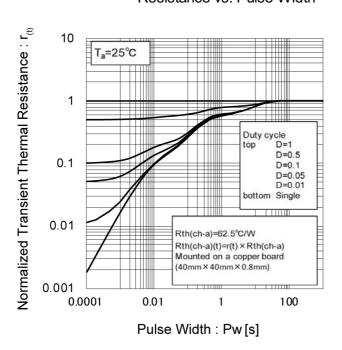
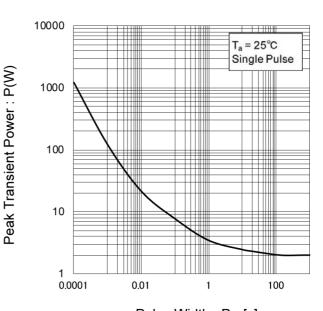


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width : Pw [s]

Drain Current : -I<sub>D</sub> [A]

### • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

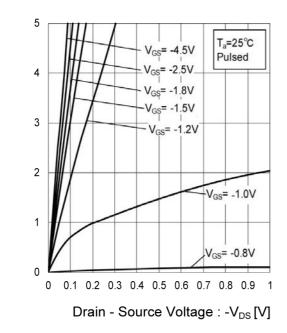
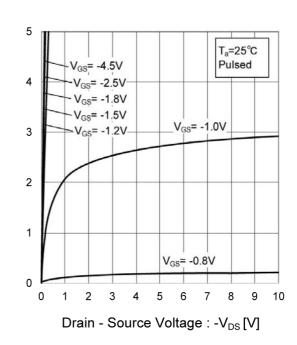


Fig.6 Typical Output Characteristics(II)



Drain Current: -l<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs. Junction Temperature

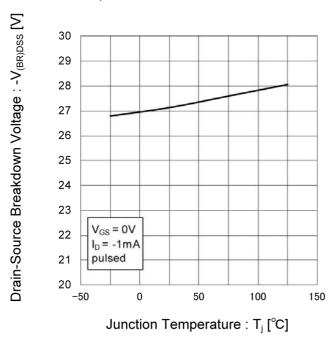


Fig.8 Typical Transfer Characteristics

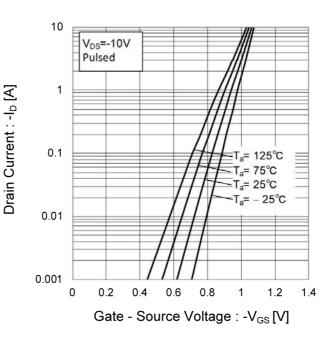


Fig.9 Gate Threshold Voltage vs. Junction Temperature

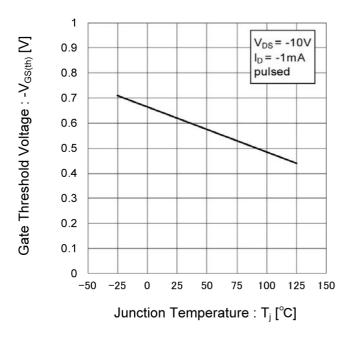
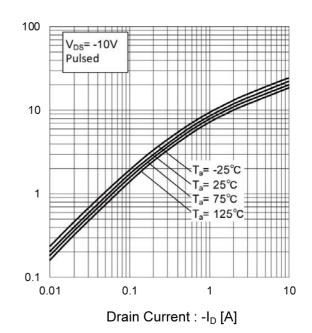


Fig.10 Tranceconductance vs. Drain Current



Forward Transfer Admittance : Y<sub>fs</sub> [S]

Fig.11 Drain Current Derating Curve

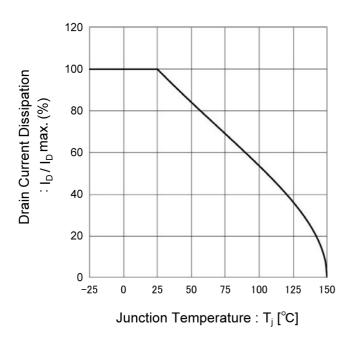


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

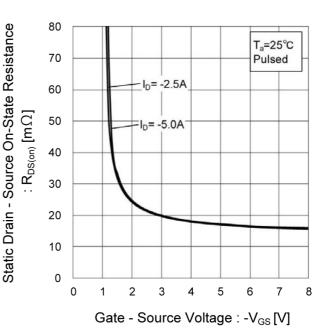


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

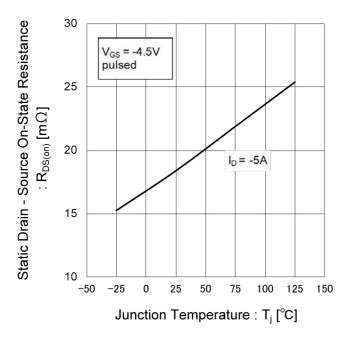


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

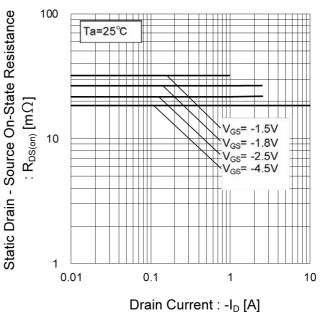


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

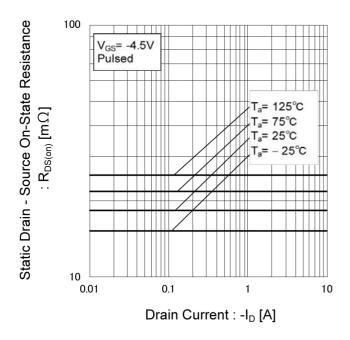


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

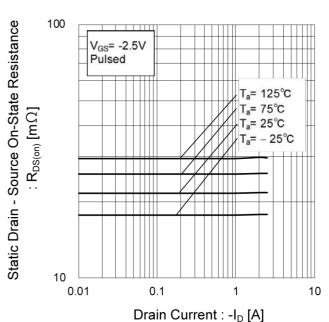


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

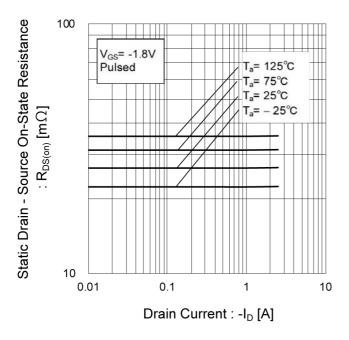


Fig.18 Static Drain - Source On - State Resistance vs. Drain Current(V)

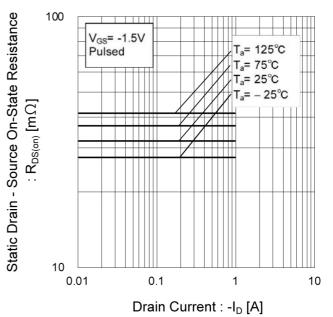


Fig.19 Typical Capacitance vs. Drain - Source Voltage

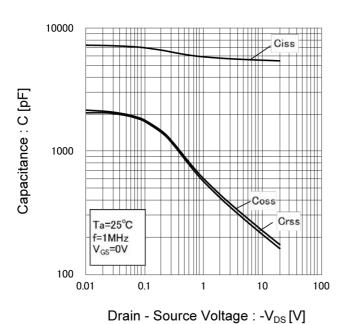
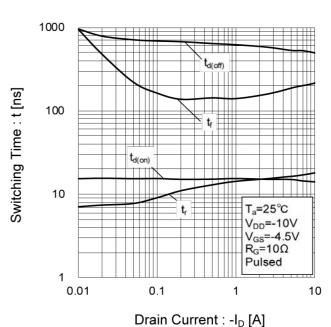


Fig.20 Switching Characteristics



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Fig.21 Dynamic Input Characteristics

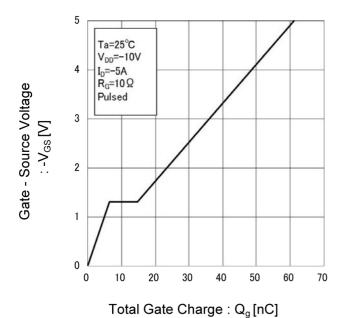
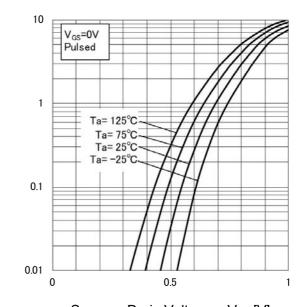


Fig.22 Source Current vs. Source Drain Voltage



Source Current : -I<sub>s</sub> [A]

Source - Drain Voltage : - $V_{\text{SD}}$  [V]

## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

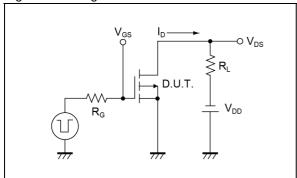


Fig.2-1 Gate Charge Measurement Circuit

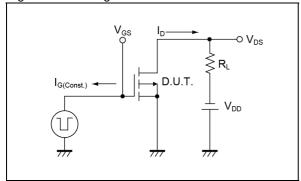


Fig.1-2 Switching Waveforms

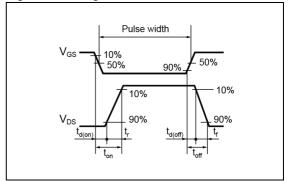
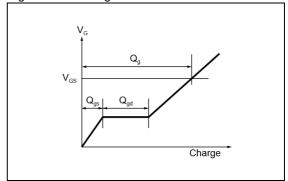
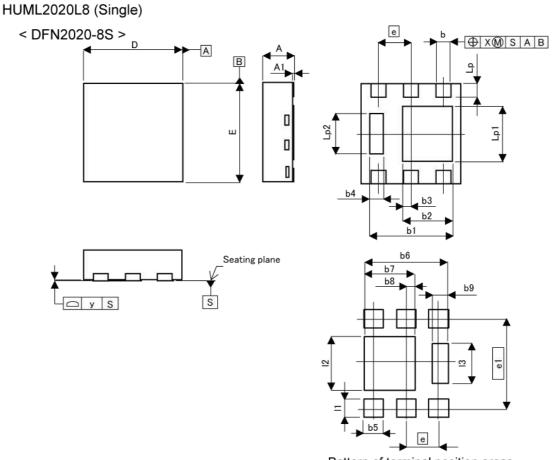


Fig.2-2 Gate Charge Waveform



## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.55	0.65	0.022	0.026
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
b1	1.55	1.75	0.061	0.069
b2	0.95	1.05	0.037	0.041
b3	0,1	175	0.0	07
b4	0.20	0.30	0.008	0.012
D	1.90	2.10	0.075	0.083
E	1.90	2.10	0.075	0.083
е	0.	65	0.0	26
Lp	0.225	0.325	0.009	0.013
Lp1	1.05	1.15	0.041	0.045
Lp2	0.75	0.85	0.030	0.033
x		0.10		0.004
у	199	0.10	58 <del>5</del> 5	0.004

DIM	MILIM	ETERS	INC	HES	
DIIVI	MIN	MAX	MIN	MAX	
b5	3 <b>≠</b> 0	0.45	( <del></del> )	0.018	
b6		1.75	545	0.069	
b7		1.05	35	0.041	
b8	0.175		0.0	0.007	
b9	( <del>4</del> )	0.30	2#5	0.012	
e1	1.725		0.068		
11	-	0.425		0.017	
12	300	1.15	( <del>**</del> )	0.045	
13	-	0.85	( £=3	0.033	

Dimension in mm/inches



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  - [h] Use of the Products in places subject to dew condensation
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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