

RGCL80TS60

600V 40A Field Stop Trench IGBT

V _{CES}	600V
I _{C(100°C)}	40A
V _{CE(sat) (Typ.)}	1.4V
P_D	148W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Soft Switching
- 3) Pb free Lead Plating; RoHS Compliant

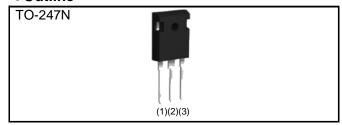
Applications

Partial Switching PFC

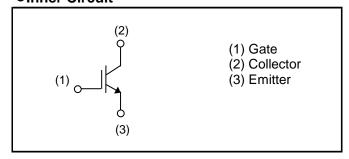
Discharge Circuit

Brake for Inverter

Outline



●Inner Circuit



Packaging Specifications

	Packaging	Tube
	Reel Size (mm)	-
Tuno	Tape Width (mm)	-
Type	Basic Ordering Unit (pcs)	450
	Taping Code	C11
	Marking	RGCL80TS60

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

		•	•	
Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	600	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	T _C = 25°C	I _C	65	А
	T _C = 100°C	I _C	40	А
Pulsed Collector Current		I _{CP} *1	160	А
Power Dissipation	T _C = 25°C	P _D	148	W
	T _C = 100°C	P _D	74	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C
44 B 1 1 1 1 1 1 1 T			•	

^{*1} Pulse width limited by T_{jmax.}

●Thermal Resistance

Parameter	Symbol	Values			Unit
raiametei		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	1.01	°C/W

ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
r ai ai nietei	Syllibol		Min.	Тур.	Max.	Unit
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	600	1	1	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 600V, V_{GE} = 0V$	ı	1	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V$, $V_{CE} = 0V$	ı	,	±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 30.0 \text{mA}$	4.5	5.5	6.5	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_C = 40A$, $V_{GE} = 15V$ $T_j = 25$ °C $T_j = 175$ °C	-	1.4 1.6	1.8 -	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Doromotor	Symbol	Conditions	Values			Unit
Parameter	, i		Min.	Тур.	Max.	Offic
Input Capacitance	C _{ies}	V _{CE} = 30V	-	2340	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	55	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	43	-	
Total Gate Charge	Q _g	V _{CE} = 300V	-	98	-	
Gate - Emitter Charge	Q_{ge}	I _C = 40A	-	20	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	38	-	•
Turn - on Delay Time	t _{d(on)}	$I_C = 40A, V_{CC} = 400V$	-	53	-	
Rise Time	t _r	$V_{GE} = 15V, R_G = 10\Omega$	-	34	-	na
Turn - off Delay Time	t _{d(off)}	T _j = 25°C	-	227	-	ns
Fall Time	t _f	Inductive Load	-	204	-	
Turn - on Switching Loss	E _{on}	*Eon includes diode	-	1.11	-	
Turn - off Switching Loss	E _{off}	reverse recovery	-	1.68	-	mJ
Turn - on Delay Time	t _{d(on)}	$I_C = 40A, V_{CC} = 400V$	-	48	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	66	-	na
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	255	-	ns
Fall Time	t _f	Inductive Load	-	310	-	
Turn - on Switching Loss	E _{on}	*Eon includes diode	-	1.51	-	
Turn - off Switching Loss	E _{off}	reverse recovery	-	2.30	-	mJ
		I _C = 160A, V _{CC} = 480V				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 600V, V_{GE} = 15V$	FU	FULL SQUARE		
		$R_G = 60\Omega, T_j = 175^{\circ}C$				

Fig.1 Power Dissipation vs. Case Temperature

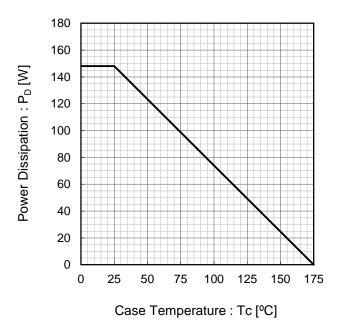


Fig.2 Collector Current vs. Case Temperature

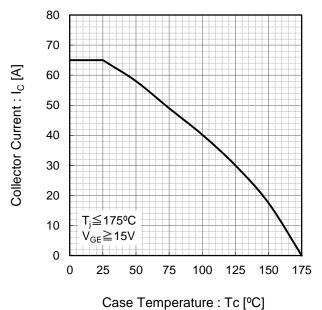


Fig.3 Forward Bias Safe Operating Area

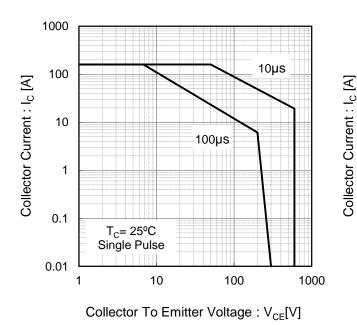
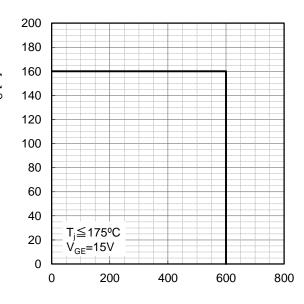


Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage : V_{CE}[V]

Fig.5 Typical Output Characteristics

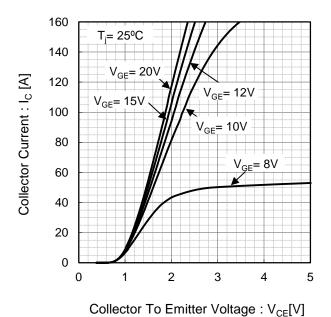
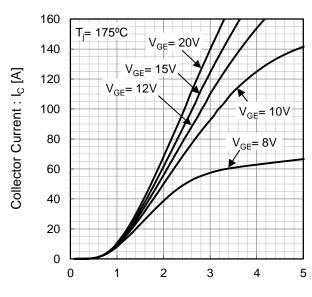


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage : V_{CE}[V]

Fig.7 Typical Transfer Characteristics

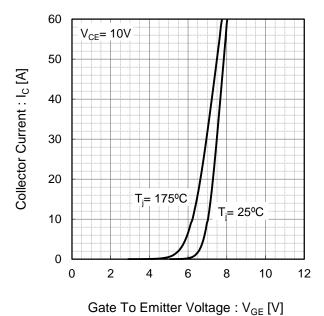
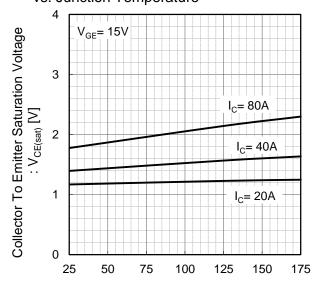
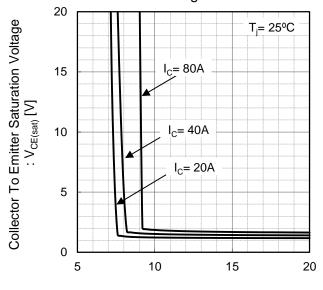


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



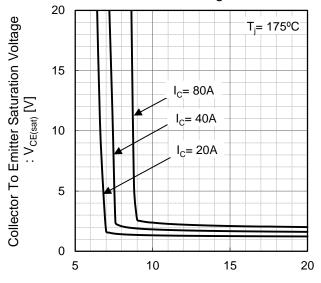
Junction Temperature : T_i [°C]

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



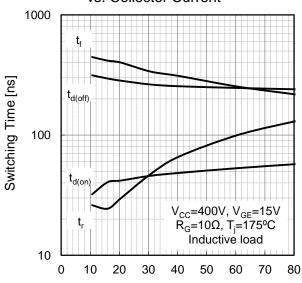
Gate To Emitter Voltage : V_{GE} [V]

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



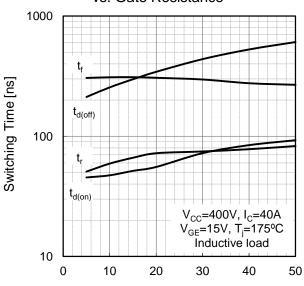
Gate To Emitter Voltage: V_{GE} [V]

Fig.11 Typical Switching Time vs. Collector Current



Collector Current : I_C [A]

Fig.12 Typical Switching Time vs. Gate Resistance



Gate Resistance : $R_G[\Omega]$

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 E_{on} 0.1 V_{CC} =400V, V_{GE} =15V R_{G} =10 Ω , T_{j} =175°C Inductive load 0.01 0 10 20 30 50 60 70 80 Collector Current : I_C [A]

vs. Gate Resistance 10 $\mathsf{E}_{\mathsf{off}}$ Switching Energy Losses [mJ] 1 Eon 0.1 V_{CC}=400V, I_C=40A V_{GE}=15V, T_j=175°C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance : $R_G[\Omega]$

Fig.14 Typical Switching Energy Losses

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] 100 Coes Cres 10 f=1MHz $V_{GE}=0V$ T_i=25°C 0.01 0.1 1 10 100 Collector To Emitter Voltage : V_{CE}[V]

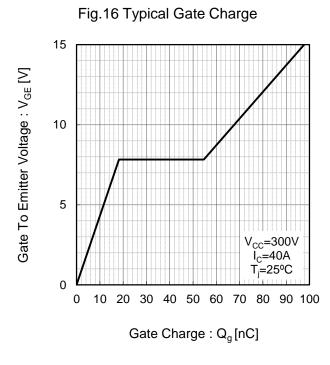
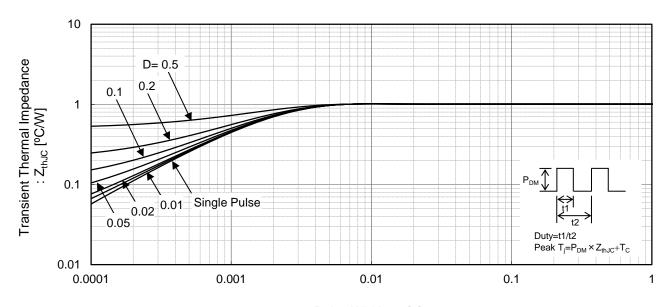


Fig.17 IGBT Transient Thermal Impedance



Pulse Width: t1[s]

●Inductive Load Switching Circuit and Waveform

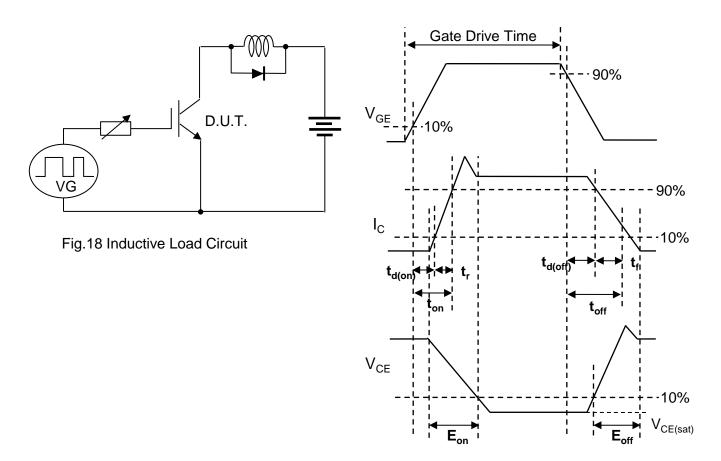


Fig.19 Inductive Load Waveform

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 XD15H120CX1
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