

V _{CES}	650V
I _{C(100°C)}	8A
V _{CE(sat) (Typ.)}	1.65V
P _D	94W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Low Switching Loss
- 3) Short Circuit Withstand Time 5µs
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating ; RoHS Compliant

Applications

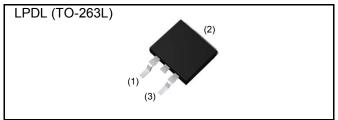
General Inverter

UPS

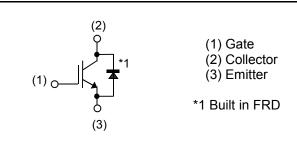
Power Conditioner

Welder

Outline



Inner Circuit



Packaging Specifications

Туре	Packaging	Taping
	Reel Size (mm)	330
	Tape Width (mm)	24
	Basic Ordering Unit (pcs)	1,000
	Packing Code	TL
	Marking	RGT16NL65D

•Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

		,		
Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V _{GES}	±30	V
Collector Current	$T_{\rm C}$ = 25°C	Ι _C	16	А
Collector Current	T _C = 100°C	Ι _C	8	А
Pulsed Collector Current		I _{CP} *1	24	А
Diada Farward Current	T _C = 25°C	I _F	16	А
Diode Forward Current	T _C = 100°C	I _F	8	А
Diode Pulsed Forward Current		I _{FP} ^{*1}	24	А
Power Dissinction	$T_{\rm C}$ = 25°C	P _D	94	W
Power Dissipation	T _C = 100°C	P _D	47	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C
*1 Pulse width limited by T		•		

*1 Pulse width limited by T_{jmax.}

Thermal Resistance

Parameter	Symbol	Values			Unit
Faranielei	Symbol	Min.	Тур.	Max.	Unit
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	1.58	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	3.60	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit	
Faranieler	Symbol Conditions –		Min.	Тур.	Max.	Unit	
Collector - Emitter Breakdown Voltage	BV _{CES}	I _C = 10μΑ, V _{GE} = 0V	650	-	-	V	
Collector Cut - off Current	I _{CES}	V _{CE} = 650V, V _{GE} = 0V	-	-	10	μA	
Gate - Emitter Leakage Current	I _{GES}	V _{GE} = ±30V, V _{CE} = 0V	-	-	±200	nA	
Gate - Emitter Threshold Voltage	V _{GE(th)}	V _{CE} = 5V, I _C = 5.5mA	5.0	6.0	7.0	V	
Collector - Emitter Saturation Voltage	V _{CE(sat)}	I _C = 8A, V _{GE} = 15V T _j = 25°C T _j = 175°C	-	1.65 2.15	2.1	V	

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			11.11
			Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	V _{CE} = 30V	-	450	-	
Output Capacitance	C _{oes}	V _{GE} = 0V	-	21	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	8	-	
Total Gate Charge	Q_g	V _{CE} = 300V	-	21	-	
Gate - Emitter Charge	Q_{ge}	I _C = 8A	-	6	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	8	-	
Turn - on Delay Time	t _{d(on)}	I _C = 8A, V _{CC} = 400V	-	13	-	
Rise Time	t _r	V _{GE} = 15V, R _G = 10Ω	-	13	-	ns
Turn - off Delay Time	$t_{d(off)}$	T _j = 25°C	-	33	-	
Fall Time	t _f	Inductive Load	-	95	-	
Turn - on Delay Time	t _{d(on)}	I _C = 8A, V _{CC} = 400V	-	13	-	
Rise Time	t _r	V _{GE} = 15V, R _G = 10Ω	-	14	-	
Turn - off Delay Time	$t_{d(off)}$	T _j = 175°C	-	50	-	ns
Fall Time	t _f	Inductive Load	-	120	-	
		I _C = 24A, V _{CC} = 520V				
Reverse Bias Safe Operating Area RBSOA		V _P = 650V, V _{GE} = 15V	FULL SQUARE			-
		R _G = 50Ω, T _j = 175°C				
		$V_{CC} \leq 360V$				
Short Circuit Withstand Time	t _{sc}	V _{GE} = 15V	5	-	-	μs
		T _j = 25°C				

3/11

•FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deremeter	Symbol	Conditions	Values			Unit
Parameter			Min.	Тур.	Max.	Unit
Diode Forward Voltage	V _F	I _F = 8A T _j = 25°C T _j = 175°C	-	1.4 1.2	1.9 -	V
Diode Reverse Recovery Time	t _{rr}	I _F = 8A	-	42	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs T _j = 25°C	-	5.2	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.12	-	μC
Diode Reverse Recovery Time	t _{rr}	I _F = 8A	-	116	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs T _j = 175°C	-	8.1	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.51	-	μC

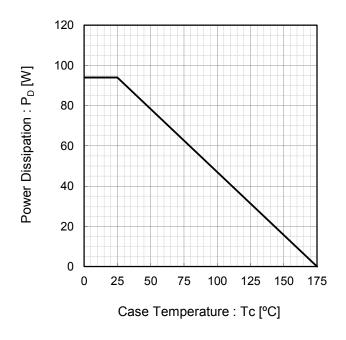


Fig.1 Power Dissipation vs. Case Temperature

Fig.2 Collector Current vs. Case Temperature

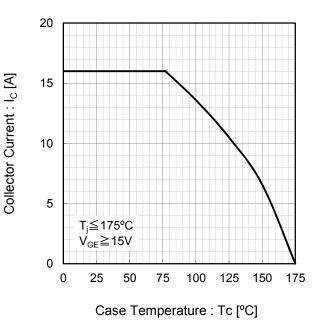
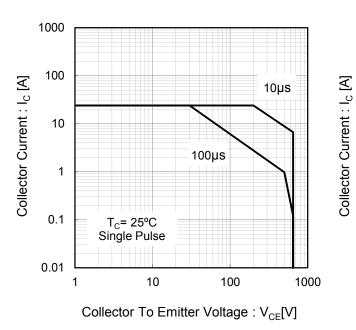
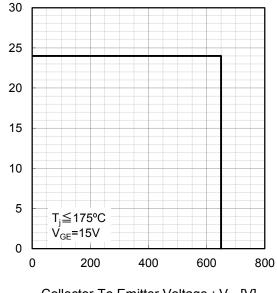


Fig.3 Forward Bias Safe Operating Area

Fig.4 Reverse Bias Safe Operating Area





Collector To Emitter Voltage : V_{CE}[V]

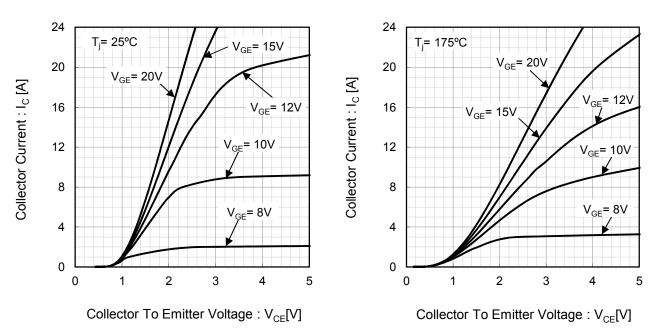
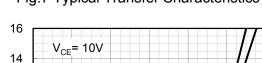


Fig.5 Typical Output Characteristics



T_i= 175°C

6

Gate To Emitter Voltage : V_{GE} [V]

8

4

Fig.7 Typical Transfer Characteristics 4 3 2

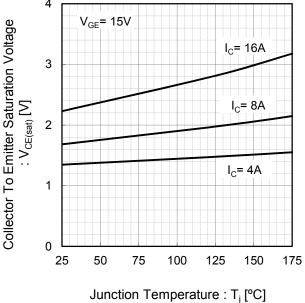
T_i= 25°C

10

12

Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

Fig.6 Typical Output Characteristics



Collector Current : I_c [A]

12

10

8

6

4

2

0

0

2

Fig.10 Typical Collector To Emitter Saturation Voltage

•Electrical Characteristic Curves

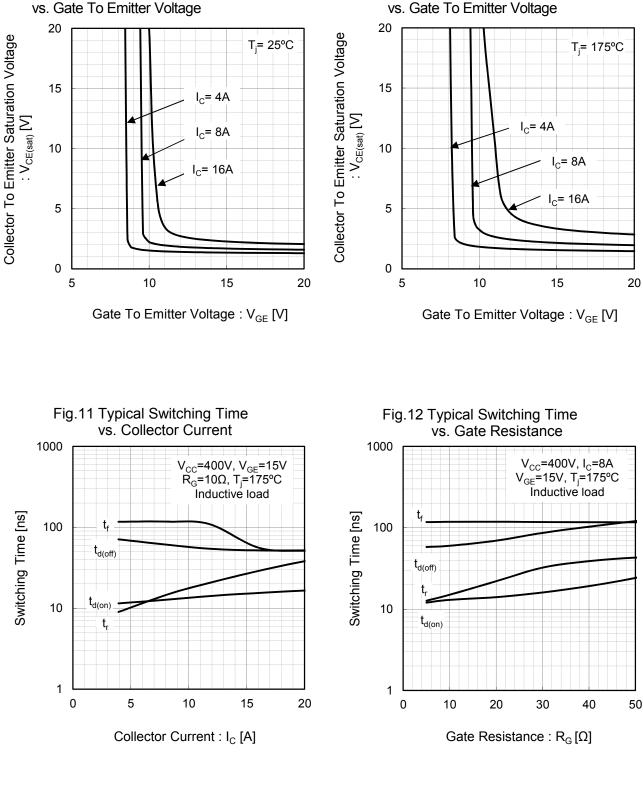
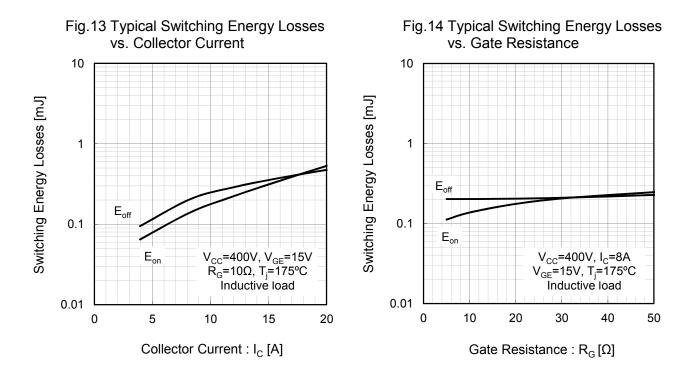
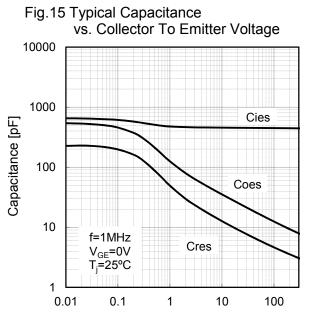


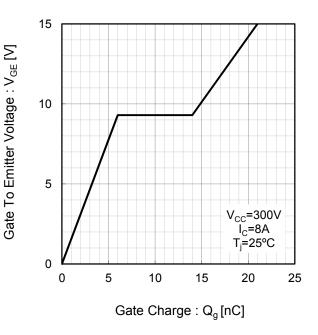
Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage





Collector To Emitter Voltage : V_{CE}[V]

Fig.16 Typical Gate Charge



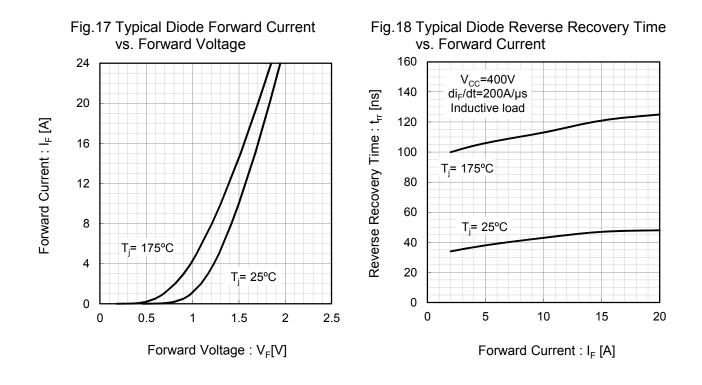
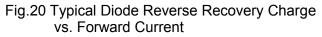
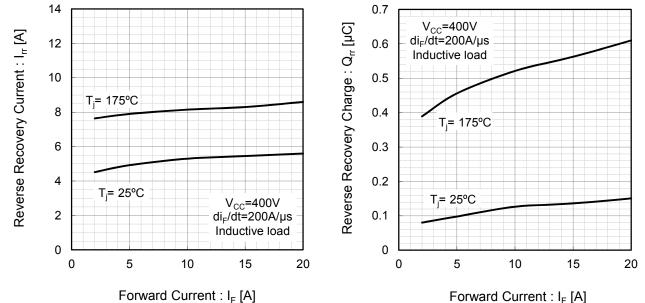


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current





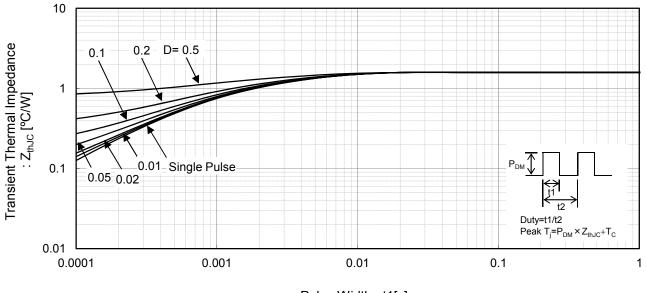
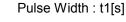
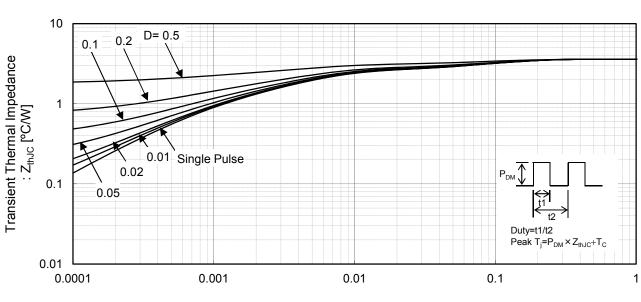


Fig.21 IGBT Transient Thermal Impedance

Fig.22 Diode Transient Thermal Impedance





Pulse Width : t1[s]

●Inductive Load Switching Circuit and Waveform

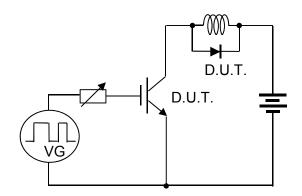


Fig.23 Inductive Load Circuit

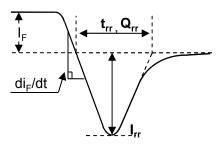


Fig.25 Diode Reverce Recovery Waveform

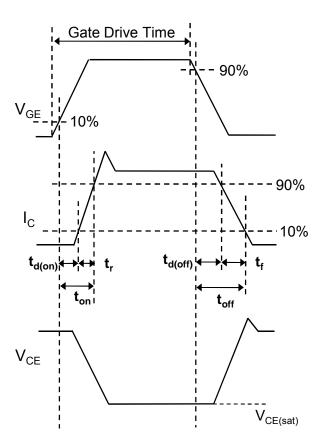


Fig.24 Inductive Load Waveform

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