

V _{CES}	650V
I _{C(100°C)}	15A
V _{CE(sat) (Typ.)}	1.65V
P _D	133W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Low Switching Loss
- 3) Short Circuit Withstand Time 5µs
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating ; RoHS Compliant

Applications

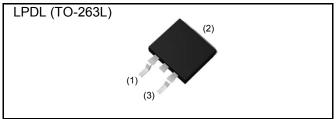
General Inverter

UPS

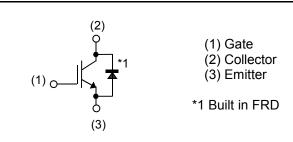
Power Conditioner

Welder

Outline



Inner Circuit



Packaging Specifications

	Packaging	Taping
	Reel Size (mm)	330
Typo	Tape Width (mm)	24
Туре	Basic Ordering Unit (pcs)	1,000
	Packing Code	TL
	Marking	RGT30NL65D

•Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V _{CES}	650	V
Gate - Emitter Voltage		V _{GES}	±30	V
Callester Current	T _C = 25°C	Ι _C	30	А
Collector Current	T _C = 100°C	Ι _C	15	А
Pulsed Collector Current	I _{CP} *1	45	А	
Diada Farward Current	T _C = 25°C	١ _F	26	А
Diode Forward Current	T _C = 100°C	I _F	15	А
Diode Pulsed Forward Current		I _{FP} ^{*1}	45	А
$T_c = 25^{\circ}C$		P _D	133	W
Power Dissipation	T _C = 100°C	P _D	66	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T _{stg}	–55 to +175	°C
*1 Dulco width limitod by T				

*1 Pulse width limited by T_{jmax.}

Thermal Resistance

Parameter	Symbol	Values			Unit
Faranieter	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	1.12	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	2.86	°C/W

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Symbol Conditions		Values			Unit	
Faranieler	Symbol	Conditions	Min. Typ.		Max.	Onic	
Collector - Emitter Breakdown Voltage	BV _{CES}	I _C = 10μΑ, V _{GE} = 0V	650	-	-	V	
Collector Cut - off Current	I _{CES}	V _{CE} = 650V, V _{GE} = 0V	-	-	10	μA	
Gate - Emitter Leakage Current	I _{GES}	V_{GE} = ±30V, V_{CE} = 0V	-	-	±200	nA	
Gate - Emitter Threshold Voltage	V _{GE(th)}	V _{CE} = 5V, I _C = 10.0mA	5.0	6.0	7.0	V	
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{c} = 15A, V_{GE} = 15V$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.65 2.15	2.1	V	

•IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deveneter	Sumbol Conditions		Values			1.1	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input Capacitance	C _{ies}	V _{CE} = 30V	-	780	-		
Output Capacitance	C _{oes}	V _{GE} = 0V	-	35	-	pF	
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	13	-		
Total Gate Charge	Q_g	V _{CE} = 300V	-	32	-		
Gate - Emitter Charge	Q_{ge}	I _C = 15A	-	8	-	nC	
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	11	-		
Turn - on Delay Time	t _{d(on)}	I _C = 15A, V _{CC} = 400V	-	18	-		
Rise Time	t _r	V _{GE} = 15V, R _G = 10Ω	-	20	-		
Turn - off Delay Time	$t_{d(off)}$	T _j = 25°C	-	64	-	ns	
Fall Time	t _f	Inductive Load	-	75	-		
Turn - on Delay Time	t _{d(on)}	I _C = 15A, V _{CC} = 400V	-	18	-		
Rise Time	t _r	V _{GE} = 15V, R _G = 10Ω	-	22	-		
Turn - off Delay Time	$t_{d(off)}$	T _j = 175°C	-	74	-	ns	
Fall Time	t _f	Inductive Load	-	130	-		
		I _C = 45A, V _{CC} = 520V			-		
Reverse Bias Safe Operating Area	RBSOA	V _P = 650V, V _{GE} = 15V	FU	LL SQUA	ARE	-	
		R _G = 50Ω, T _j = 175°C					
		$V_{CC} \leq 360V$					
Short Circuit Withstand Time	t _{sc}	V _{GE} = 15V	5	-	-	μs	
		T _j = 25°C					

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•FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Deremeter	Cumhal	Conditions		Values	L Incid	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Diode Forward Voltage	V _F	I _F = 15A T _j = 25°C T _j = 175°C	-	1.5 1.3	1.95 -	V
Diode Reverse Recovery Time	t _{rr}	I _F = 15A	-	55	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs T _j = 25°C	-	6.0	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.19	-	μC
Diode Reverse Recovery Time	t _{rr}	I _F = 15A	-	141	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	V _{CC} = 400V di _F /dt = 200A/µs T _j = 175°C	-	9.5	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.79	-	μC

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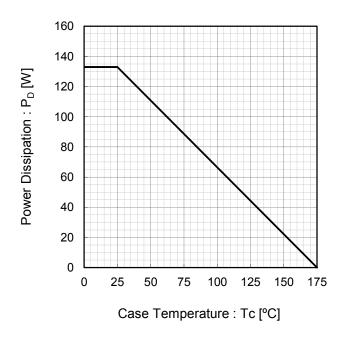


Fig.1 Power Dissipation vs. Case Temperature

Fig.2 Collector Current vs. Case Temperature

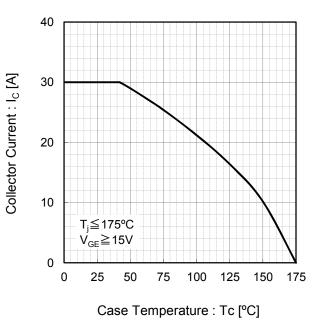
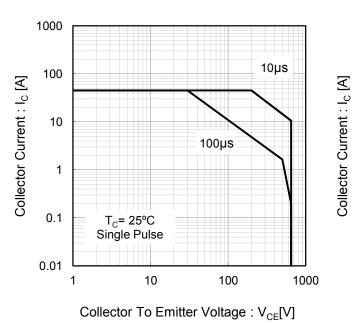
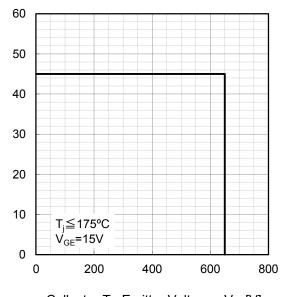


Fig.3 Forward Bias Safe Operating Area

Fig.4 Reverse Bias Safe Operating Area





Collector To Emitter Voltage : $V_{CE}[V]$

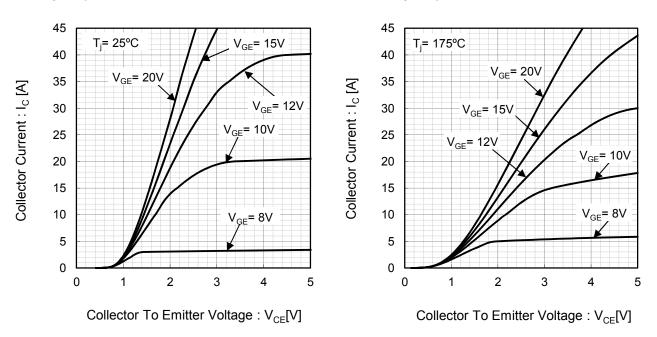


Fig.5 Typical Output Characteristics

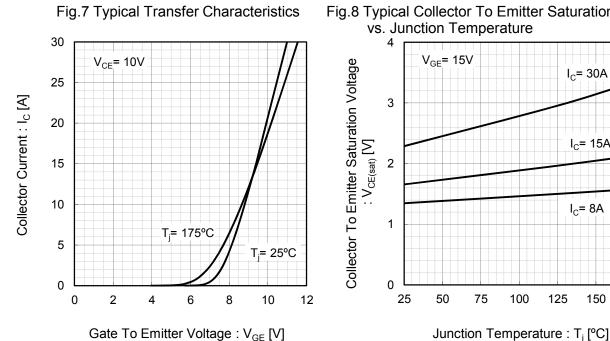
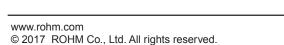


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature

Fig.6 Typical Output Characteristics



I_C= 30A

 $I_{c} = 15A$

I_C= 8A

150

175

125

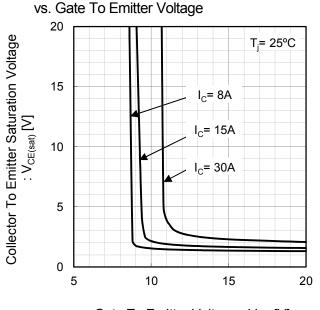


Fig.9 Typical Collector To Emitter Saturation Voltage

Gate To Emitter Voltage : V_{GE} [V]

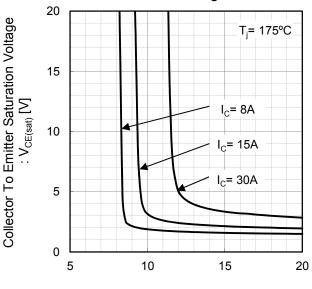
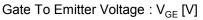
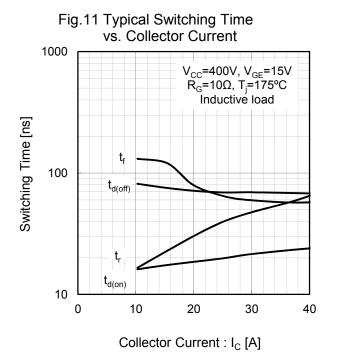
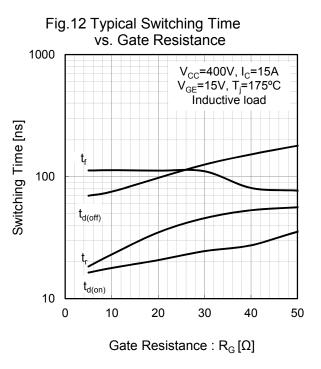
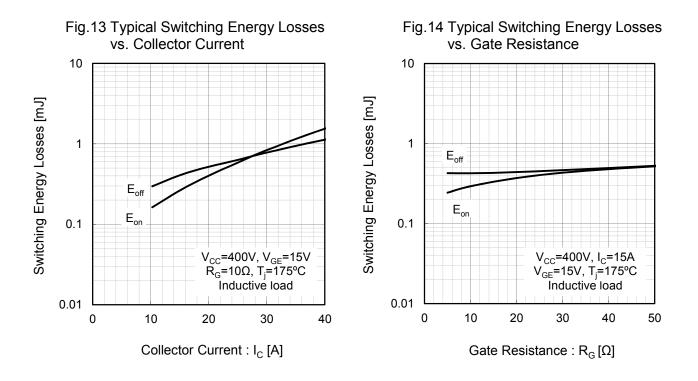


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage









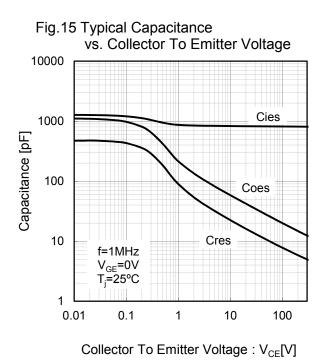
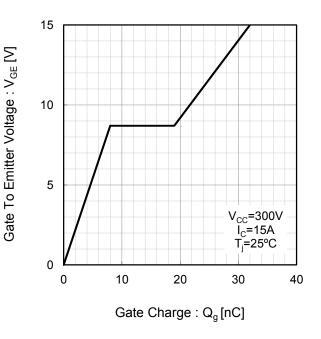


Fig.16 Typical Gate Charge



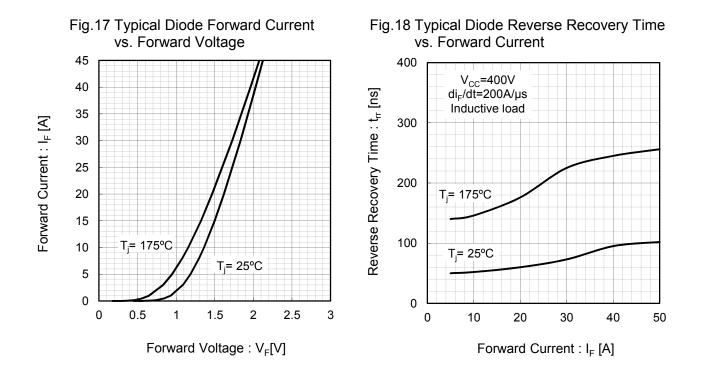


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

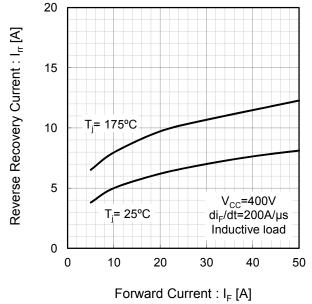
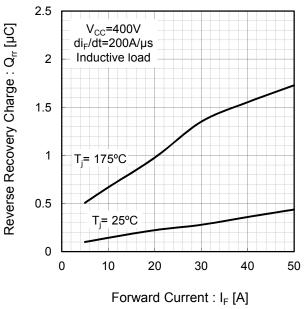


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



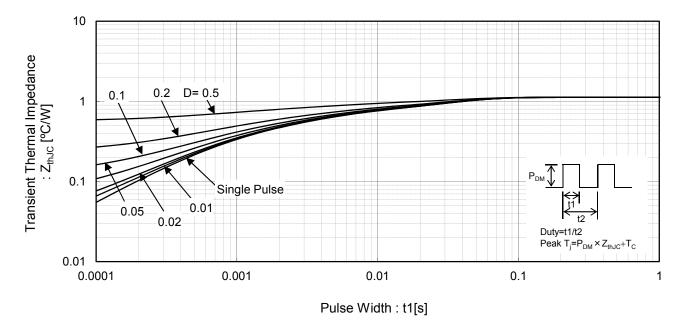


Fig.21 IGBT Transient Thermal Impedance



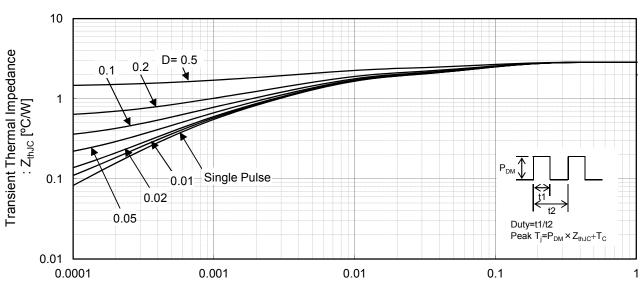


Fig.22 Diode Transient Thermal Impedance

Pulse Width : t1[s]

●Inductive Load Switching Circuit and Waveform

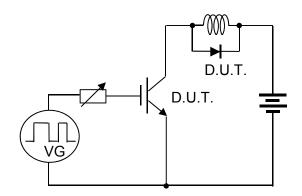


Fig.23 Inductive Load Circuit

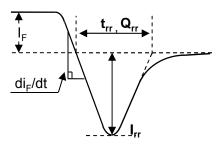


Fig.25 Diode Reverce Recovery Waveform

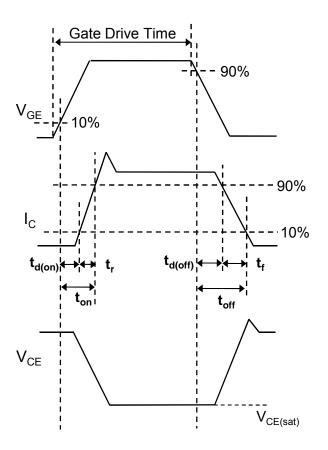


Fig.24 Inductive Load Waveform

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RGT30NL65D - Web Page

Part Number	RGT30NL65D
Package	LPDL
Unit Quantity	1000
Minimum Package Quantity	1000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes

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