

V <sub>CES</sub>	650V
I <sub>C(100°C)</sub>	15A
V <sub>CE(sat) (Typ.)</sub>	1.65V
P <sub>D</sub>	133W

#### Features

- 1) Low Collector Emitter Saturation Voltage
- 2) Low Switching Loss
- 3) Short Circuit Withstand Time 5µs
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating ; RoHS Compliant

### Applications

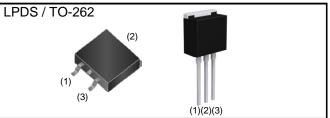
**General Inverter** 

UPS

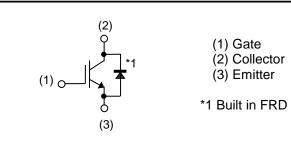
**Power Conditioner** 

Welder

## Outline



#### Inner Circuit



### Packaging Specifications

	Packaging	Taping / Tube
	Reel Size (mm)	330 / -
Type	Tape Width (mm)	24 / -
Туре	Basic Ordering Unit (pcs)	1,000 / 1,000
	Packing code	TL / C9
	Marking	RGT30NS65D

## ●Absolute Maximum Ratings (at T<sub>C</sub> = 25°C unless otherwise specified)

	•	,		
Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V <sub>CES</sub>	650	V
Gate - Emitter Voltage		V <sub>GES</sub>	±30	V
Collector Current	$T_{\rm C} = 25^{\circ}{\rm C}$	Ι <sub>C</sub>	30	А
Collector Current	T <sub>C</sub> = 100°C	Ι <sub>C</sub>	15	А
Pulsed Collector Current		I <sub>CP</sub> *1	45	А
Diode Forward Current	$T_{\rm C} = 25^{\circ}{\rm C}$	١ <sub>F</sub>	26	А
Dioue Forward Current	T <sub>C</sub> = 100°C	١ <sub>F</sub>	15	А
Diode Pulsed Forward Current		I <sub>FP</sub> <sup>*1</sup>	45	А
Power Dissipation	$T_{\rm C} = 25^{\circ}{\rm C}$	P <sub>D</sub>	133	W
Power Dissipation	T <sub>C</sub> = 100°C	P <sub>D</sub>	66	W
Operating Junction Temperature		Tj	-40 to +175	°C
Storage Temperature		T <sub>stg</sub>	-55 to +175	°C
*1 Pulso width limited by T		•	•	

\*1 Pulse width limited by T<sub>jmax.</sub>

#### Thermal Resistance

Parameter	Symbol	Values			Unit
	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	1.12	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j\text{-}c)}$	-	-	2.86	°C/W

## •IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

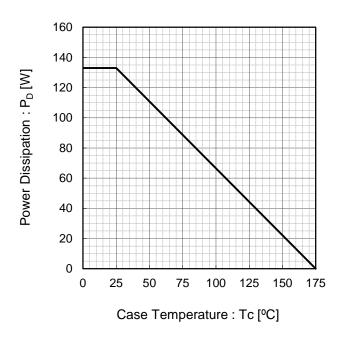
Parameter	Symbol	Conditions	Values			Unit	
Farameter	Symbol	Symbol Conditions		Тур.	Max.	Unit	
Collector - Emitter Breakdown Voltage	BV <sub>CES</sub>	I <sub>C</sub> = 10μΑ, V <sub>GE</sub> = 0V	650	-	-	V	
Collector Cut - off Current	I <sub>CES</sub>	V <sub>CE</sub> = 650V, V <sub>GE</sub> = 0V	-	-	10	μA	
Gate - Emitter Leakage Current	I <sub>GES</sub>	$V_{GE} = \pm 30V, V_{CE} = 0V$	-	-	±200	nA	
Gate - Emitter Threshold Voltage	V <sub>GE(th)</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 10.0mA	5.0	6.0	7.0	V	
Collector - Emitter Saturation Voltage	V <sub>CE(sat)</sub>	$I_{C} = 15A, V_{GE} = 15V$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.65 2.15	2.1 -	V	

# •IGBT Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol Conditions	Conditions	Values			
		Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C <sub>ies</sub>	V <sub>CE</sub> = 30V	-	780	-	
Output Capacitance	C <sub>oes</sub>	$V_{GE} = 0V$	-	35	-	pF
Reverse Transfer Capacitance	C <sub>res</sub>	f = 1MHz	-	13	-	
Total Gate Charge	$Q_g$	V <sub>CE</sub> = 300V	-	32	-	
Gate - Emitter Charge	$Q_{ge}$	I <sub>C</sub> = 15A	-	8	-	nC
Gate - Collector Charge	$Q_{gc}$	V <sub>GE</sub> = 15V	-	11	-	
Turn - on Delay Time	t <sub>d(on)</sub>	$I_{\rm C} = 15 {\rm A},  {\rm V}_{\rm CC} = 400 {\rm V}$	-	18	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_{G} = 10\Omega$	-	20	-	ns
Turn - off Delay Time	t <sub>d(off)</sub>	T <sub>j</sub> = 25°C	-	64	-	
Fall Time	t <sub>f</sub>	Inductive Load	-	75	-	
Turn - on Delay Time	t <sub>d(on)</sub>	$I_{\rm C} = 15 {\rm A},  {\rm V}_{\rm CC} = 400 {\rm V}$	-	18	-	
Rise Time	t <sub>r</sub>	$V_{GE} = 15V, R_{G} = 10\Omega$	-	22	-	
Turn - off Delay Time	$t_{d(off)}$	T <sub>j</sub> = 175°C	-	74	-	ns
Fall Time	t <sub>f</sub>	Inductive Load	-	130	-	
		$I_{\rm C} = 45 {\rm A},  V_{\rm CC} = 520 {\rm V}$				
Reverse Bias Safe Operating Area	RBSOA	$V_{P} = 650V, V_{GE} = 15V$	FU	LL SQUA	RE	-
		R <sub>G</sub> = 50Ω, T <sub>j</sub> = 175°C				
		$V_{CC} \leq 360V$				
Short Circuit Withstand Time	t <sub>sc</sub>	V <sub>GE</sub> = 15V	5	-	-	μs
		$T_j = 25^{\circ}C$				

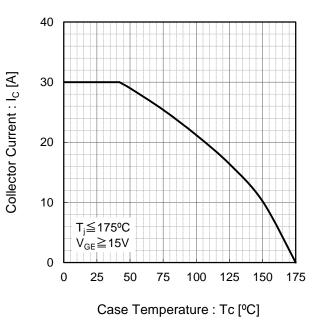
# •FRD Electrical Characteristics (at $T_j = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Тур.	Max.	Unit
Diode Forward Voltage	V <sub>F</sub>	$I_F = 15A$ $T_j = 25^{\circ}C$	-	1.5	1.95	V
		T <sub>j</sub> = 175°C	-	1.3	-	
Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 15A$ $V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	55	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	6.0	-	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.19	-	μC
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 15A V <sub>CC</sub> = 400V di <sub>F</sub> /dt = 200A/µs T <sub>j</sub> = 175°C	-	141	-	ns
Diode Peak Reverse Recovery Current	I <sub>rr</sub>		-	9.5	-	А
Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	0.79	-	μC



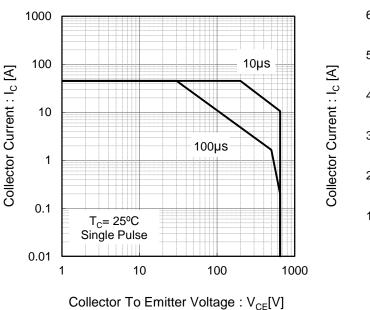
#### Fig.1 Power Dissipation vs. Case Temperature

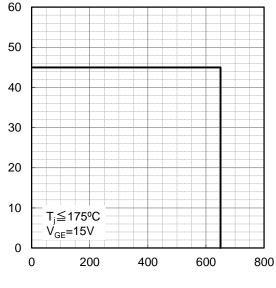
Fig.2 Collector Current vs. Case Temperature



### Fig.3 Forward Bias Safe Operating Area

Fig.4 Reverse Bias Safe Operating Area





Collector To Emitter Voltage :  $V_{CE}[V]$ 

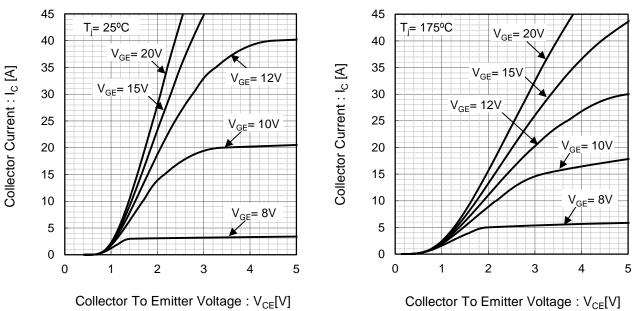
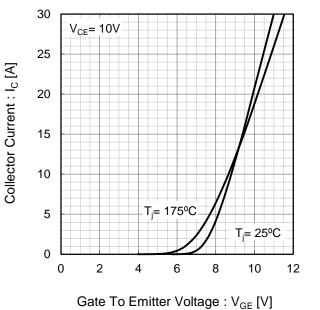
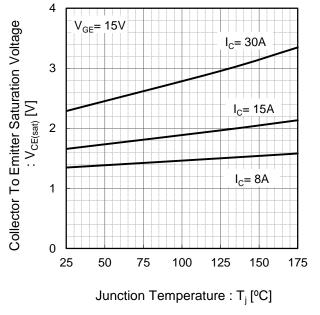


Fig.5 Typical Output Characteristics

#### Fig.7 Typical Transfer Characteristics

Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature





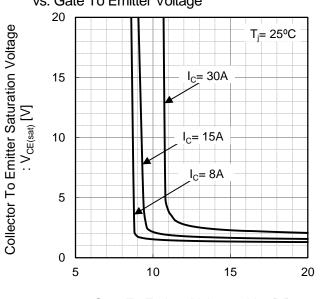


Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

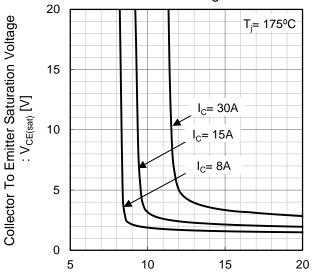


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage





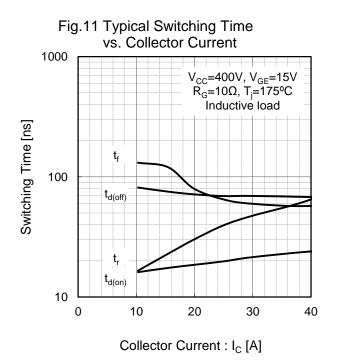
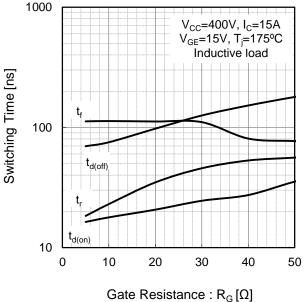
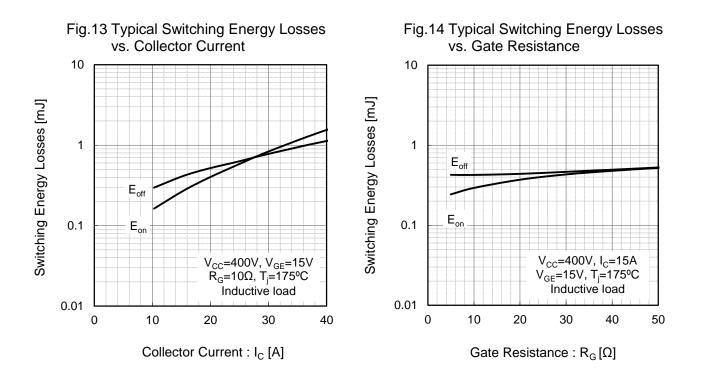
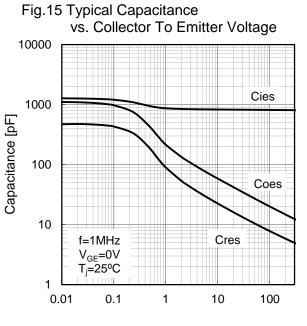


Fig.12 Typical Switching Time vs. Gate Resistance

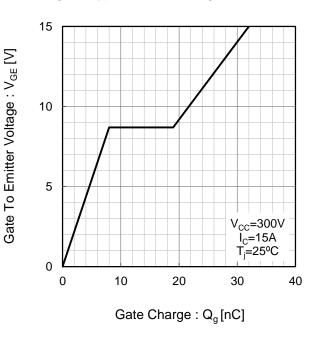






Collector To Emitter Voltage : V<sub>CE</sub>[V]

Fig.16 Typical Gate Charge



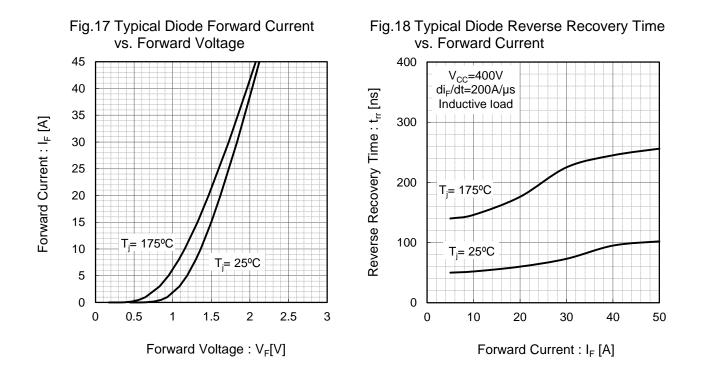


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

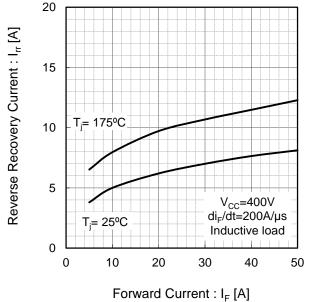
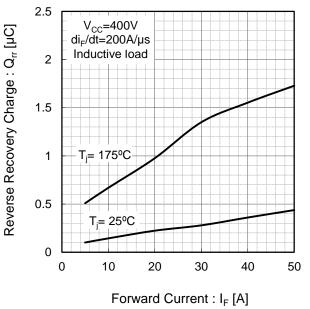
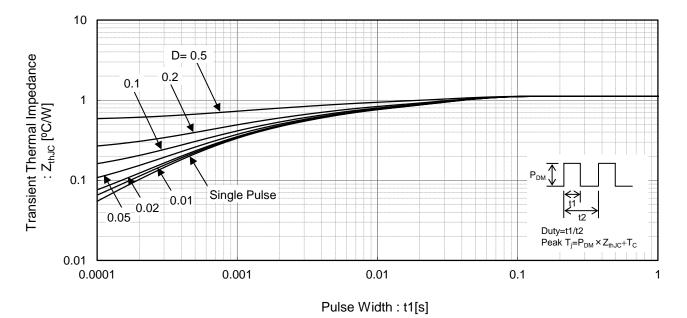


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current









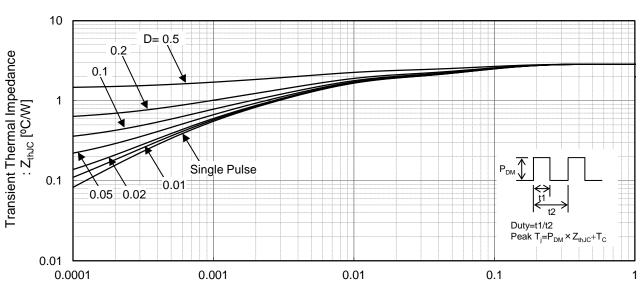


Fig.22 Diode Transient Thermal Impedance

Pulse Width : t1[s]

## ●Inductive Load Switching Circuit and Waveform

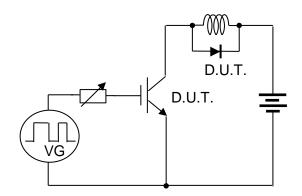


Fig.23 Inductive Load Circuit

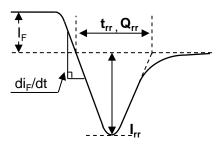


Fig.25 Diode Reverce Recovery Waveform

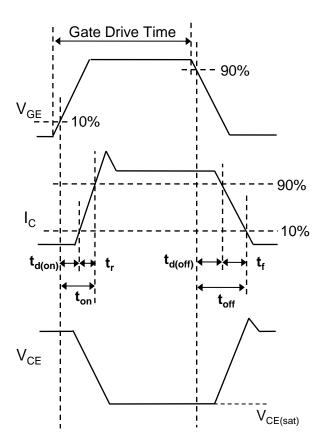


Fig.24 Inductive Load Waveform

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