RGW50TK65D

650V 25A Field Stop Trench IGBT

Datasheet

V _{CES}	650V
I _{C (100°C)}	18A
V _{CE(sat) (Typ.)}	1.5V
P_D	67W

Outline TO-3PFM

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD
- 5) Pb free Lead Plating; RoHS Compliant

Application

PFC

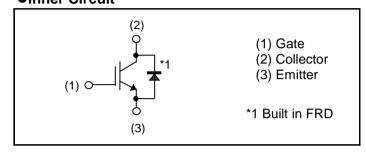
UPS

Welding

Solar Inverter

ΙH

●Inner Circuit



Packaging Specifications

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Packaging	Tube				
Reel Size (mm)	-				
Tape Width (mm)	-				
Basic Ordering Unit (pcs)	450				
Packing Code	C11				
Marking	RGW50TK65D				
	Packaging Reel Size (mm) Tape Width (mm) Basic Ordering Unit (pcs) Packing Code				

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage	Collector - Emitter Voltage		650	V
Gate - Emitter Voltage		V_{GES}	±30	V
Callactar Current	T _C = 25°C	I _C	30	Α
Collector Current	T _C = 100°C	I _C	18	Α
Pulsed Collector Current	Pulsed Collector Current		100	Α
Diode Forward Current	T _C = 25°C	I _F	27	А
	T _C = 100°C	I _F	16	Α
Diode Pulsed Forward Current	iode Pulsed Forward Current		100	Α
Power Dissipation	T _C = 25°C	P _D	67	W
	T _C = 100°C	P _D	33	W
Operating Junction Temperature		T _j	-40 to +175	°C
Storage Temperature		T _{stg}	-55 to +175	°C

^{*1} Pulse width limited by T_{jmax.}

●Thermal Resistance

Parameter	Cymphol	Values			Unit
Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	2.24	°C/W
Thermal Resistance Diode Junction - Case	R _{θ(j-c)}	-	-	2.79	°C/W

●IGBT Electrical Characteristics (at T_i = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
r arameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	ı	ı	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	-	-	10	μA
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	ı	ı	±200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5V, I_{C} = 16.4 \text{mA}$	5.0	6.0	7.0	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_{C} = 25A, V_{GE} = 15V,$ $T_{j} = 25^{\circ}C$ $T_{j} = 175^{\circ}C$	-	1.5 1.85	1.9 -	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Daramatar	Symbol	Conditions	Values			l lmit
Parameter			Min.	Тур.	Max.	Unit
Input Capacitance	C _{ies}	$V_{CE} = 30V$,	-	2080	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$,	-	56	-	pF
Reverse transfer Capacitance	C _{res}	f = 1MHz	-	38	-	
Total Gate Charge	Q_g	V _{CE} = 400V,	-	73	-	
Gate - Emitter Charge	Q_{ge}	I _C = 25A,	-	15	-	nC
Gate - Collector Charge	Q _{gc}	V _{GE} = 15V	-	28	-	
Turn - on Delay Time	t _{d(on)}		-	35	-	ns mJ
Rise Time	t _r	$I_C = 25A, V_{CC} = 400V,$ $V_{GF} = 15V, R_G = 10\Omega,$	-	11	-	
Turn - off Delay Time	t _{d(off)}	$V_{GE} = 15V$, $K_G = 10\Omega$, $T_j = 25^{\circ}C$ Inductive Load $*E_{on}$ include diode reverse recovery	1	102	1	
Fall Time	t _f		ı	53	ı	
Turn - on Switching Loss	E _{on}		1	0.39	1	
Turn - off Switching Loss	E _{off}		1	0.43	1	
Turn - on Delay Time	t _{d(on)}		-	34	-	
Rise Time	t _r	$I_C = 25A, V_{CC} = 400V,$ $V_{GE} = 15V, R_G = 10\Omega,$	1	12	1	ns
Turn - off Delay Time	t _{d(off)}	$T_i = 175^{\circ}C$	1	118	-	
Fall Time	t _f	Inductive Load	-	78	-	
Turn - on Switching Loss	E _{on}	*E _{on} include diode reverse recovery	-	0.41	-	mJ
Turn - off Switching Loss	E _{off}		-	0.60	-	IIIJ
Reverse Bias Safe Operating Area		$I_C = 100A, V_{CC} = 520V,$				
	RBSOA	$V_P = 650V, V_{GE} = 15V,$	FU	FULL SQUARE		-
		$R_G = 100\Omega, T_j = 175^{\circ}C$				

●FRD Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Parameter	Sumbol Conditions	Values			l lmit	
	Symbol	Conditions	Min.	Тур.	Max.	Unit
		I _F = 20A,				
Diode Forward Voltage	V_{F}	$T_j = 25^{\circ}C$	-	1.45	1.9	V
		T _j = 175°C	-	1.55	-	
Diode Reverse Recovery Time	t _{rr}		-	92	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	I _F = 20A, V _{CC} = 400V,	-	6.7	-	А
Diode Reverse Recovery Charge	Q _{rr}	di _F /dt = 200A/µs, T _j = 25°C	-	0.34	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	14.1	-	μJ
Diode Reverse Recovery Time	t _{rr}	$I_F = 20A,$ $V_{CC} = 400V,$ $di_F/dt = 200A/\mu s,$ $T_j = 175^{\circ}C$	-	123	-	ns
Diode Peak Reverse Recovery Current	I _{rr}		-	7.8	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.59	-	μC
Diode Reverse Recovery Energy	E _{rr}		-	30.7	-	μJ

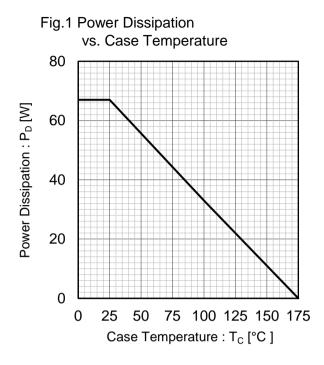


Fig.3 Forward Bias Safe Operating Area

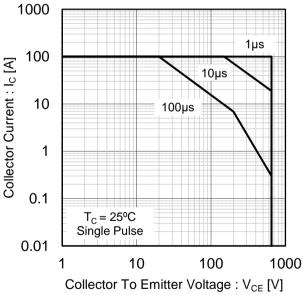


Fig.4 Reverse Bias Safe Operating Area

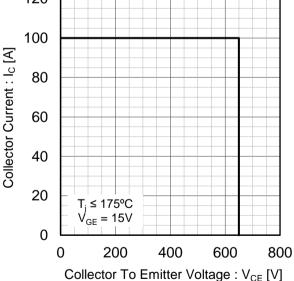


Fig.5 Typical Output Characteristics

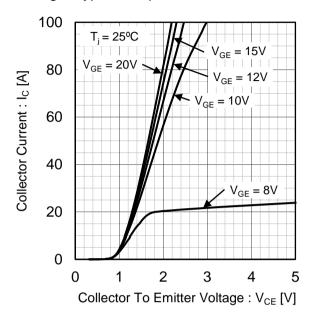


Fig.6 Typical Output Characteristics

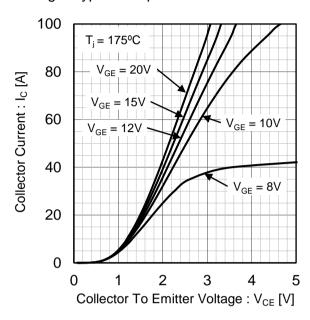


Fig.7 Typical Transfer Characteristics

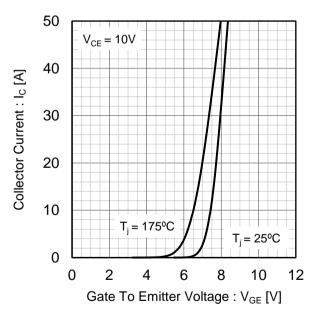
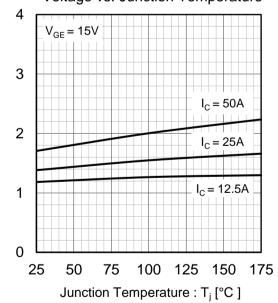


Fig.8 Typical Collector to Emitter Saturation Voltage vs. Junction Temperature



ROHM

Collector To Emitter Saturation

Voltage: V_{CE(sat)} [V]

0

5

Electrical Characteristic Curves

Fig.9 Typical Collector to Emitter Saturation

Fig.10 Typical Collector to Emitter Saturation Voltage vs. Gate to Emitter Voltage

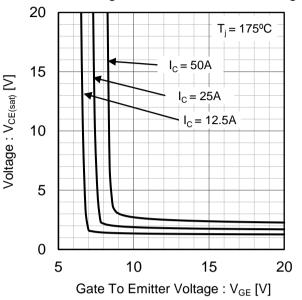


Fig.11 Typical Switching Time vs. Collector Current

10

15

Gate To Emitter Voltage: VGE [V]

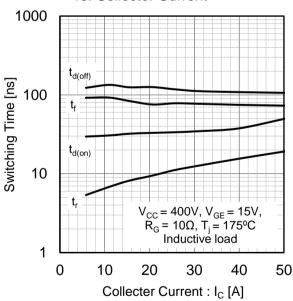
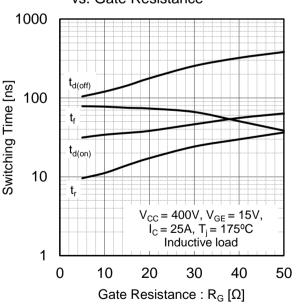


Fig.12 Typical Switching Time vs. Gate Resistance



Collector To Emitter Saturation

20

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 E_{off} 0.1 $V_{CC} = 400V, V_{GE} = 15V,$ $R_G = 10\Omega, T_j = 175^{\circ}C$ Inductive load 0.01 0 10 20 30 40 50

Collecter Current : I_C [A]

Fig.14 Typocal Switching Energy Losses vs. Gate Resistance 10 Switching Energy Losses [mJ] 1 E_{off} E_{on} 0.1
$$\begin{split} &V_{\text{CC}} = 400\text{V}, \, I_{\text{C}} = 25\text{A}, \\ &V_{\text{GE}} = 15\text{V}, \, T_{\text{j}} = 175^{\circ}\text{C} \\ &\text{Inductive load} \end{split}$$
0.01 0 10 20 30 50 Gate Resistance : $R_G[\Omega]$

Fig.15 Typical Capacitance vs. Collector to Emitter Voltage 10000 \mathbf{C}_{ies} 1000 Capacitance [pF] C_{oes} 100 10 C_{res} f = 1MHz $V_{GE} = 0V$ $T_i = 25^{\circ}C$ 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CE} [V]

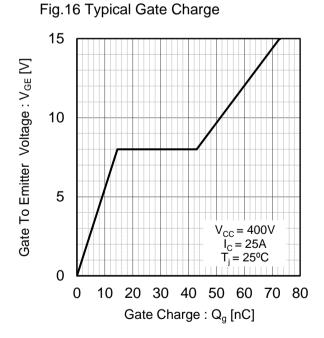


Fig.17 Typical Diode Forward Current vs. Forward Voltage

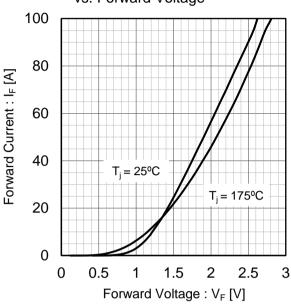


Fig.18 Typical Diode Revese Recovery Time vs. Forward Current

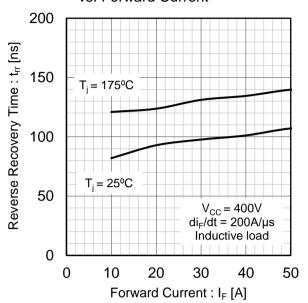


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

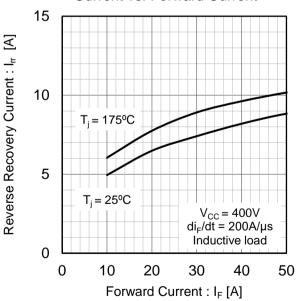


Fig.20 Typical Diode Rrverse Recovery Charge vs. Forward Current

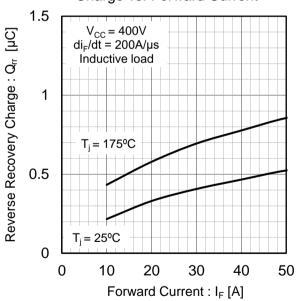


Fig.21 Typical IGBT Transient Thermal Impedance

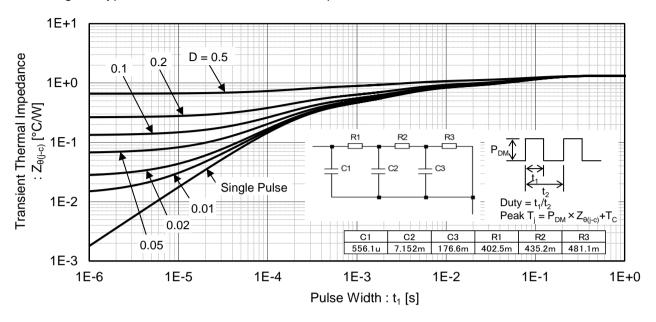
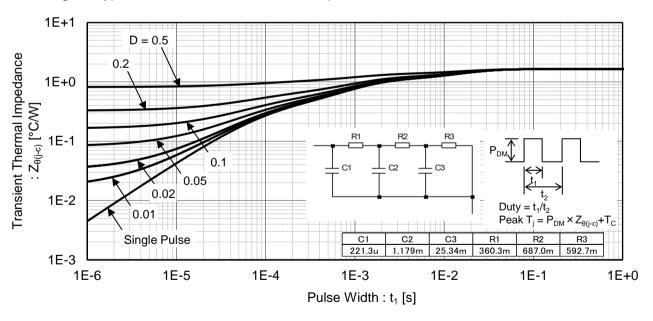


Fig.22 Typical Diode Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

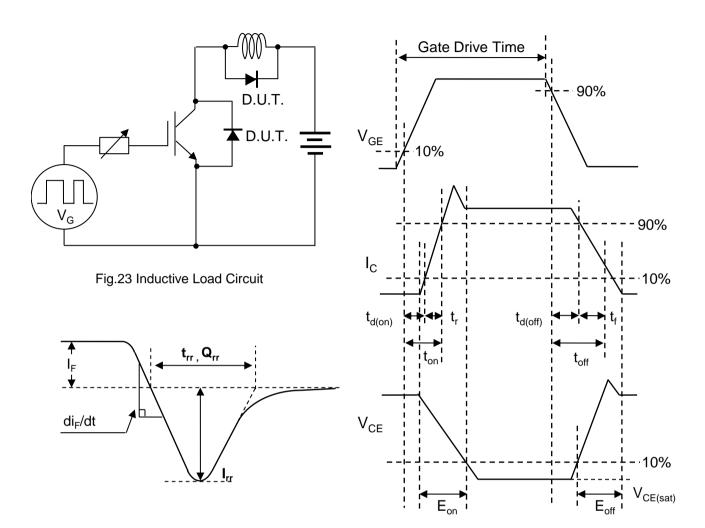


Fig.25 Diode Reverse Recovery Waveform

Fig.24 Inductive Load Waveform

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