

Nch 40V 95A Power MOSFET

V _{DSS}	40V
R _{DS(on)} (Max.)	3.6mΩ
I _D	±95A
P _D	59W

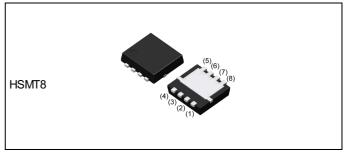
● Features

- 1) Low on resistance
- 2) High Power small mold Package (HSMT8)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen Free

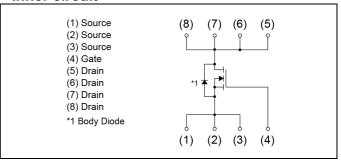
Application

Switching

Outline



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	3000
	Taping code	TB1
	Marking	G040BG

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Para	meter	Symbol	Value	Unit
Drain - Source voltage		V _{DSS}	40	V
Continuous drain surrent	Silicon limit (V _{GS} =10V)	I _D *1	±95	Α
Continuous drain current	T _a = 25°C (V _{GS} =10V)	I _D *2	±40	Α
Pulsed drain current		I _{DP} *3	±380	Α
Gate - Source voltage		V_{GSS}	±20	V
Avalanche current, single p	ulse	I _{AS} *4	27	Α
Avalanche energy, single p	ulse	E _{AS} *4	56	mJ
Dower dissinction		P _D *2	59	W
Power dissipation		P _D *5	2.0	W
Junction temperature		T _j	150	°C
Operating junction and stor	T _{stg}	-55 to +150	°C	

●Thermal resistance

Doromotor	Curah al	Values			I India
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *2	-	-	2.1	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	62.5	°C/W

● Electrical characteristics (T_a = 25°C)

Davanastan	Cymaele ed	Conditions	Values			Linit	
Parameter	Symbol	mbol Conditions -		Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	40	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j} I_D = 1 \text{mA}$ referenced to 25°C		-	28.9	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 40V, V _{GS} = 0V	-	-	2	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	1	1	±200	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 1mA$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	-4.6	-	mV/°C	
Static drain - source	D *6	V _{GS} = 10V, I _D = 40A	-	2.8	3.6	C	
on - state resistance	R _{DS(on)} *6	V _{GS} = 4.5V, I _D = 20A	-	4.7	6.5	mΩ	
Gate resistance	R_{G}	-	-	2.0	-	Ω	
Forward Transfer Admittance	Y _{fs} *6	V _{DS} = 5V, I _D = 20A	19	-	-	S	

^{*1} Limited by silicon chip capability.

^{*2} T_c =25°C, Limited only by maximum temperature allowed.

^{*3} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*4} L \simeq 0.1mH, V_{DD} = 20V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*5} Mounted on a Cu board (40×40×0.8mm)

^{*6} Pulsed

● Electrical characteristics (T_a = 25°C)

Daramatar	Cymahal	Symbol Conditions		Values			
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	1580	-		
Output capacitance	C _{oss}	V _{DS} = 20V	-	725	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	85	-		
Turn - on delay time	t _{d(on)} *6	V _{DD} ≈ 20V,V _{GS} = 10V	-	15	-		
Rise time	t r*6	I _D = 20A	1	15	1	no	
Turn - off delay time	t _{d(off)} *6	$R_L \simeq 1\Omega$	-	51	-	ns	
Fall time	t _f *6	$R_G = 10\Omega$	-	17	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Daramatar	Cymahal	Canditiana		Values			l limit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Total gate charge	O *6		V _{GS} = 10V	-	25.0	-	
Total gate charge	Q _g *6	V _{DD} ≈ 20V		-	12.5	-	5 C
Gate - Source charge	Q _{gs} *6	I _D = 40A	V _{GS} = 4.5V	-	3.9	-	nC
Gate - Drain charge	Q _{gd} *6			-	4.5	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter			Values			Unit
			Min.	Тур.	Max.	Offic
Continuous forward current	I _S	T _a = 25°C	-	-	40	Α
Pulse forward current	I _{SP} *3	1 _a – 25 C	-	-	380	Α
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = 40A	-	-	1.2	V
Reverse recovery time	t _{rr} *6	I _S = 40A, V _{GS} =0V	-	37.1	-	ns
Reverse recovery charge	Q _{rr} *6	di/dt = 100A/μs	-	34.2	-	nC

Fig.1 Power Dissipation Derating Curve

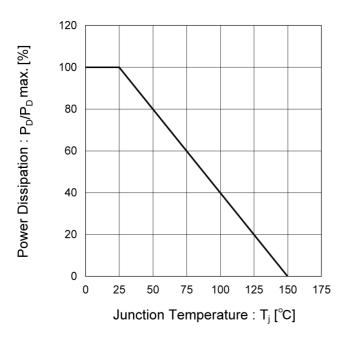
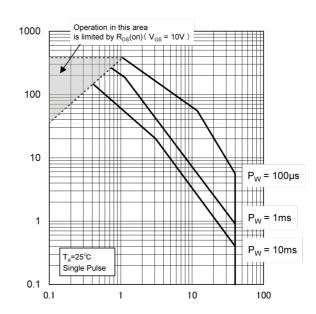


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : $V_{DS}[V]$

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

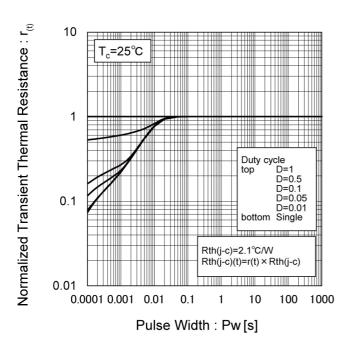


Fig.4 Single Pulse Maximum Power Dissipation

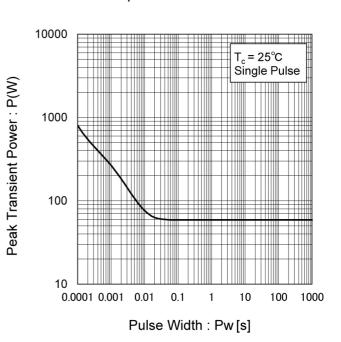


Fig.5 Typical Output Characteristics(I)

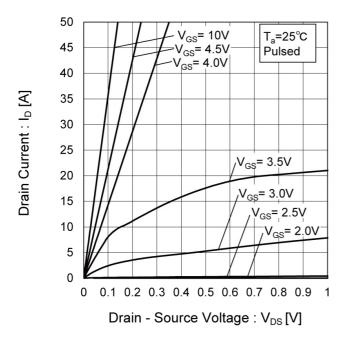


Fig.6 Typical Output Characteristics(II)

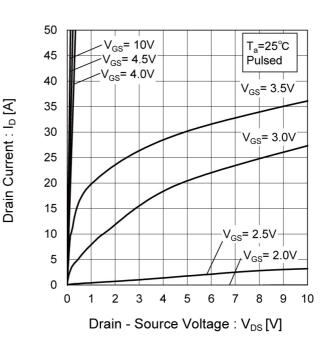


Fig.7 Normalized Breakdown Voltage vs. Junction Temperature

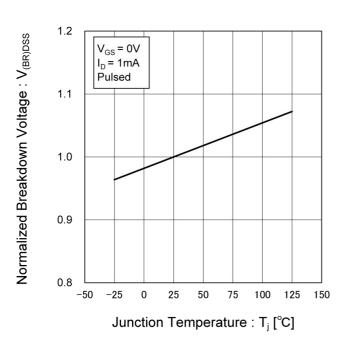


Fig.8 Typical Transfer Characteristics

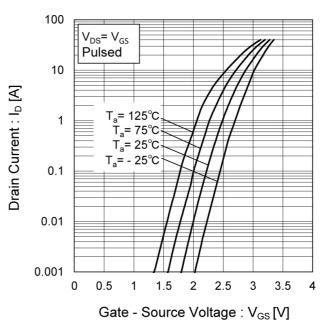


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

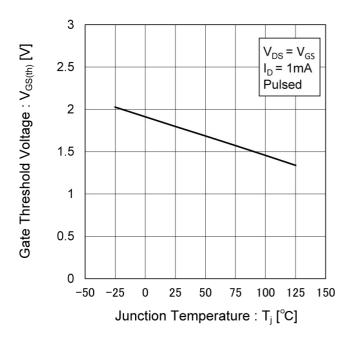


Fig.10 Forward Transfer Admittance vs.
Drain Current

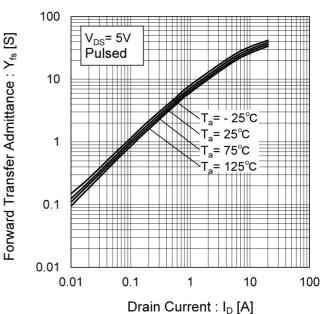


Fig.11 Drain Current Derating Curve

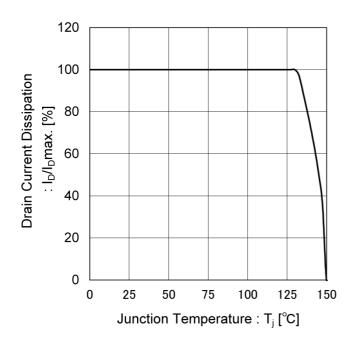


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

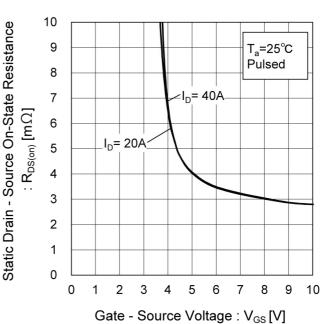


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

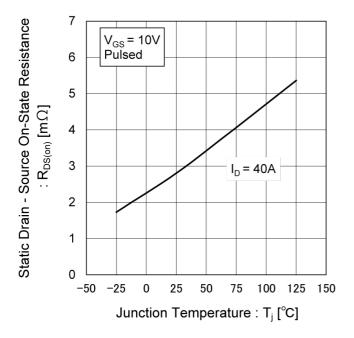


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

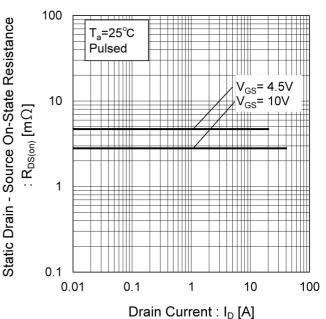


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

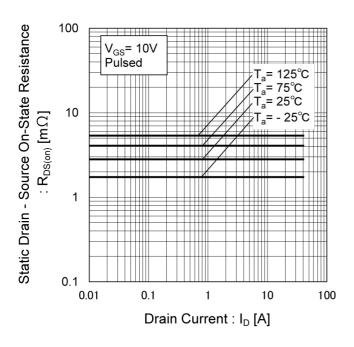


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

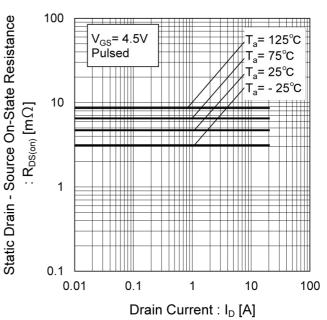


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

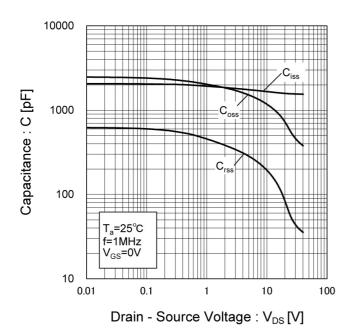


Fig.18 Switching Characteristics

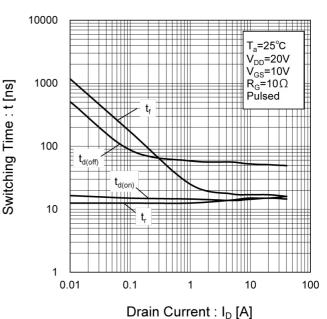


Fig.19 Typical Gate Charge

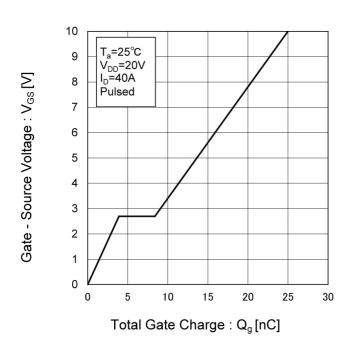
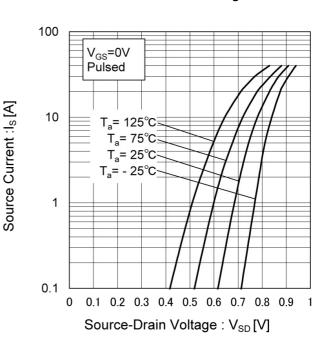


Fig.20 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

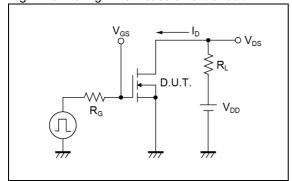


Fig.1-2 Switching Waveforms

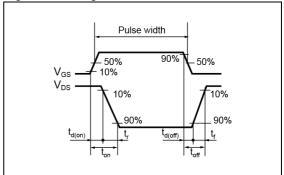


Fig.2-1 Gate Charge Measurement Circuit

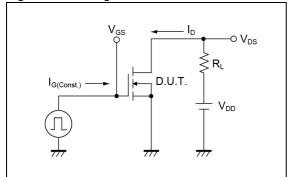


Fig.2-2 Gate Charge Waveform

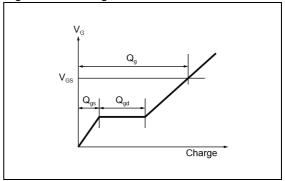


Fig.3-1 Avalanche Measurement Circuit

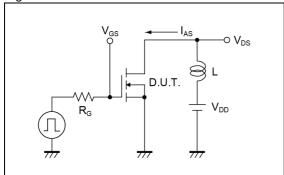
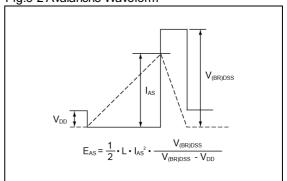


Fig.3-2 Avalanche Waveform



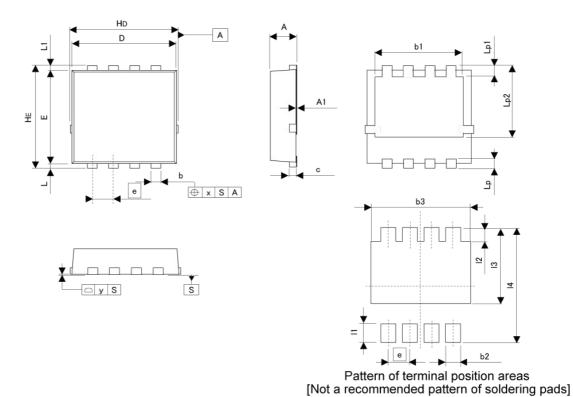
Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions

HSMT8 (TB1)

(3.3x3.3)



[Not a recommended pattern of soldering pads]

DIM	MILIME	TERS	INCI	HES	
DIIVI	MIN	MAX	MIN	MAX	
Α	0.65	0.85	0.026	0.033	
A1	0.00	0.10	0.000	0.004	
b	0.24	0.42	0.009	0.017	
b1	2.29	2.69	0.090	0.106	
С	0.05	0.25	0.002	0.010	
D	3.05	3.25	0.120	0.128	
Е	2.95	3.15	0.116	0.124	
е	0.	65	0.0	.026	
HD	3.20	3.40	0.126	0.134	
HE	3.20	3.40	0.126	0.134	
L	0.05	0.23	0.002	0.009	
L1	0.05	0.23	0.002	0.009	
Lp	0.20	0.60	0.008	0.024	
Lp1	0.20	0.60	0.008	0.024	
Lp2	1.83	2.63	0.072	0.104	
Х	-	0.10	-	0.004	
у	-	0.10	-	0.004	

DIM	MILIMETERS INC		HES	
DIIVI	MIN	MAX	MIN	MAX
b2	-	0.52	(1-)	0.020
b3	-	2.79	-	0.110
11	-	0.70	2-2	0.028
12	(-)	0.70	(=)	0.028
13	12/	2.53	2	0.100
14	-	3.60	(-)	0.142

Dimension in mm/inches



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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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