| V <sub>DSS</sub>           | 40V    |
|----------------------------|--------|
| R <sub>DS(on)</sub> (Max.) | 1.86mΩ |
| I <sub>D</sub>             | ±120A  |
| P <sub>D</sub>             | 178W   |

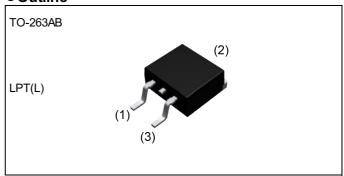
## Features

- 1) Low on resistance
- 2) High power small mold package (LPTL)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen free
- 5) 100% UIS tested

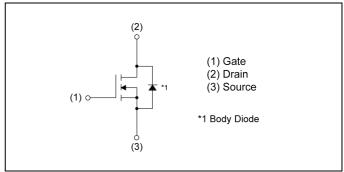
## Application

Switching

## Outline



## •Inner circuit



Packaging specifications

|      | Packing                   | Embossed<br>Tape |
|------|---------------------------|------------------|
| Туре | Basic ordering unit (pcs) | 1000             |
|      | Taping code               | TLL              |
|      | Marking                   | RJ1G12BGN        |

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

| Parameter                                  | Symbol                | Value             | Unit |   |
|--|-----------------------|-------------------|------|---|
| Drain - Source voltage                     |                       | V <sub>DSS</sub>  | 40   | V |
| Continuous drain current                   | V <sub>GS</sub> = 10V | I <sub>D</sub> *1 | ±120 | Α |
| Pulsed drain current                       | I <sub>DP</sub> *2    | ±240              | Α    |   |
| Gate - Source voltage                      | V <sub>GSS</sub>      | ±20               | V    |   |
| Avalanche current, single pulse            | I <sub>AS</sub> *3    | 40                | Α    |   |
| Avalanche energy, single pulse             | E <sub>AS</sub> *3    | 117               | mJ   |   |
| Power dissipation                          | P <sub>D</sub> *1     | 178               | W    |   |
| Junction temperature                       | T <sub>j</sub>        | 150               | °C   |   |
| Operating junction and storage temperature | T <sub>stg</sub>      | -55 to +150       | °C   |   |

## ●Thermal resistance

| Parameter                           | Cymah al             | Values |      |      | l leit |
|-------------------------------------|----------------------|--------|------|------|--------|
|                                     | Symbol               | Min.   | Тур. | Max. | Unit   |
| Thermal resistance, junction - case | R <sub>thJC</sub> *1 | -      | ı    | 0.7  | °C/W   |

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

| Davamatav  | Cymah ol                                  | Conditions   | Values |      |      | Unit  |  |
|--|---|--|--------|------|------|-------|--|
| Parameter  | Symbol Conditions —                       |  | Min.   | Тур. | Max. | Offic |  |
| Drain - Source breakdown voltage                               | V <sub>(BR)DSS</sub>                      | $V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$                                       |        | -    | -    | V     |  |
| Breakdown voltage temperature coefficient                      | $\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$ | $\frac{\Delta V_{(BR)DSS}}{\Delta T_j} I_D = 1 \text{mA}$ referenced to 25°C |        | 26.2 | -    | mV/°C |  |
| Zero gate voltage drain current                                | I <sub>DSS</sub>                          | $I_{DSS}$ $V_{DS} = 40V, V_{GS} = 0V$  |        | -    | 1    | μA    |  |
| Gate - Source leakage current                                  | I <sub>GSS</sub>                          | $I_{GSS}$ $V_{GS} = \pm 20V$ , $V_{DS} = 0V$                                 |        | 1    | ±500 | nA    |  |
| Gate threshold voltage   | V <sub>GS(th)</sub>                       | $V_{DS} = V_{GS}$ , $I_D = 2mA$  | 1.0    | -    | 2.5  | V     |  |
| Gate threshold voltage temperature coefficient $\Delta V_{CS}$ |   | I <sub>D</sub> = 1mA<br>referenced to 25°C                                   | -      | -4.9 | -    | mV/°C |  |
| Static drain - source  | D *4                                      | V <sub>GS</sub> = 10V, I <sub>D</sub> = 50A                                  | -      | 1.38 | 1.86 | O     |  |
| on - state resistance  | R <sub>DS(on)</sub> *4                    | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 50A                                 | -      | 1.54 | 2.08 | mΩ    |  |
| Gate resistance  | R <sub>G</sub>                            | R <sub>G</sub> f = 1MHz, open drain  |        | 1.2  | -    | Ω     |  |
| Forward Transfer<br>Admittance                                 | Y <sub>fs</sub>  *4                       | V <sub>DS</sub> = 5V, I <sub>D</sub> = 40A                                   |        | -    | -    | S     |  |

<sup>\*1</sup> Tc=25°C, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw $\leq$ 10 $\mu$ s , Duty cycle $\leq$ 1%

<sup>\*3</sup> L  $\simeq$  0.1mH, V<sub>DD</sub> = 20V, R<sub>G</sub> = 25 $\Omega$ , Starting T<sub>j</sub> = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

| Davameter                    | Cumbal                 | Conditions                                  |      | Unit  |      |       |  |
|------------------------------|------------------------|---|------|-------|------|-------|--|
| Parameter                    | Symbol Conditions —    |   | Min. | Тур.  | Max. | Urill |  |
| Input capacitance            | C <sub>iss</sub>       | V <sub>GS</sub> = 0V                        | -    | 12500 | -    |       |  |
| Output capacitance           | C <sub>oss</sub>       | V <sub>DS</sub> = 20V                       | -    | 1900  | -    | pF    |  |
| Reverse transfer capacitance | C <sub>rss</sub>       | f = 1MHz                                    | -    | 680   | -    |       |  |
| Turn - on delay time         | t <sub>d(on)</sub> *4  | V <sub>DD</sub> ≈ 20V,V <sub>GS</sub> = 10V | 1    | 40    | -    |       |  |
| Rise time                    | t <sub>r</sub> *4      | I <sub>D</sub> = 50A                        | -    | 33    | -    |       |  |
| Turn - off delay time        | t <sub>d(off)</sub> *4 | $R_L \simeq 0.4\Omega$                      | -    | 230   | -    | ns    |  |
| Fall time                    | t <sub>f</sub> *4      | $R_G = 10\Omega$                            | -    | 130   | -    |       |  |

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

| Darameter            | Cymahal            | Conditions            |                        | Values |      |      | l limit    |
|----------------------|--------------------|-----------------------|------------------------|--------|------|------|------------|
| Parameter            | Symbol             |                       |                        | Min.   | Тур. | Max. | Unit       |
| T                    | Qg*4               |                       | V <sub>GS</sub> = 10V  | -      | 165  | -    |            |
| Total gate charge    |                    | V <sub>DD</sub> ≃ 20V |                        | -      | 82   | -    | <b>5</b> C |
| Gate - Source charge | Q <sub>gs</sub> *4 | I <sub>D</sub> = 50A  | V <sub>GS</sub> = 4.5V | -      | 31   | -    | nC         |
| Gate - Drain charge  | Q <sub>gd</sub> *4 |                       |                        | -      | 24   | -    |            |

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

| Davamatav                  | Cymabal            | Conditions                                |      | l limit |      |      |
|----------------------------|--------------------|---|------|---------|------|------|
| Parameter                  | Symbol Conditions  |   | Min. | Тур.    | Max. | Unit |
| Continuous forward current | I <sub>S</sub>     | T 05°0                                    | -    | -       | 120  | Α    |
| Pulse forward current      | I <sub>SP</sub> *2 | T <sub>a</sub> = 25°C                     | -    | -       | 240  | Α    |
| Forward voltage            | V <sub>SD</sub> *4 | $V_{GS} = 0V, I_{S} = 50A$                | -    | -       | 1.2  | V    |
| Reverse recovery time      | t <sub>rr</sub> *4 | I <sub>S</sub> = 50A, V <sub>GS</sub> =0V | -    | 295     | -    | ns   |
| Reverse recovery charge    | Q <sub>rr</sub> *4 | di/dt = 100A/µs                           | -    | 92      | -    | nC   |

Fig.1 Power Dissipation Derating Curve

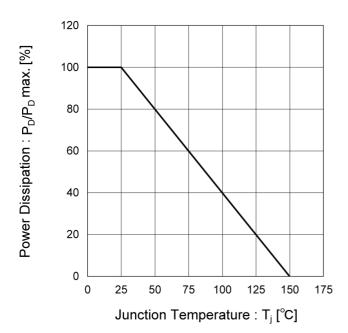
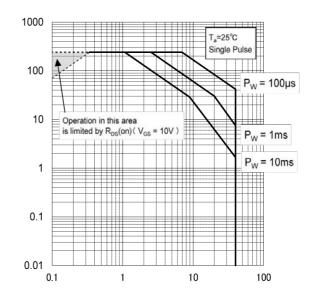


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

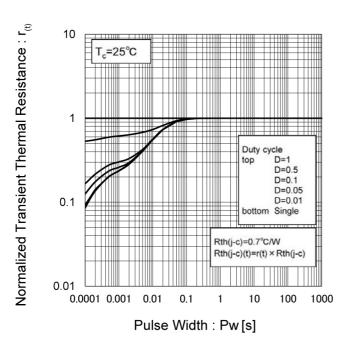


Fig.4 Single Pulse Maximum Power dissipation

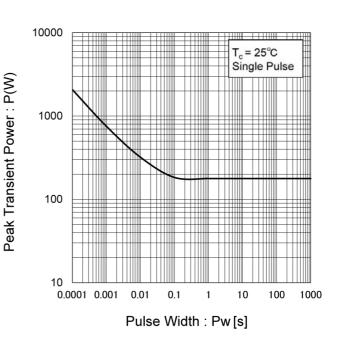
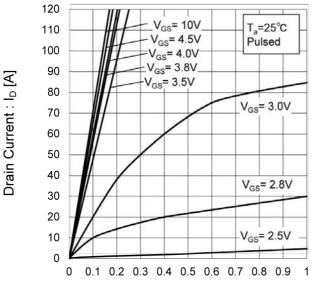
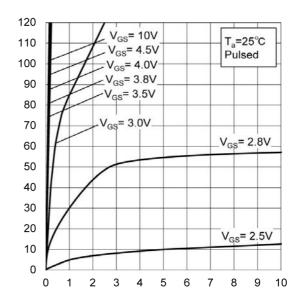


Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs.
Junction Temperature

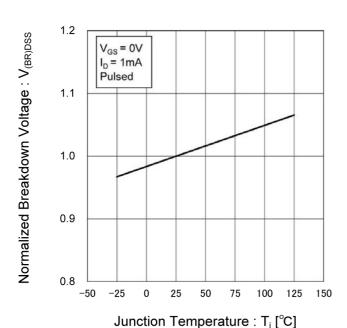


Fig.8 Typical Transfer Characteristics

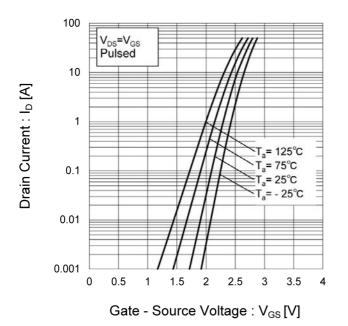
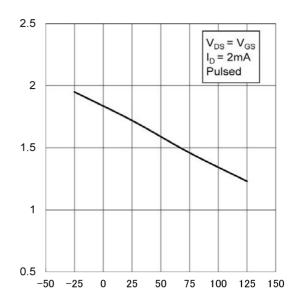


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage: VGS(th) [V]

6/11

Junction Temperature : T<sub>j</sub> [°C]

Fig.10 Forward Transfer Admittance vs.
Drain Current

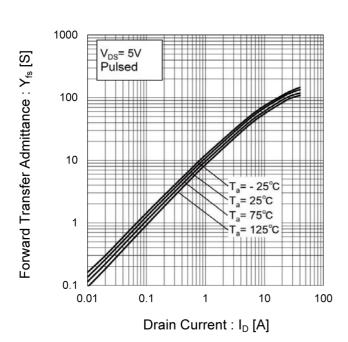


Fig.11 Drain Current Derating Curve

120 Drain Current Dissipation: I<sub>D</sub>/I<sub>D</sub>max. [%] 100 80 60 40 20 0 -25 0 25 75 100 125 150 Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

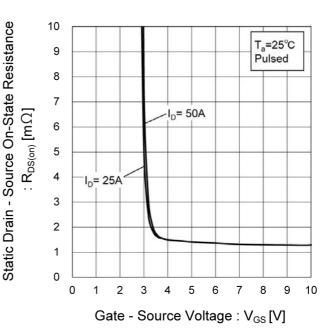


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

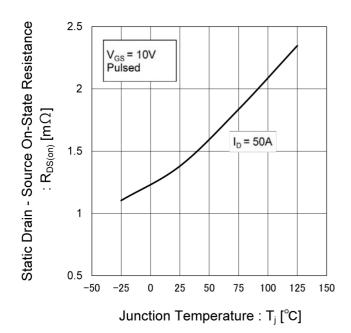


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

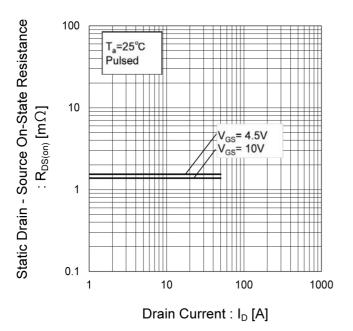


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

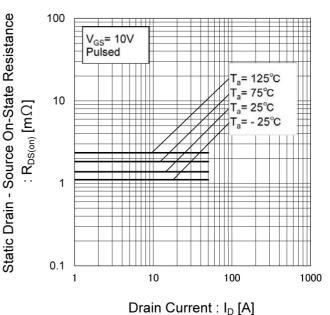


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

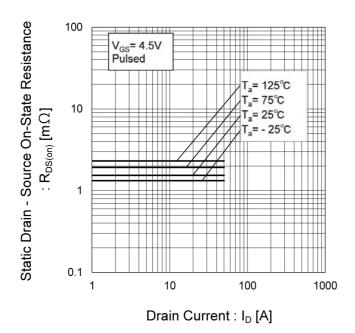
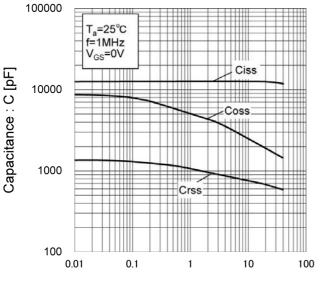


Fig.17 Typical Capacitance vs. Drain - Source Voltage



Drain - Source Voltage: V<sub>DS</sub> [V]

Fig.18 Switching Characteristics

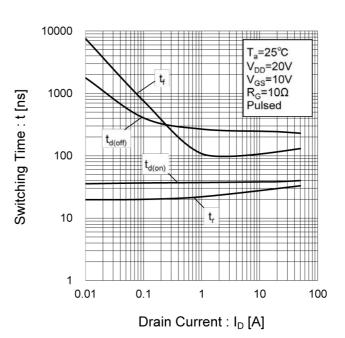


Fig.19 Dynamic Input Characteristics

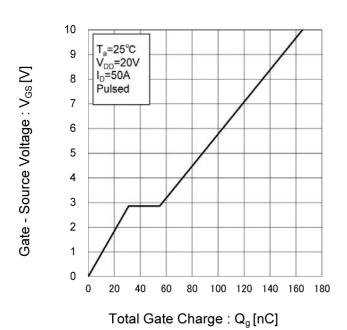
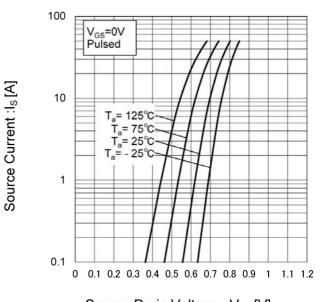


Fig.20 Source Current vs. Source Drain Voltage



Source-Drain Voltage: V<sub>SD</sub>[V]

## Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

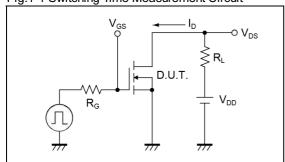


Fig.2-1 Gate Charge Measurement Circuit

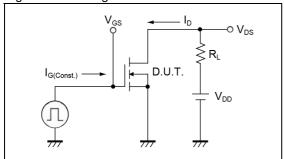


Fig.3-1 Avalanche Measurement Circuit

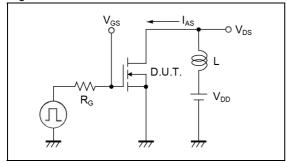


Fig.1-2 Switching Waveforms

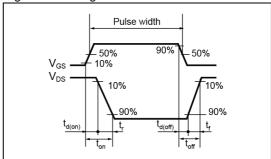


Fig.2-2 Gate Charge Waveform

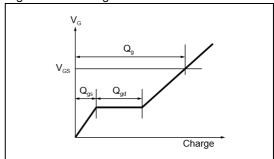
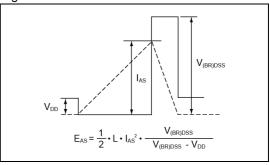
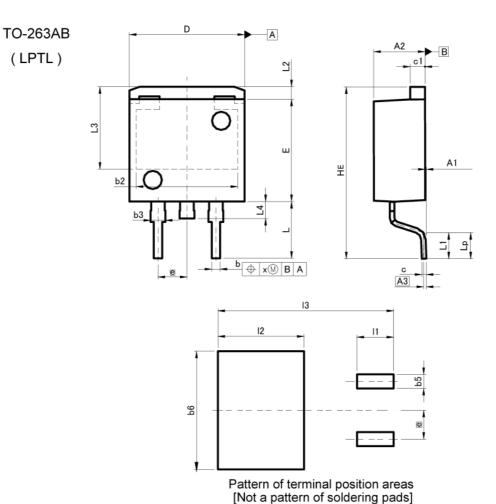


Fig.3-2 Avalanche Waveform



## Dimensions



**MILIMETERS** INCHES DIM MIN 0.00 MAX 0.30 MIN MAX A1 0.000 0.012 4.70 0.169 4.30 0.185 A3 0.010 0.25 0.68 0.98 0.027 0.039 b2 8.90 0.350 b3 0.045 0.057 1.14 1.44 0.024 0.059 0.30 0.012 C 0.60 1.10 9.80 8.80 1.50 10.40 9.20 0.043 0.386 c1 D 0.409 E 0.346 0.362 0.100 0.606 0.209 0.106 14.80 4.70 2.10 15.40 5.30 2.70 HE 0.583 0.185 L1 0.083 L2 1.10 0.043 L3 7.25 1.50 0.285 L4 0.059 2.00 0.25 0.079 0.010 Lp 2.60 0.102

| DIM  | MILIM           | ETERS INC |          | HES   |
|------|-----------------|-----------|----------|-------|
| DIM  | MIN             | MAX       | MIN      | MAX   |
| b5   |                 | 1.23      | æ        | 0.049 |
| b6   | -               | 10.40     | <b>—</b> | 0.409 |
| - 11 | -               | 3.20      | -        | 0.126 |
| 12   | 2.75            | 7.55      | 2.72     | 0.297 |
| 13   | 5 <del>-1</del> | 15.40     | -        | 0.606 |

Dimension in mm/inches



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|---------|----------|------------|----------|
| CLASSⅢ  | CLASSIII | CLASS II b | CLASSIII |
| CLASSIV | CLASSIII | CLASSIII   | CLASSIII |

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  - [f] Sealing or coating our Products with resin or other coating materials
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  - [h] Use of the Products in places subject to dew condensation
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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  - [c] the Products are exposed to direct sunshine or condensation
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- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
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