2.5V Drive Nch MOS FET

RJP020N06

Structure

Silicon N-channel MOS FET

● Features

- 1) Low On-resistance.
- 2) Low voltage drive (2.5V drive).

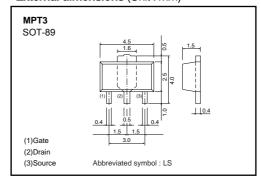
Applications

Switching

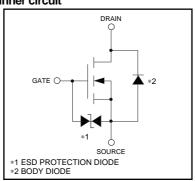
Packaging specifications

	Package	Taping
Type	Code	T100
	Basic ordering unit (pieces)	1000
RJP020N06		0

●External dimensions (Unit : mm)



●Inner circuit



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		V _{DSS}	60	V
Gate-source voltage		V _{GSS}	±12	V
Drain current	Continuous	ID	±2.0	Α
Drain current	Pulsed	I _{DP} *1	±8.0	Α
Source current	Continuous	Is	2.0	А
(Body diode)	Pulsed	Isp *1	8.0	Α
Total power dissipation		Pp	500	mW
		PD	2 *2	W
Channel temperature	Tch	150	°C	
Range of storage temperature		Tstg	-55 to +150	°C

Thermal resistance

Parameter	Symbol	Limits	Unit	
Channel to ambient	Dth(oh o)	250	°C/W	
Channel to ambient	Rth(ch-a)	62.5 *	°C/W	

^{*} When mounted on a 40×40×0.7mm ceramic board

^{*1} Pw≤10μs, Duty cycle≤1% *2 When mounted on a 40×40×0.7mm ceramic board

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	Igss	-	_	±10	μΑ	Vgs= ±12V, Vps=0V
Drain-source breakdown voltage	V _{(BR) DSS}	60	_	_	V	I _D = 1mA, V _{GS} =0V
Zero gate voltage drain current	IDSS	_	_	1	μΑ	V _{DS} = 60V, V _{GS} =0V
Gate threshold voltage	V _{GS (th)}	0.8	_	1.5	V	V _{DS} = 10V, I _D = 1mA
		-	165	240	mΩ	I _D = 2A, V _{GS} = 4.5V
Static drain-source on-state resistance	R _{DS (on)} *	-	170	250	mΩ	I _D = 2A, V _{GS} = 4V
resistance		-	210	300	mΩ	I _D = 2A, V _{GS} = 2.5V
Forward transfer admittance	Y _{fs} *	1.5	_	_	S	V _{DS} = 10V, I _D = 2A
Input capacitance	Ciss	-	160	_	pF	V _{DS} = 10V
Output capacitance	Coss	_	50	_	pF	Vgs=0V
Reverse transfer capacitance	Crss	_	45	_	pF	f=1MHz
Turn-on delay time	t _{d (on)} *	_	8	_	ns	V _{DD} ≒ 30V
Rise time	tr *	_	18	_	ns	ID= 1A
Turn-off delay time	t _{d (off)} *	_	40	_	ns	V _{GS} = 4V R _L =30Ω
Fall time	t _f *	-	20	_	ns	R _G =10Ω
Total gate charge	Qg *	_	5	10	nC	V _{DD} ≒30V
Gate-source charge	Q _{gs} *	_	1	_	nC	V _{GS} = 4V
Gate-drain charge	Q _{gd} *	_	2.5	_	nC	I _D = 2A

*Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsp	-	_	1.2	V	I _S = 2A, V _{GS} =0V

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JAPAN	USA	EU	CHINA	
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CLASSIV	CLASSⅢ	CLASSⅢ		

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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