Nch 30V 7.5A Small Signal MOSFET

V _{DSS}	30V
R _{DS(on)} (Max.)	17mΩ
I _D	±7.5A
P _D	1.5W

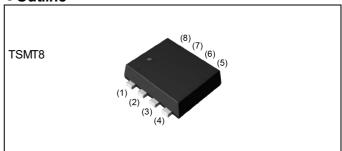
● Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSST8).
- 4) Pb-free lead plating; RoHS compliant

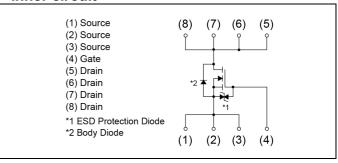
Application

DC/DC converters

Outline



●Inner circuit



Packaging specifications

	Jiiig opcomoduciio	
	Packing	Embossed Tape
	Reel size (mm)	180
Type	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	XR

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	30	V
Continuous drain current	I _D	±7.5	Α
Pulsed drain current	I _{DP} *1	±30	Α
Gate - Source voltage	V _{GSS}	±20	V
Dower discinction	P _D *2	1.5	W
Power dissipation	P _D *3	1.1	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Deremeter	Cumb ol	Values			Lloit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registance junction, ambient	R _{thJA} *2	1	1	83.3	°C/W
Thermal resistance, junction - ambient	R _{thJA} *3	1	1	113	°C/W

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	34.15	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V	1	-	1	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	1	-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = 10V, I_{D} = 1mA$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	-2.34	-	mV/°C	
		V _{GS} = 10V, I _D = 7.5A	-	12	17		
Static drain - source on - state resistance	R _{DS(on)} *4	V _{GS} = 4.5V, I _D = 7.5A	-	17	24	mΩ	
		$V_{GS} = 4.0V, I_D = 7.5A$	1	19	27		
Gate resistance	R _G f = 1MHz, open drain		1	2.5	1	Ω	
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = 10V, I _D = 7.5A	4.5	-	-	S	

^{*1} Pw ≤ 10µs, Duty cycle ≤ 1%

^{*2} Mounted on a ceramic board (30×30×0.8mm)

^{*3} Mounted on a FR4 (25×25×0.8mm,Cu pad:625mm²)

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

Davarantari	Curahal	Conditions	Values			Lloit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	440	-		
Output capacitance	C _{oss}	V _{DS} = 10V	-	170	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	85	-		
Turn - on delay time	t _{d(on)} *4	V _{DD} ≈ 15V,V _{GS} = 10V	-	7	-		
Rise time	t _r *4	I _D = 3.75A	-	25	-	no	
Turn - off delay time	t _{d(off)} *4	$R_L \simeq 4\Omega$	-	35	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	7	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ u	,				
Parameter	Symbol	Conditions	Values			Unit
raianietei	Symbol Conditions		Min.	Тур.	Max.	Offic
Total gate charge	Qg*4	V _{DD} ≃ 15V,	-	6.8	-	
Gate - Source charge	Q _{gs} *4	$I_D = 7.5A,$	-	1.6	-	nC
Gate - Drain charge	Q _{gd} *4	$V_{GS} = 5V$	-	2.6	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Daramatar	Symbol	Conditions	Values			Unit	
raianetei	Parameter Symbol Condi		Min.		Max.	Unit	
Continuous forward current	I _S	T = 25°C	-	-	1.25	Α	
Pulse forward current	I _{SP} *1	T _a = 25℃	-	-	30	Α	
Forward voltage	V _{SD} *4	V _{GS} = 0V, I _S = 7.5A	-	-	1.2	V	

Fig.1 Power Dissipation Derating Curve

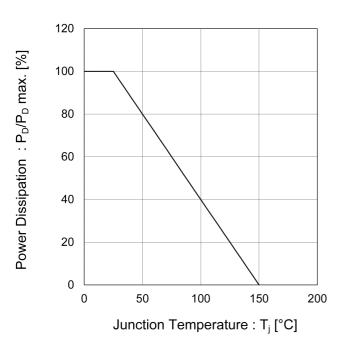
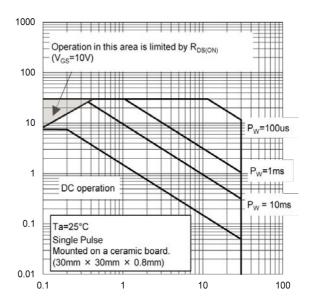


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

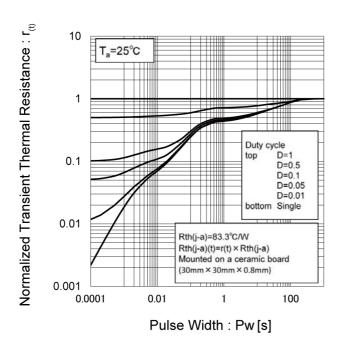
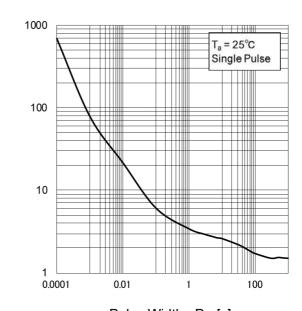


Fig.4 Single Pulse Maximum Power dissipation



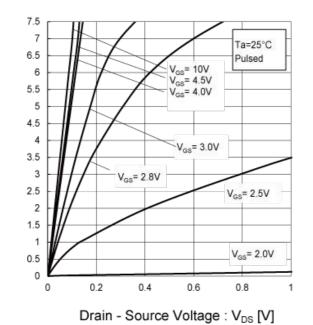
Pulse Width : Pw [s]

Peak Transient Power: P(W)

Drain Current : I_D [A]

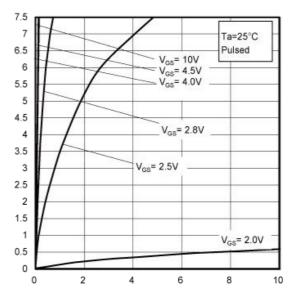
• Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain Current : I_D [A]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : VDS [V]

Fig.7 Breakdown Voltage vs. Junction Temperature

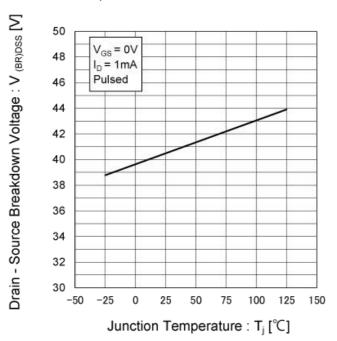
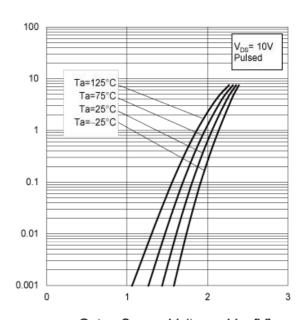


Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : V_{GS} [V]

Drain Current : I_D [A]

Fig.9 Gate Threshold Voltage vs. Junction Temperature

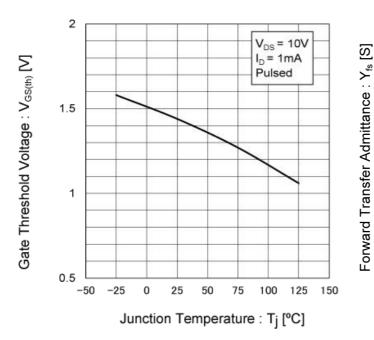


Fig.10 Forward Transfer Admittance vs.
Drain Current

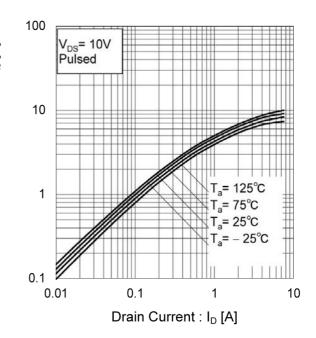


Fig.11 Drain Current Derating Curve

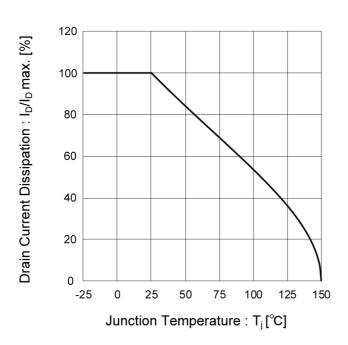
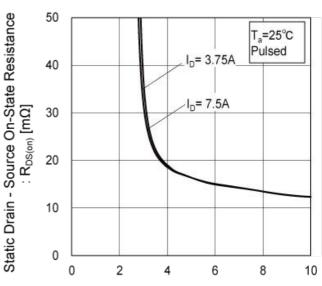


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : VGS [V]

RQ1E075XN

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

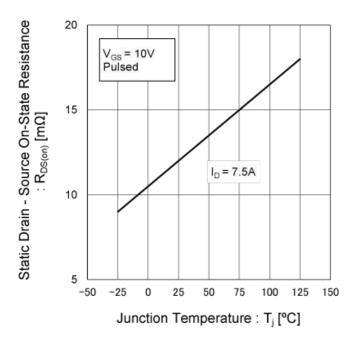


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

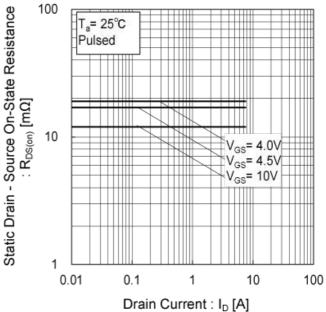


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

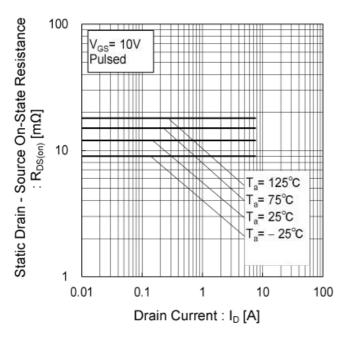


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

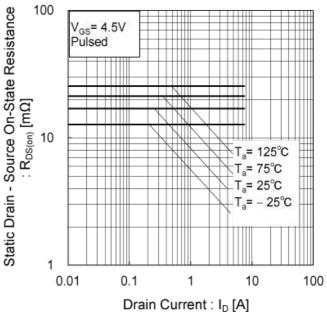


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

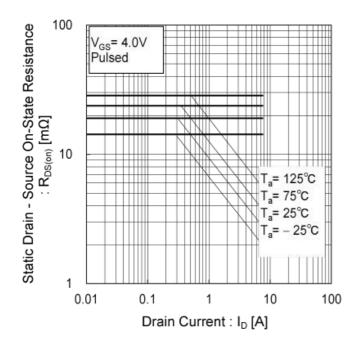


Fig.18 Typical Capacitance vs. Drain - Source Voltage

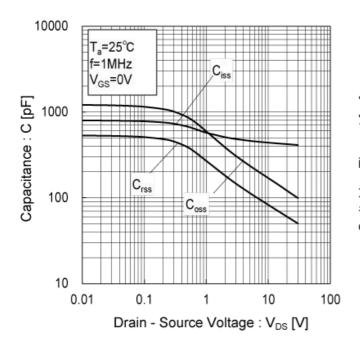


Fig.19 Switching Characteristics

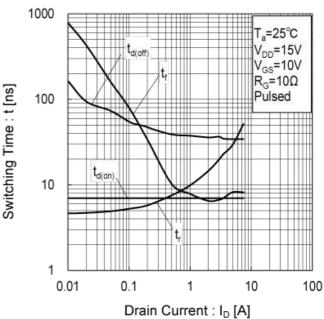


Fig.20 Dynamic Input Characteristics

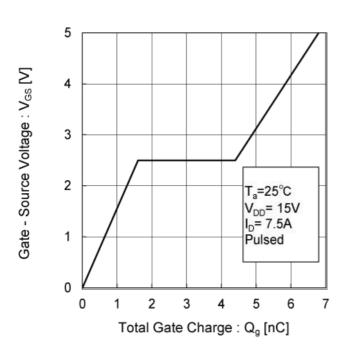
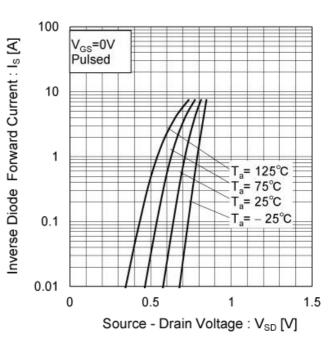


Fig.21 Source Current vs. Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

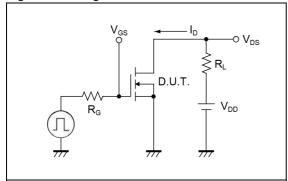


Fig.2-1 Gate Charge Measurement Circuit

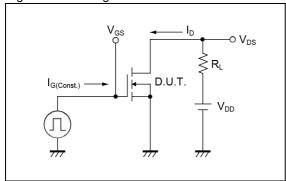


Fig.1-2 Switching Waveforms

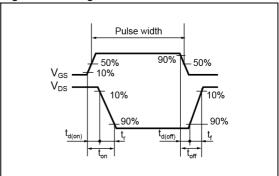
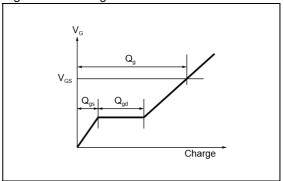


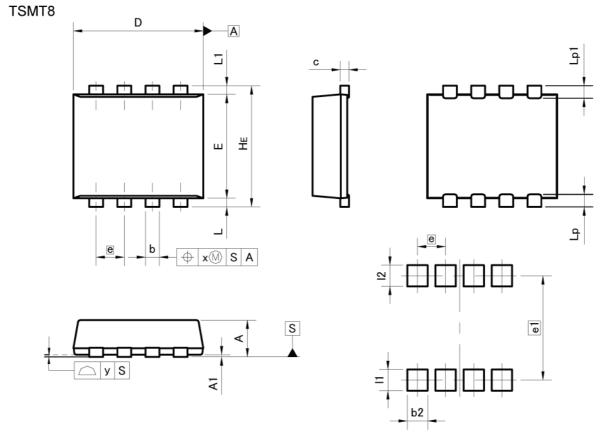
Fig.2-2 Gate Charge Waveform



Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

DIM -	MILIM	ETERS	INC	HES
DIM [MIN	MAX	MIN	MAX
Α	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
С	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.	65	0.0	26
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
x	10 12	0.10	·=	0.004
У	375	0.10	_	0.004

DIM	MILIM	MILIMETERS		HES
DIM	MIN	MAX	MIN	MAX
b2	2 44	0.47	-	0.019
e1	2.	2.41		95
11	-	0.49	-	0.019
12	-	0.49	-	0.019

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCTI
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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