Nch 30V 15A Power MOSFET

| V _{DSS} | 30V |
|----------------------------|--------|
| R _{DS(on)} (Max.) | 15.2mΩ |
| I _D | ±15A |
| P _D | 14W |

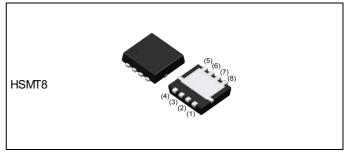
● Features

- 1) Low on resistance
- 2) High Power Package (HSMT8)
- 3) Pb-free lead plating; RoHS compliant
- 4) Halogen Free

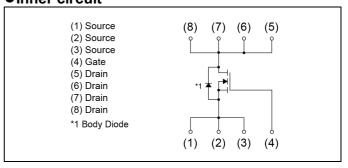
Application

Switching

Outline



●Inner circuit



Packaging specifications

| | Packing | Embossed Tape |
|------|-----------------|------------------|
| | Reel size (mm) | 330 |
| Type | Tape width (mm) | 12 |
| | Quantity (pcs) | 3000 |
| | Taping code | ТВ |
| | Marking | E080BN |

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

| Parameter | Symbol | Value | Unit | |
|-----------------------------------|-----------------------|--------------------|------|----|
| Drain - Source voltage | V _{DSS} | 30 | V | |
| Continuous drain aurent | T _c = 25°C | I _D *1 | ±15 | Α |
| Continuous drain current | T _a = 25°C | I _D | ±8 | Α |
| Pulsed drain current | I _{DP} *2 | ±32 | Α | |
| Gate - Source voltage | V _{GSS} | ±20 | V | |
| Avalanche current, single pulse | I _{AS} *3 | 8.0 | Α | |
| Avalanche energy, single pulse | | E _{AS} *3 | 4.6 | mJ |
| Dower dissination | P _D *1 | 14 | W | |
| Power dissipation | P _D *4 | 2.0 | W | |
| Junction temperature | T _j | 150 | °C | |
| Operating junction and storage te | T _{stg} | -55 to +150 | °C | |

●Thermal resistance

| Doromotor | Curah al | Values | | | I India |
|--|----------------------|--------|------|------|---------|
| Parameter | Symbol | Min. | Тур. | Max. | Unit |
| Thermal resistance, junction - case | R _{thJC} *1 | - | - | 8.9 | °C/W |
| Thermal resistance, junction - ambient | R _{thJA} *4 | - | - | 62.5 | °C/W |

● Electrical characteristics (T_a = 25°C)

| Darameter | Symbol Conditions - | | Values | | | Lloit |
|--|--|---|--------|------|------|-------|
| Parameter Symbol | | Conditions | Min. | Тур. | Max. | Unit |
| Drain - Source breakdown voltage | $V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$ | | 30 | - | - | V |
| Breakdown voltage temperature coefficient | $\frac{\Delta V_{(BR)DSS}}{\Delta T_i} I_D = 1 \text{mA}$ referenced to 25°C | | - | 21 | - | mV/°C |
| Zero gate voltage drain current | I _{DSS} | V _{DS} = 30V, V _{GS} = 0V | 1 | 1 | 1 | μΑ |
| Gate - Source leakage current | I _{GSS} | I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$ | | 1 | ±100 | nA |
| Gate threshold voltage | $V_{GS(th)}$ | V _{DS} = 10V, I _D = 1mA | 1.0 | 1 | 2.5 | V |
| Gate threshold voltage temperature coefficient | $\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$ | I _D = 1mA referenced to 25°C | - | -3 | - | mV/°C |
| Static drain - source | D *5 | V _{GS} = 10V, I _D = 8A | - | 11.0 | 15.2 | O |
| on - state resistance | R _{DS(on)} *5 | V _{GS} = 4.5V, I _D = 8A | - | 16.0 | 22.0 | mΩ |
| Gate resistance | R_{G} | f=1MHz, open drain | - | 3.2 | - | Ω |
| Forward Transfer Admittance | Y _{fs} *5 | V _{DS} = 5V, I _D = 8A | 7 | - | - | S |

^{*1} Tc=25°C, Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*3} L \simeq 0.1mH, V_{DD} = 15V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} Mounted on a Cu board (40×40×0.8mm)

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C)

| Parameter | Symbol | Conditions | Values | | | Unit | |
|------------------------------|--------------------------|-----------------------------------|--------|------|------|-------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | UIIIL | |
| Input capacitance | C _{iss} | V _{GS} = 0V | 1 | 660 | 1 | | |
| Output capacitance | C _{oss} | V _{DS} = 15V | ı | 90 | 1 | pF | |
| Reverse transfer capacitance | C _{rss} | f = 1MHz | ı | 75 | 1 | | |
| Turn - on delay time | t _{d(on)} *5 | $V_{DD} \simeq 15V, V_{GS} = 10V$ | 1 | 8 | 1 | | |
| Rise time | t _r *5 | I _D = 4A | 1 | 20 | 1 | no | |
| Turn - off delay time | t _{d(off)} *5 | $R_L \simeq 3.75\Omega$ | 1 | 33 | 1 | ns | |
| Fall time | t _f *5 | $R_G = 10\Omega$ | - | 7 | - | | |

● Gate charge characteristics (T_a = 25°C)

| Doromotor | Cumbal | Canditions | | Values | | | Lleit |
|----------------------|--------------------|-----------------------|------------------------|--------|------|------|------------|
| Parameter | Symbol Conditions | | OTIS | Min. | Тур. | Max. | Unit |
| Total gate above | O *5 | | V _{GS} = 10V | - | 14.5 | - | |
| Total gate charge | Q_g^{*5} | V _{DD} ≈ 15V | | - | 7.2 | - | " C |
| Gate - Source charge | Q _{gs} *5 | I _D = 8A | V _{GS} = 4.5V | - | 2.0 | - | nC |
| Gate - Drain charge | Q _{gd} *5 | | | - | 3.0 | - | |

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

| Darameter | Symbol | Conditions | Values | | | Unit | |
|----------------------------|--------------------|--|--------|------|------|-------|--|
| Parameter | Symbol | Conditions | Min. | Тур. | Max. | Offic | |
| Continuous forward current | I _S | T = 25°C | - | - | 1.67 | Α | |
| Pulse forward current | I _{SP} *2 | T _a = 25°C | - | - | 32 | Α | |
| Forward voltage | V _{SD} *5 | V _{GS} = 0V, I _S = 1.67A | - | - | 1.2 | V | |

• Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve

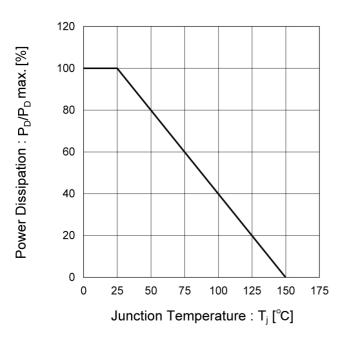
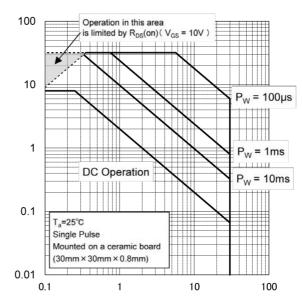


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage: V_{DS}[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

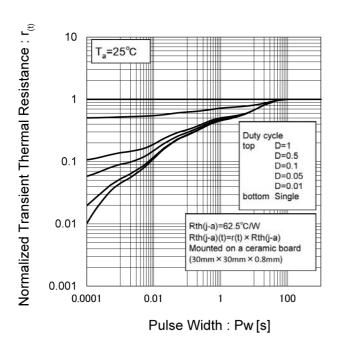
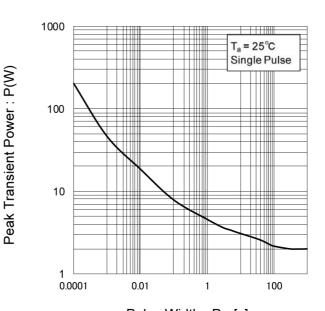


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width : Pw [s]

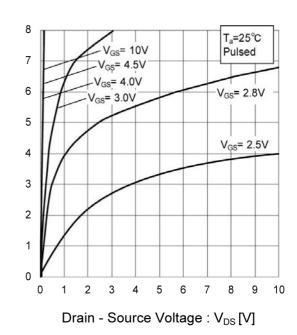
Drain Current : I_D [A]

• Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

8 T_a=25°C 7 Pulsed V_{GS}= 10V V_{GS}= 4.5V V_{GS}= 3.0V 6 V_{GS}= 4.0V 5 V_{GS}= 2.8V 4 3 2 V_{GS}= 2.5V 1 0 0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9 Drain - Source Voltage: V_{DS}[V]

Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.7 Breakdown Voltage vs.
Junction Temperature

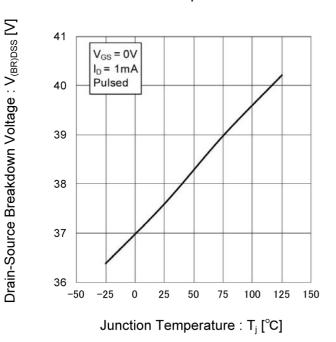
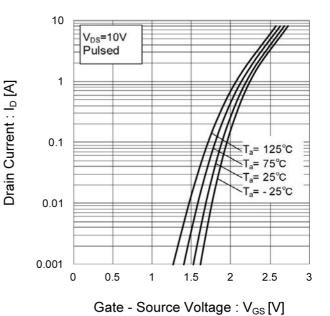


Fig.8 Typical Transfer Characteristics



• Electrical characteristic curves

Fig.9 Gate Threshold Voltage vs.
Junction Temperature

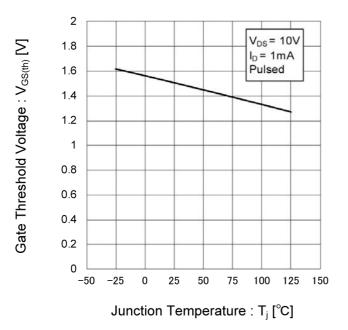


Fig.10 Forward Transfer Admittance vs.
Drain Current

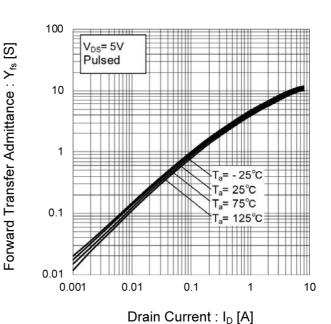


Fig.11 Drain Current Derating Curve

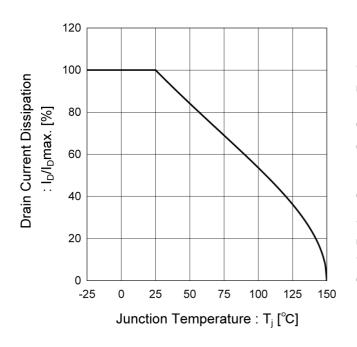
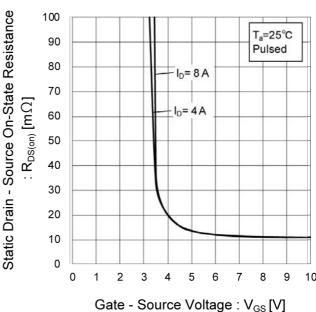


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Electrical characteristic curves

Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature

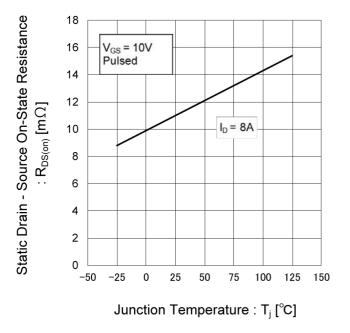


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

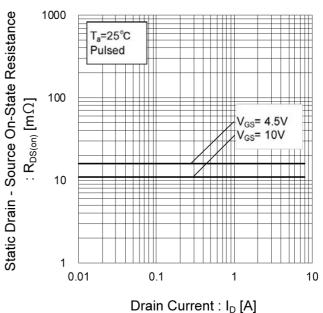
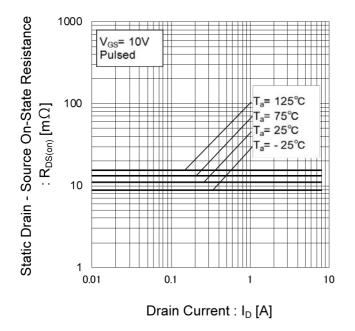
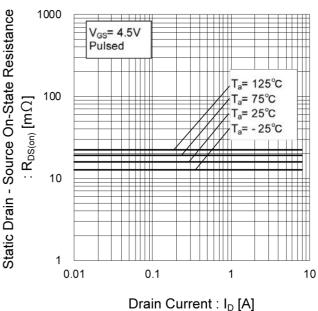


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

Fig.16 Static Drain - Source On - State
Resistance vs. Drain Current (III)





• Electrical characteristic curves

Fig.17 Typical Capacitance vs.

Drain - Source Voltage

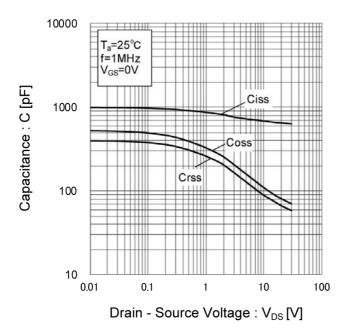


Fig.18 Switching Characteristics

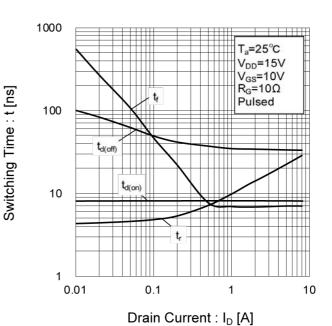


Fig.19 Dynamic Input Characteristics

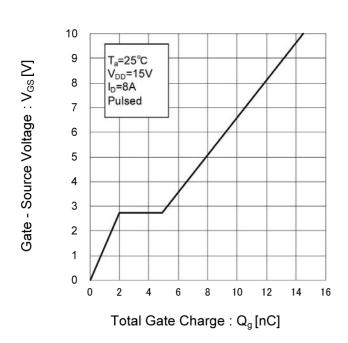
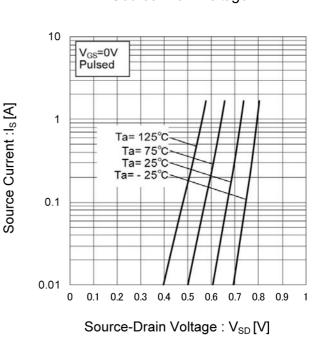


Fig.20 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

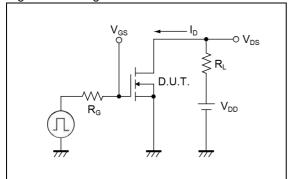


Fig.1-2 Switching Waveforms

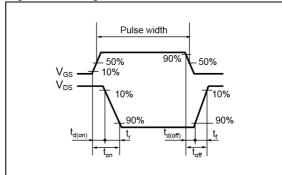


Fig.2-1 Gate Charge Measurement Circuit

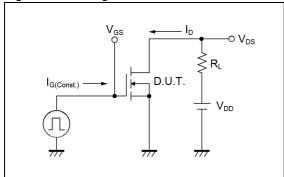


Fig.2-2 Gate Charge Waveform

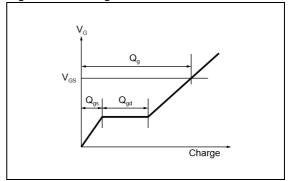


Fig.3-1 Avalanche Measurement Circuit

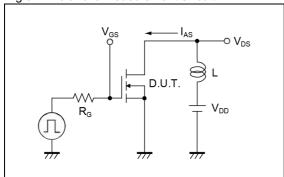
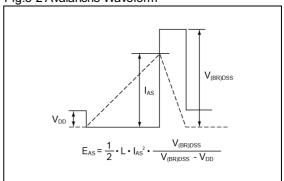


Fig.3-2 Avalanche Waveform



Notice

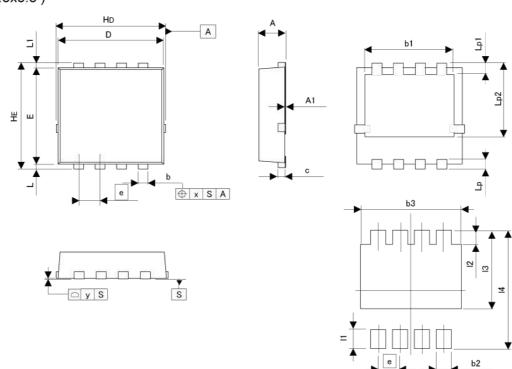
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions

HSMT8

(3.3x3.3)



| DIM | MILIME | TERS | INC | HES |
|-----|--------|------|-------|-------|
| DIM | MIN | MAX | MIN | MAX |
| Α | 0.70 | 0.90 | 0.028 | 0.035 |
| A1 | 0.00 | 0.05 | 0.000 | 0.002 |
| b | 0.27 | 0.37 | 0.011 | 0.015 |
| b1 | 2.50 | 2.70 | 0.098 | 0.106 |
| С | 0.10 | 0.30 | 0.004 | 0.012 |
| D | 3.10 | 3.30 | 0.122 | 0.130 |
| E | 2.90 | 3.10 | 0.114 | 0.122 |
| е | 0. | 65 | 0.026 | |
| HD | 3.20 | 3.40 | 0.126 | 0.134 |
| HE | 3.20 | 3.40 | 0.126 | 0.134 |
| L | 0.07 | 0.25 | 0.003 | 0.010 |
| L1 | 0.07 | 0.25 | 0.003 | 0.010 |
| Lp | 0.20 | 0.40 | 0.008 | 0.016 |
| Lp1 | 0.25 | 0.45 | 0.010 | 0.018 |
| Lp2 | 2.20 | 2.40 | 0.087 | 0.094 |
| х | | 0.10 | | 0.004 |
| У | 0.00 | 0.10 | | 0.004 |

| DIM | MILIMETERS | | INC | HES | |
|-------|--------------------|------|-----|-------|--|
| DIIVI | MIN | MAX | MIN | MAX | |
| b2 | 10 <u>1</u> 25 | 0.47 | 2 | 0.019 | |
| b3 | 1070 | 2.70 | | 0.106 | |
| 11 | (#) | 0.50 | | 0.020 | |
| 12 | (a) | 0.55 | 2 | 0.022 | |
| 13 | 20 5 /5 | 2.40 | 1- | 0.094 | |
| 14 | 527 | 3.40 | - | 0.134 | |

Dimension in mm/inches



Pattern of terminal position areas [Not a pattern of soldering pads]

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|---------|----------|------------|----------|
| CLASSⅢ | CLASSⅢ | CLASS II b | CL ACCTI |
| CLASSIV | CLASSIII | CLASSⅢ | CLASSIII |

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 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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