

RQ3E110AJ

## Nch 30V 24A Middle Power MOSFET

#### Datasheet

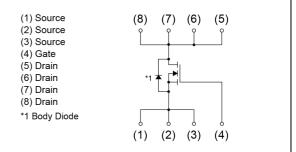
V <sub>DSS</sub>	30V
R <sub>DS(on)</sub> (Max.)	11.7mΩ
Ι <sub>D</sub>	±24A
P <sub>D</sub>	15W

## Features

- 1) Low on resistance.
- 2) Small Surface Mount Package.
- 3) Pb-free lead plating ; RoHS compliant

●Outline	
HSMT8	(4) <sub>(3)(2)(1)</sub> (5) <sub>(6)</sub> (7) <sub>(8)</sub>

#### ●Inner circuit



## Packaging specifications

Туре	Packing	Embossed Tape
	Reel size (mm)	330
	Tape width (mm)	12
	Quantity (pcs)	3000
	Taping code	ТВ
	Marking	E110AJ

## • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Paramete	Symbol	Value	Unit	
Drain - Source voltage	V <sub>DSS</sub>	30	V	
Continuous dusis sumont	$T_c = 25^{\circ}C$	۱ <sub>D</sub> *1	±24	А
Continuous drain current	$T_a = 25^{\circ}C$	I <sub>D</sub>	±11	А
Pulsed drain current	I <sub>DP</sub> *2	±44	Α	
Gate - Source voltage	V <sub>GSS</sub>	±12	V	
Avalanche current, single pulse		I <sub>AS</sub> *3	11	А
Avalanche energy, single pulse		E <sub>AS</sub> *3	4.5	mJ
Power dissipation		P <sub>D</sub> <sup>*1</sup>	15	W
		P <sub>D</sub> *4	2.0	W
Junction temperature		Tj	150	°C
Operating junction and storage t	T <sub>stg</sub>	-55 to +150	°C	

#### •Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}^{*1}$	-	-	8.1	°C/W
Thermal resistance, junction - ambient	$R_{thJA}^{*4}$	-	-	62.5	°C/W

## •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Cumph of	Conditions		Values	Values		
Parameter Symbol		Conditions	Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	18	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±12V, $V_{DS}$ = 0V	-	-	±100	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 1mA$	0.5	-	1.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-1.8	-	mV/°C	
Static drain - source	D *5	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 11A	-	8.8	11.7		
on - state resistance	$R_{DS(on)}^{*5}$	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 5.5A	-	12.6	16.5	mΩ	
Forward Transfer Admittance	Y <sub>fs</sub>  * <sup>5</sup>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 11A	9.8	-	-	S	

\*1 Tc=25°C, Limited only by maximum temperature allowed.

\*2 Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

\*3 L  $\simeq$  0.05mH, V\_{DD} = 15V, R\_G = 25 $\Omega$ , STARTING T\_j = 25°C Fig.3-1,3-2

- \*4 Mounted on a Cu board (40×40×0.8mm)
- \*5 Pulsed



## • Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Sumphal	Conditions	Values			l lait	
Parameter	Symbol	mbol Conditions –		Min. Typ. Max		- Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1500	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15V	-	175	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	115	-		
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 15 V, V_{GS} = 4.5 V$	-	21	-		
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 5.5A	-	21	-	20	
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ≃ 2.73Ω	-	54	-	ns	
Fall time	t <sub>f</sub> *5	R <sub>G</sub> = 10Ω	-	20	-		

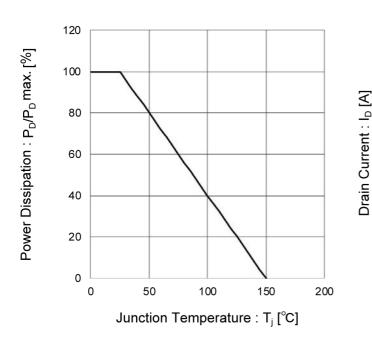
## • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≃ 15V,	-	13.5	-	
Gate - Source charge	$Q_{gs}^{*5}$	I <sub>D</sub> = 11A,	-	3.5	-	nC
Gate - Drain charge	${\sf Q}_{\sf gd}{}^{*5}$	V <sub>GS</sub> = 4.5V	-	3.4	-	

## •Body diode electrical characteristics (Source-Drain) ( $T_a = 25^{\circ}C$ )

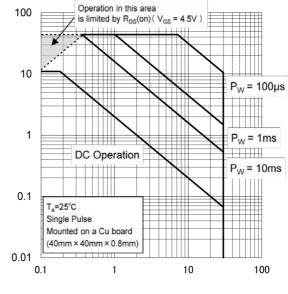
Parameter	Symbol	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ <sub>s</sub>	$T = 25^{\circ}$	-	-	1.67	А
Pulse forward current	I <sub>SP</sub> *2	∙ T <sub>a</sub> = 25°C	-	-	44	А
Forward voltage	$V_{SD}^{*5}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.67A	-	-	1.2	V





## Fig.1 Power Dissipation Derating Curve

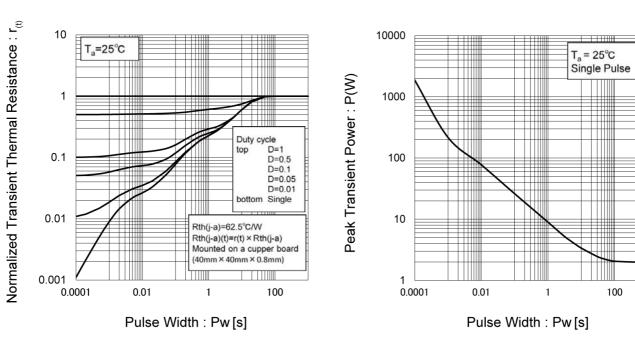
Fig.2 Maximum Safe Operating Area



Drain - Source Voltage :  $V_{\text{DS}}$  [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

# Fig.4 Single Pulse Maximum Power dissipation





11

10

9

8

7

6

5

4

### • Electrical characteristic curves



## Fig.5 Typical Output Characteristics(I)

V<sub>GS</sub>= 4.5V

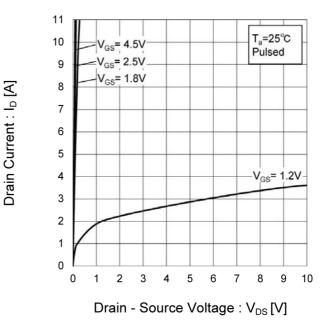
V<sub>GS</sub>= 2.5V

V<sub>GS</sub>= 1.8V-

T<sub>a</sub>=25°C

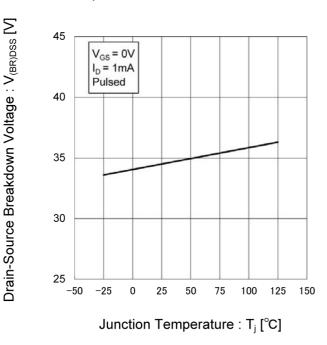
Pulsed

Fig.6 Typical Output Characteristics(II)

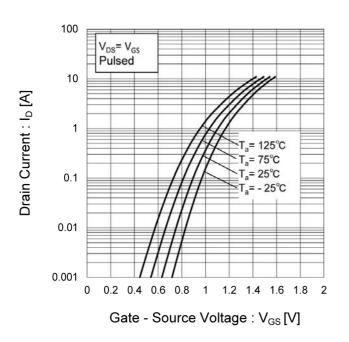


#### 3 2 1 0 0 0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9 1 Drain - Source Voltage : V<sub>DS</sub>[V]

Fig.7 Breakdown Voltage vs. Junction Temperature







## Fig.8 Typical Transfer Characteristics

## Fig.9 Gate Threshold Voltage vs. Junction Temperature

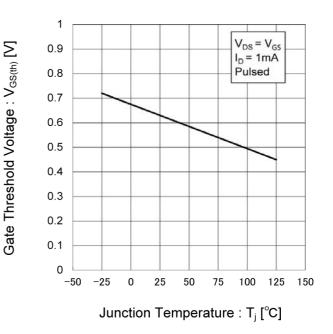


Fig.10 Forward Transfer Admittance vs. Drain Current

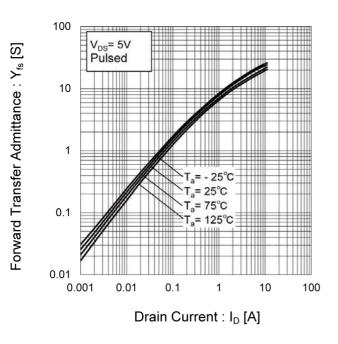
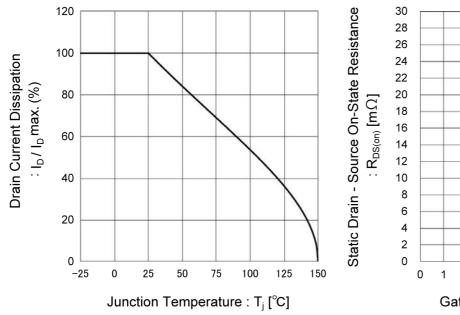




Fig.11 Drain Current Derating Curve



## Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

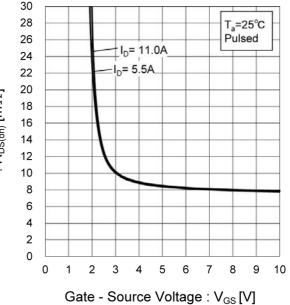
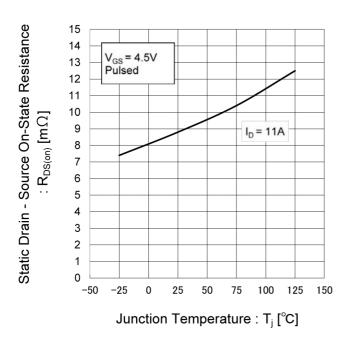


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature





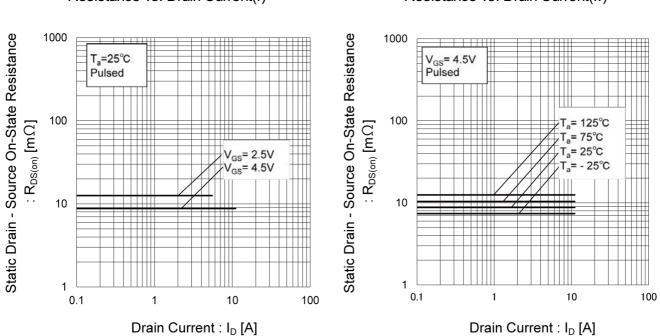
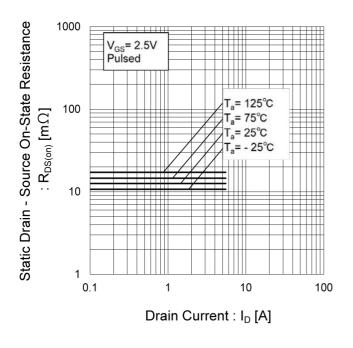
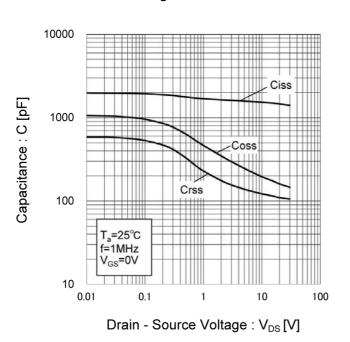


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I) Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)







## Fig.17 Typical Capacitance vs. Drain -Source Voltage

Fig.18 Switching Characteristics

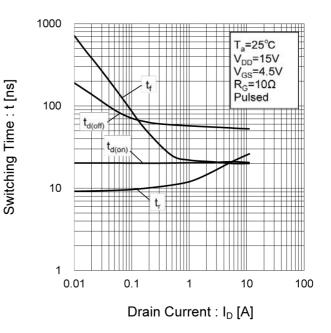


Fig.19 Dynamic Input Characteristics

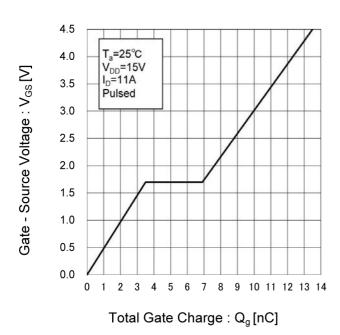
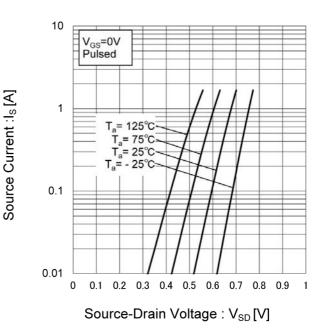


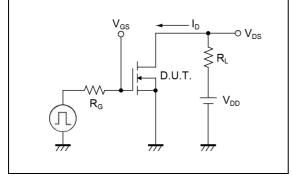
Fig.20 Source Current vs. Source Drain Voltage



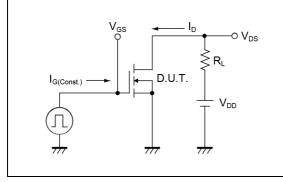


#### Measurement circuits

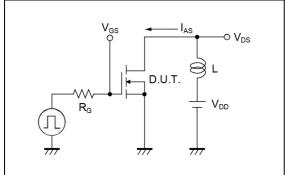
Fig.1-1 Switching Time Measurement Circuit



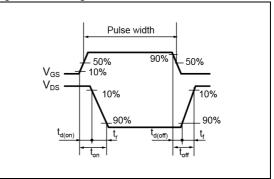
#### Fig.2-1 Gate Charge Measurement Circuit



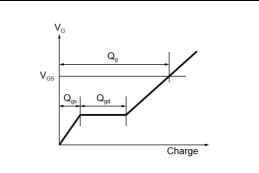
#### Fig.3-1 Avalanche Measurement Circuit



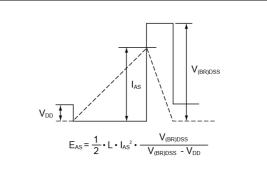
#### Fig.1-2 Switching Waveforms



#### Fig.2-2 Gate Charge Waveform



#### Fig.3-2 Avalanche Waveform

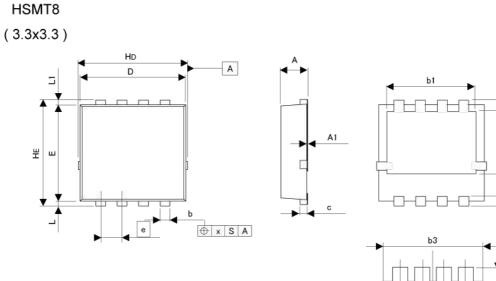


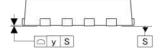
## Notice

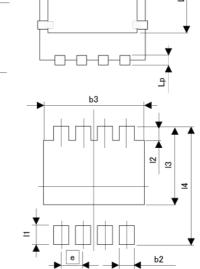
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



## Dimensions







5

2

Pattern of terminal position areas [Not a pattern of soldering pads]

DIM -	MILIME	ETERS	INC	HES
	MIN	MAX	MIN	MAX
A	0.70	0.90	0.028	0.035
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
b1	2.50	2.70	0.098	0.106
с	0.10	0.30	0.004	0.012
D	3.10	3.30	0.122	0.130
E	2.90	3.10	0.114	0.122
е	0.	65	0.0	)26
HD	3.20	3.40	0.126	0.134
HE	3.20	3.40	0.126	0.134
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.20	0.40	0.008	0.016
Lp1	0.25	0.45	0.010	0.018
Lp2	2.20	2.40	0.087	0.094
x	-	0.10	-	0.004
у	(*)	0.10		0.004
-	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	19 <u>1</u> 8	0.47	) <u> </u>	0.019
b3	270	2.70	-	0.106
11	1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 - 1990 -	0.50		0.020
12	122	0.55		0.022
13	2.55	2.40	×	0.094
14	5 <b>-</b> 2	3.40		0.134

Dimension in mm/inches





# Notice

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  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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