

RQ3G150GN

Nch 40V 39A Middle Power MOSFET

Datasheet

V _{DSS}	40V		
R _{DS(on)} (Max.)	7.2mΩ		
I _D	±39A		
P _D	20W		

Features

- 1) Low on resistance.
- 2) High Power Package (HSMT8).
- 3) Pb-free lead plating ; RoHS compliant.
- 4) Halogen Free.

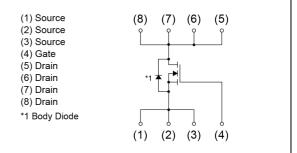
Application

DC/DC converter

Switching

●Outline	
HSMT8	(4)(3)(2)(1) (4)(3)(2)(1)

●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	3000
	Taping code	ТВ
	Marking	G150GN

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Paramete	Symbol	Value	Unit	
Drain - Source voltage	V _{DSS}	40	V	
Continuous dusis sumont	$T_c = 25^{\circ}C$	I _D *1	±39	А
Continuous drain current	$T_a = 25^{\circ}C$	I _D	±15	А
Pulsed drain current	I _{DP} *2	±60	А	
Gate - Source voltage	V _{GSS}	±20	V	
Avalanche current, single pulse		I _{AS} *3	15	А
Avalanche energy, single pulse	E _{AS} *3	17	mJ	
Power dissipation		P _D ^{*1}	20	W
		P _D *4	2	W
Junction temperature		Tj	150	°C
Operating junction and storage t	T _{stg}	-55 to +150	°C	

Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}^{*1}	-	-	6.2	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*4}	-	-	62.5	°C/W

•Electrical characteristics (T_a = 25°C)

Deremeter	Currence of	Conditions	Values			1.1:4
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		40	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1 mA$ referenced to 25°C		-	26.2	-	mV/°C
Zero gate voltage drain current	I_{DSS} V_{DS} = 40V, V_{GS} = 0V		-	-	1	μA
Gate - Source leakage current	I _{GSS}	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		-	±100	nA
Gate threshold voltage	V _{GS(th)}	$V_{GS(th)}$ $V_{DS} = V_{GS}$, $I_D = 1mA$		-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	$\Delta V_{GS(th)} I_D = 1mA$		-4.9	-	mV/°C
Static drain - source	D *5	V _{GS} = 10V, I _D = 15A	-	5.1	7.2	
on - state resistance	${\sf R}_{\sf DS(on)}^{*5}$	V _{GS} = 4.5V, I _D = 15A	-	6.4	8.9	mΩ
Gate resistance	R _G f=1MHz, open drain		-	1.4	-	Ω
Forward Transfer Admittance	Y _{fs} * ⁵			-	-	S

*1Tc=25°C, Limited only by maximum temperature allowed.

*2 Pw \leq 10µs, Duty cycle \leq 1%

*3 L \simeq 0.1mH, V_{DD} = 20V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2

*4 Mounted on a Cu board (40×40×0.8mm)

* Limited only by maximum chamel temperaturer allowed.

*5 Pulsed





•Electrical characteristics ($T_a = 25^{\circ}C$)

Devenuetor	C: mah al	Conditions	Values			L Incit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	1450	-		
Output capacitance	C _{oss}	V _{DS} = 20V	-	260	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	80	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 20V, V_{GS}$ = 10V	-	16.8	-		
Rise time	t _r *5	I _D = 7.5A	-	6.4	-		
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 2.7\Omega$	-	62.1	-	ns	
Fall time	t _f *5	R _G = 10Ω	-	11.0	-		

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Sumbol	bol Conditions		Values			1 1
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total acto charge	O *5		V _{GS} = 10V	-	24.1	-	
Total gate charge	Q_g^{*5}	$V_{DD} \simeq 20V$		-	11.6	-	nC
Gate - Source charge	Q_{gs}^{*5}	I _D = 15A	V _{GS} = 4.5V	-	4.7	-	nc
Gate - Drain charge	Q _{gd} *5			-	3.0	-	

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Deremeter	Symbol Conditions		Values			Unit
Parameter			Min.	Тур.	Max.	Unit
Continuous forward current	۱ _s	T _a = 25℃	-	-	1.67	А
Pulse forward current	I_{SP}^{*2}	$T_a = 25 C$	-	-	60	А
Forward voltage	V_{SD}^{*5}	V _{GS} = 0V, I _S = 1.67A	-	-	1.2	V
Reverse recovery time	t _{rr} *5	I _S = 15A, V _{GS} =0V	-	27	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/µs	-	23	-	nC



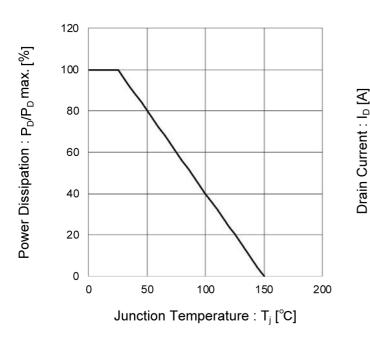


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

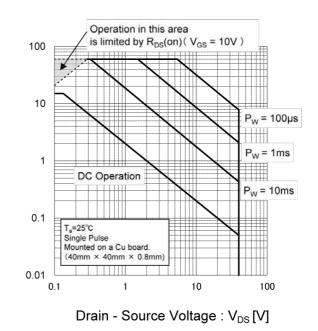
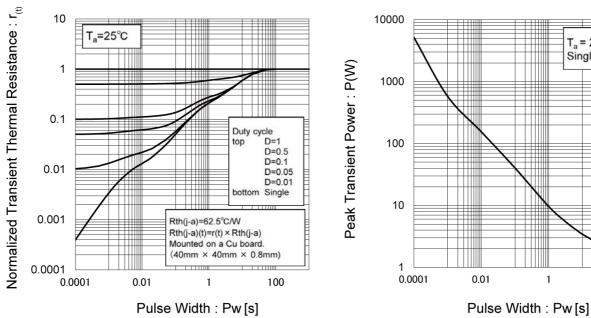


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power dissipation





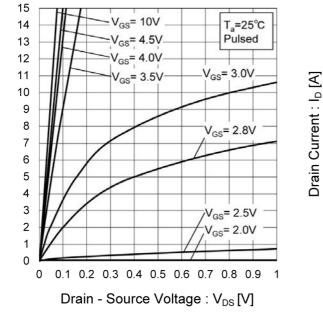
100

1

T_a = 25°C Single Pulse

 Fig.5 Typical Output Characteristics(I)
 Fig.6 Typical Output Characteristics(II)

Drain Current : I_D [A]



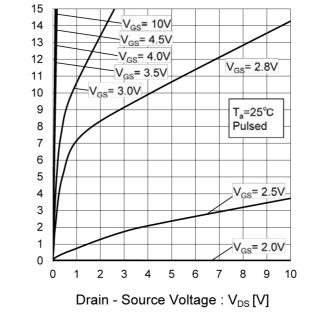
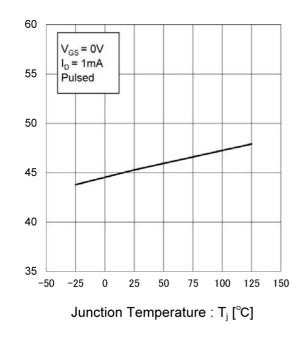


Fig.7 Breakdown Voltage vs. Junction Temperature

Drain-Source Breakdown Voltage : V_{(BR)DSS} [V]





100

10

1

0.1

0.01

0.001

0

0.5

1

V_{DS}= V_{GS}

= 125°C

T_= 75°C T_= 25°C 25°C

Pulsed



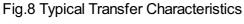


Fig.9 Gate Threshold Voltage vs. Junction Temperature

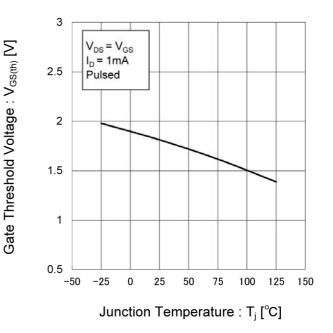


Fig.10 Forward Transfer Admittance vs. **Drain Current**

1.5

Gate - Source Voltage : V_{GS} [V]

2

2.5

3

3.5

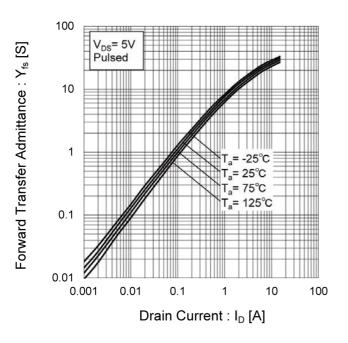




Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

• Electrical characteristic curves

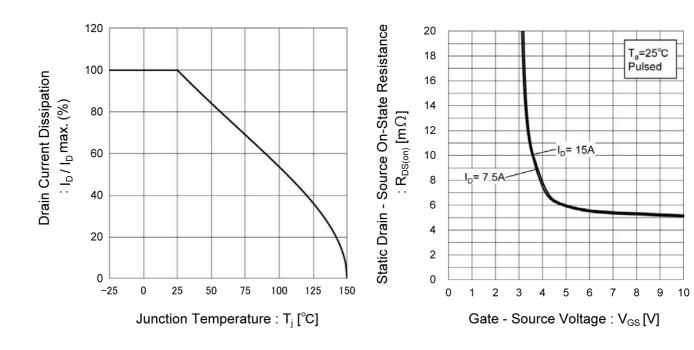
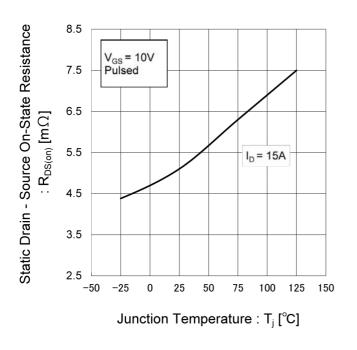


Fig.11 Drain Current Derating Curve

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature





• Electrical characteristic curves

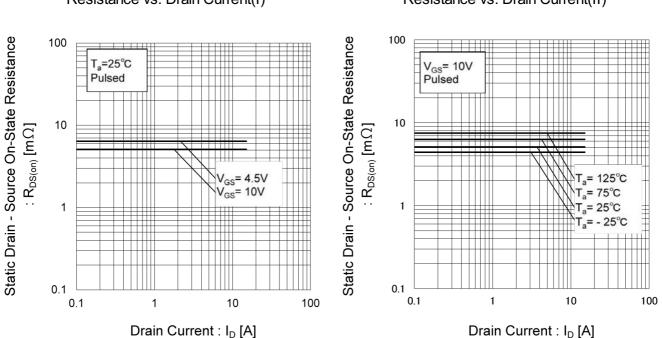
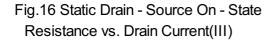
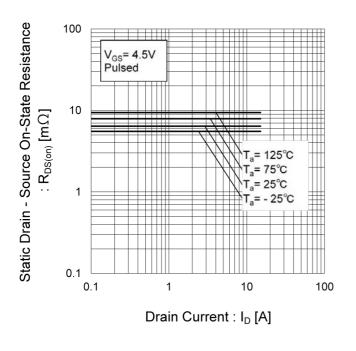


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I) Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)







• Electrical characteristic curves

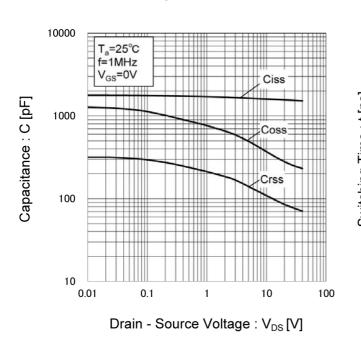


Fig.17 Typical Capacitance vs. Drain -Source Voltage

Fig.18 Switching Characteristics

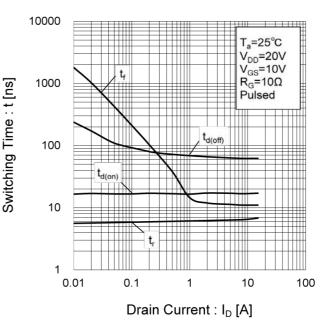


Fig.19 Dynamic Input Characteristics

Gate - Source Voltage : V_{GS} [V]

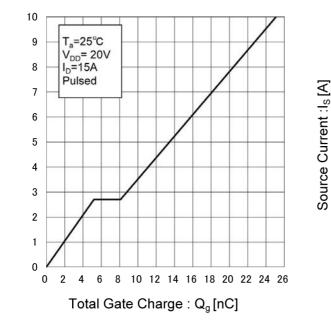
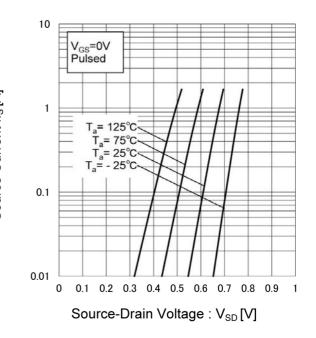


Fig.20 Source Current vs. Source Drain Voltage





Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

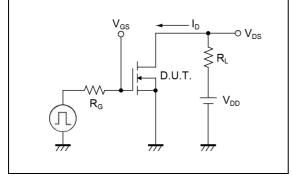


Fig.2-1 Gate Charge Measurement Circuit

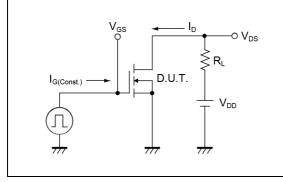


Fig.3-1 Avalanche Measurement Circuit

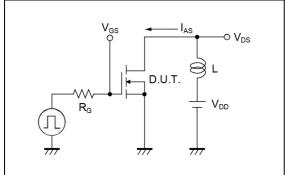


Fig.1-2 Switching Waveforms

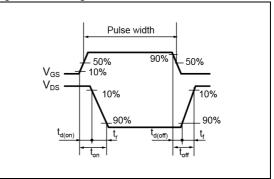


Fig.2-2 Gate Charge Waveform

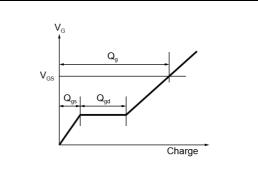
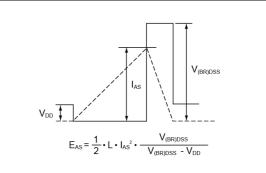


Fig.3-2 Avalanche Waveform

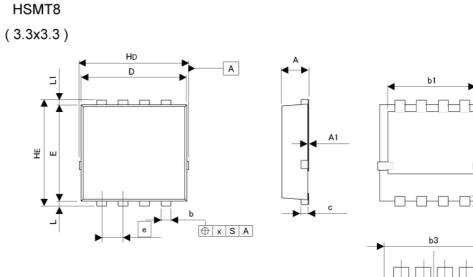


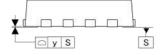
Notice

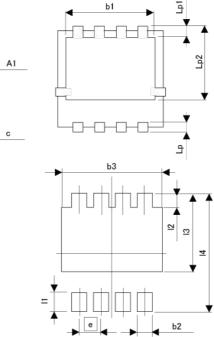
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions







Pattern of terminal position areas [Not a pattern of soldering pads]

DIM -	MILIMETERS		INC	HES
	MIN	MAX	MIN	MAX
A	0.70	0.90	0.028	0.035
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
b1	2.50	2.70	0.098	0.106
с	0.10	0.30	0.004	0.012
D	3.10	3.30	0.122	0.130
E	2.90	3.10	0.114	0.122
е	0.	65	0.0	26
HD	3.20	3.40	0.126	0.134
HE	3.20	3.40	0.126	0.134
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.20	0.40	0.008	0.016
Lp1	0.25	0.45	0.010	0.018
Lp2	2.20	2.40	0.087	0.094
x	-	0.10	-	0.004
у	(H)	0.10		0.004
-	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b2	5 <u>1</u> 5	0.47	2 L	0.019
b3	125	2.70		0.106
11	(#)	0.50	1 4 1	0.020
12	142	0.55		0.022
13	2. 2 5	2.40		0.094

Dimension in mm/inches

14

3.40



0.134

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 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
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