Pch -20V -2.5A Small Signal MOSFET

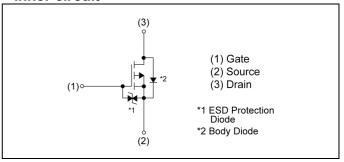
V _{DSS}	-20V
R _{DS(on)} (Max.)	95mΩ
I _D	±2.5A
P _D	1.0W

Features

- 1) Low on resistance
- 2) Built-in G-S Protection Diode
- Space saving small surface mount package (TSMT3)
- 4) Pb-free lead plating; RoHS compliant

● Outline SOT-346T SC-96 TSMT3 (1) (2)

•Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Type	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TL
	Marking	TY

Application

Switching

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	-20	V
Continuous drain current	I _D	±2.5	Α
Pulsed drain current	I _{DP} *1	±10	Α
Gate - Source voltage	V _{GSS}	±12	V
Daving discination	P _D *2	1.0	W
Power dissipation	P _D *3	0.7	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Deremeter	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	UTIIL
Thermal registeres innetion, ambient	R _{thJA} *2	-	-	125	°C/W
Thermal resistance, junction - ambient	R _{thJA} *3	-	-	178	°C/W

● Electrical characteristics (T_a = 25°C)

Davamatav	Cymah ol	Conditions	Values		Values		Lloit
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = -1mA$	-20	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j} I_D = -1 \text{mA}$ referenced to 25°C		-18.7	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = -20V, V _{GS} = 0V	-	-	-1	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 12V, V_{DS} = 0V$	1	-	±10	μA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = -10V, I_{D} = -1mA$	-0.7	-	-2.0	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I _D = -1mA referenced to 25°C	-	2.3	-	mV/°C	
		V_{GS} = -4.5V, I_D = -2.5A	-	70	95		
Static drain - source on - state resistance	R _{DS(on)} *4	V _{GS} = -4.0V, I _D = -2.5A	-	75	105	mΩ	
		V _{GS} = -2.5V, I _D = -1.25A	-	115	160		
Forward Transfer Admittance	Y _{fs} *4	V _{DS} = -10V, I _D = -1.2A	2.3	-	-	S	

^{*1} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*2} Mounted on a ceramic boad (30×30×0.8mm)

^{*3} Mounted on a FR4 (25×25×0.8mm)

^{*4} Pulsed

●Electrical characteristics (T_a = 25°C)

Davamatav	Current ed	Conditions	Values			l leit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	630	-		
Output capacitance	C _{oss}	V _{DS} = -10V	-	110	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	75	-		
Turn - on delay time	t _{d(on)} *4	$V_{DD} \simeq -15V, V_{GS} = -4.5V$	-	12	-		
Rise time	t _r *4	I _D = -1.25A	-	18	-	no	
Turn - off delay time	t _{d(off)} *4	R _L ≃ 12Ω	-	50	-	ns	
Fall time	t _f *4	$R_G = 10\Omega$	-	20	-		

• Gate charge characteristics $(T_a = 25^{\circ}C)$

	\ a	,				
Parameter	Symbol	Conditions	Values			l leit
raiametei	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	Qg*4	V _{DD} ≃ -15V.	-	7.0	-	
Gate - Source charge	Q _{gs} *4	$V_{DD} \simeq -15V,$ $I_{D} = -2.5A,$ $V_{GS} = -4.5V$	-	1.5	-	nC
Gate - Drain charge	Q _{gd} *4	V _{GS} = -4.5V	-	2.0	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Darameter	Symbol	Conditions	Values			l leit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I _S	T - 25°C	-	-	-0.8	Α
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	-3.2	Α
Forward voltage	V _{SD} *4	$V_{GS} = 0V, I_S = -0.8A$	-	-	-1.2	V

Fig.1 Power Dissipation Derating Curve

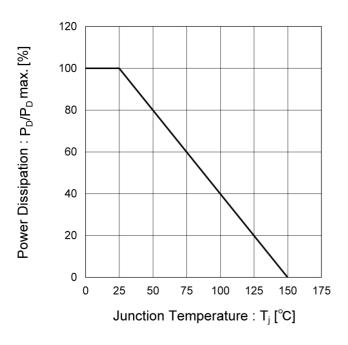
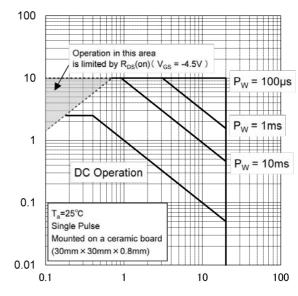


Fig.2 Maximum Safe Operating Area



Drain Current: -l_D [A]

Drain - Source Voltage: -VDS [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

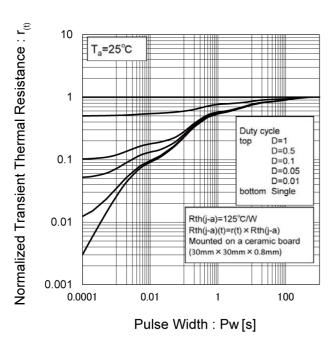
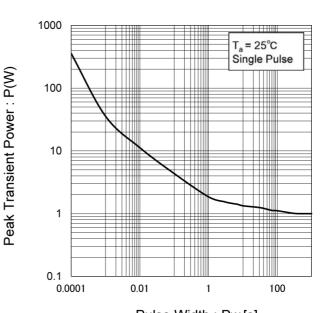


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width: Pw[s]

Fig.5 Typical Output Characteristics(I)

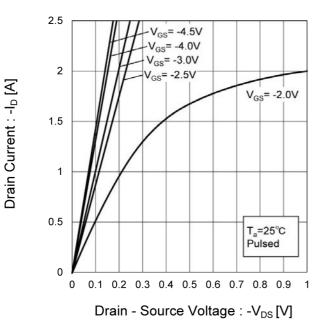
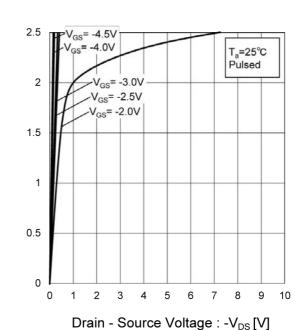


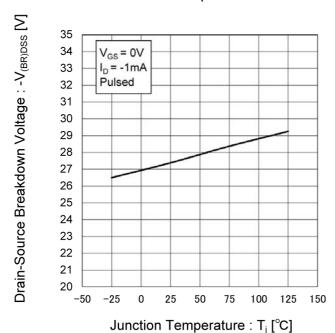
Fig.6 Typical Output Characteristics(II)



Drain Current : -I_D [A]

Fig.7 Breakdown Voltage vs.

Junction Temperature



ROHM

Fig.8 Typical Transfer Characteristics

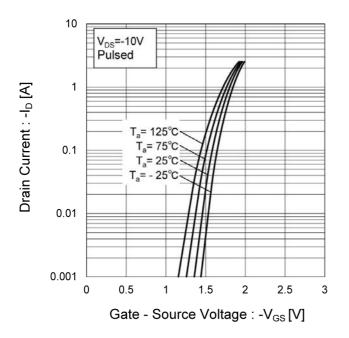


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

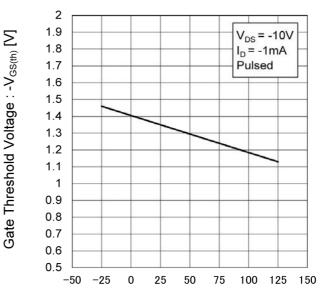


Fig.10 Drain Current Derating Curve

120 100 Drain Current Dissipation 80 : I_D/I_Dmax. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T_j [°C]

Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

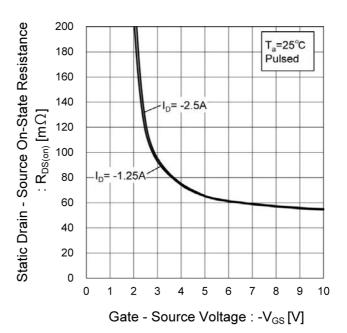


Fig.12 Static Drain - Source On - State Resistance vs. Junction Temperature

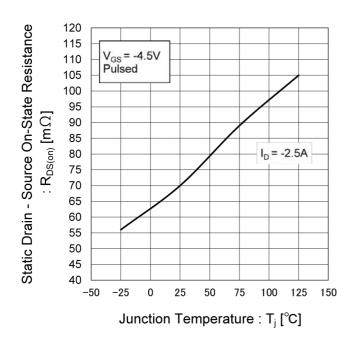


Fig.13 Static Drain - Source On - State
Resistance vs. Drain Current (I)

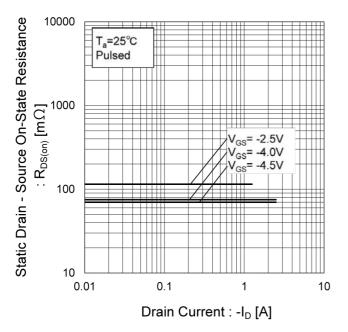


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (II)

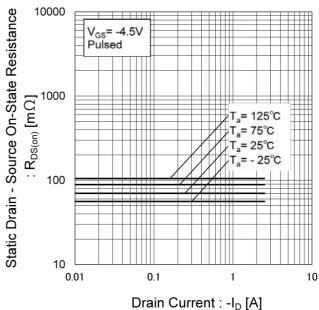


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (III)

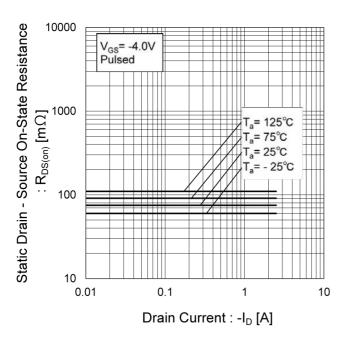


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (IV)

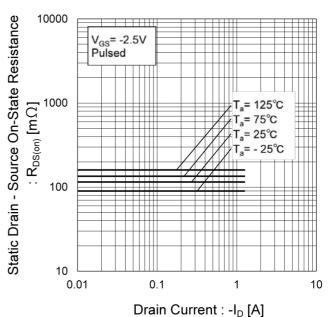


Fig.17 Typical Capacitance vs.

Drain - Source Voltage

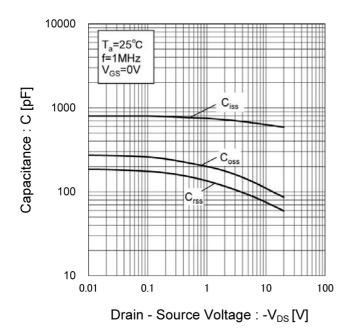


Fig.18 Switching Characteristics

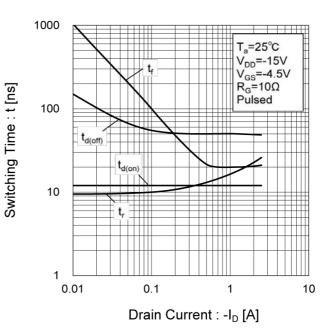


Fig.19 Dynamic Input Characteristics

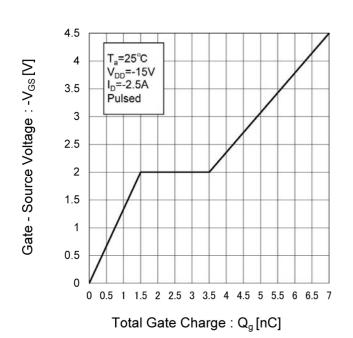
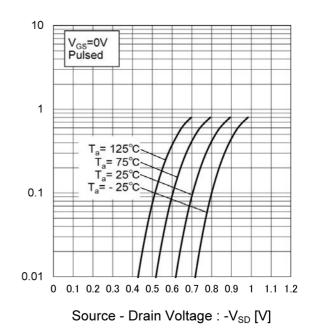


Fig.20 Source Current vs.

Source Drain Voltage



Source Current : -I_s [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

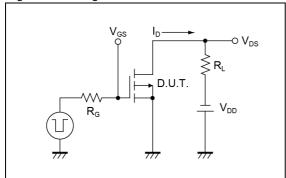


Fig.2-1 Gate Charge Measurement Circuit

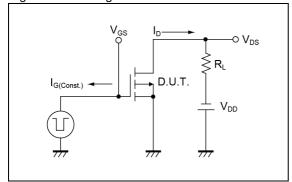


Fig.1-2 Switching Waveforms

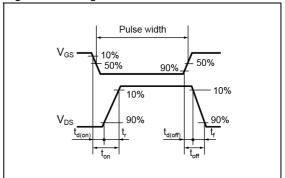
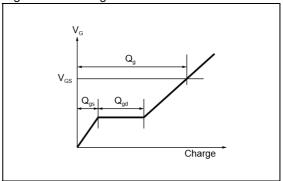


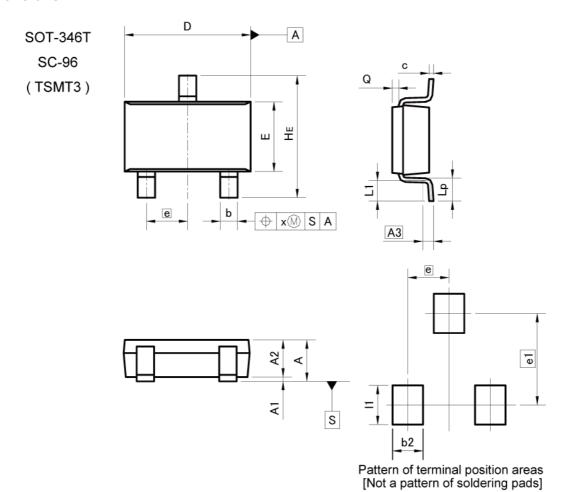
Fig.2-2 Gate Charge Waveform



Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions



DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	*	1.00	-	0.039
A1	0.00	0.10	0.000	0.004
A2	0.75	0.95	0.030	0.037
A3	0.:	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.10	0.26	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.9	0.95		37
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.010
x	7.5 0	0.20		0.008

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2		0.70	57/2	0.028
e1	2.	10	0.0	083
11	=0	0.90	77 2	0.035

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CL ACCTI
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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 - [b] Installation of redundant circuits to reduce the impact of single or multiple circuit failure
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PGA-E Rev.004

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