

V_{DSS}	-30V
$R_{DS(on)}(Max.)$	26mΩ
I_D	±5.0A
P_D	1.0W

●Features

- 1) Low on - resistance
- 2) Small Surface Mount Package (TSMT3)
- 3) Pb-free lead plating ; RoHS compliant

●Application

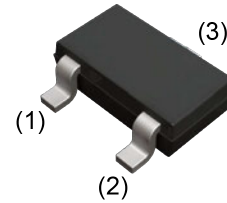
Switching

●Outline

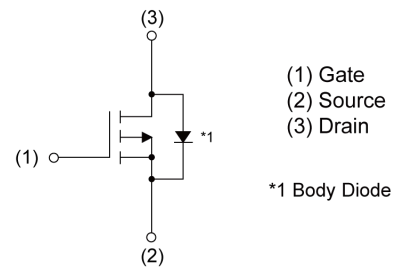
SOT-346T

SC-96

TSMT3



●Inner circuit



●Packaging specifications

Type	Packing	Embossed Tape
	Reel size (mm)	180
Tape width (mm)	8	
Quantity (pcs)	3000	
Taping code	TCL	
Marking	JX	

●Absolute maximum ratings ($T_a = 25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	-30	V
Continuous drain current	I_D	±5.0	A
Pulsed drain current	I_{DP}^{*1}	±18	A
Gate - Source voltage	V_{GSS}	±20	V
Avalanche current, single pulse	I_{AS}^{*2}	-5.0	A
Avalanche energy, single pulse	E_{AS}^{*2}	18	mJ
Power dissipation	P_D^{*3}	1.0	W
	P_D^{*4}	0.76	W
Junction temperature	T_j	150	°C
Operating junction and storage temperature range	T_{stg}	-55 to +150	°C

● Thermal resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal resistance, junction - ambient	R_{thJA}^{*3}	-	-	125	°C/W
	R_{thJA}^{*4}	-	-	165	°C/W

● Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -1mA$	-30	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	$I_D = -1mA$ referenced to 25°C	-	-22	-	mV/°C
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -30V, V_{GS} = 0V$	-	-	-1	μA
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -1mA$	-1.0	-	-2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	$I_D = -1mA$ referenced to 25°C	-	2.9	-	mV/°C
Static drain - source on - state resistance	$R_{DS(on)}^{*5}$	$V_{GS} = -10V, I_D = -5A$	-	21	26	mΩ
		$V_{GS} = -4.5V, I_D = -5A$	-	30	37	
Gate resistance	R_G	$f = 1MHz, \text{open drain}$	-	4.9	-	Ω
Forward Transfer Admittance	$ Y_{fs} ^{*5}$	$V_{DS} = -5V, I_D = -5A$	4.3	-	-	S

*1 $P_w \leq 10\mu\text{s}$, Duty cycle $\leq 1\%$

*2 $L \approx 1mH$, $V_{DD} = -15V$, $R_G = 25\Omega$, Starting $T_j = 25^\circ\text{C}$ Fig.3-1,3-2

*3 Mounted on a ceramic board (30×30×0.8mm)

*4 Mounted on a FR4 (25×25×0.8mm)

*5 Pulsed

● Electrical characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input capacitance	C_{iss}	$V_{GS} = 0V$	-	880	-	pF
Output capacitance	C_{oss}	$V_{DS} = -15V$	-	160	-	
Reverse transfer capacitance	C_{rss}	$f = 1\text{MHz}$	-	125	-	
Turn - on delay time	$t_{d(on)}^{*5}$	$V_{DD} \approx -15V, V_{GS} = -10V$	-	9.5	-	ns
Rise time	t_r^{*5}	$I_D = -2.5A$	-	13	-	
Turn - off delay time	$t_{d(off)}^{*5}$	$R_L \approx 6\Omega$	-	52	-	
Fall time	t_f^{*5}	$R_G = 10\Omega$	-	21	-	

● Gate charge characteristics ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit	
			Min.	Typ.	Max.		
Total gate charge	Q_g^{*5}	$V_{DD} \approx -15V$ $I_D = -5A$	$V_{GS} = -10V$	-	19.4	-	nC
			$V_{GS} = -4.5V$	-	9.7	-	
Gate - Source charge	Q_{gs}^{*5}			-	3.0	-	
Gate - Drain charge	Q_{gd}^{*5}		-	3.7	-		

● Body diode electrical characteristics (Source-Drain) ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Continuous forward current	I_S	$T_a = 25^\circ\text{C}$	-	-	-0.8	A
Pulse forward current	I_{SP}^{*1}		-	-	-18	A
Forward voltage	V_{SD}^{*5}	$V_{GS} = 0V, I_S = -0.8A$	-	-	-1.2	V

● Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve



Fig.2 Maximum Safe Operating Area

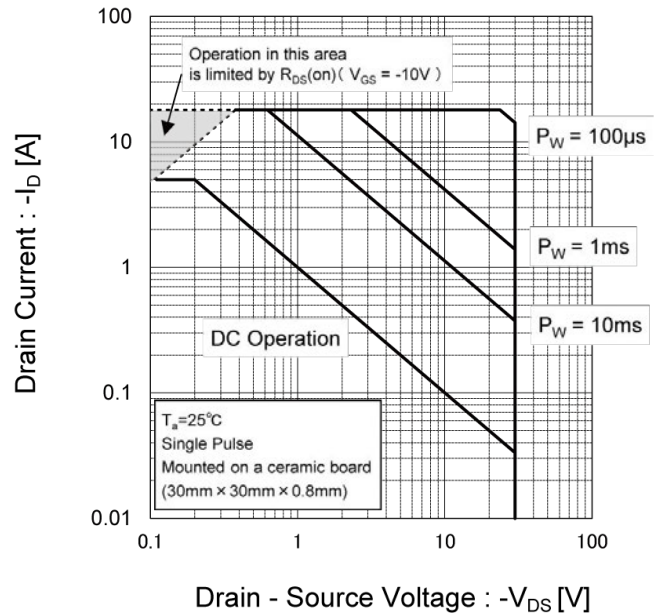


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

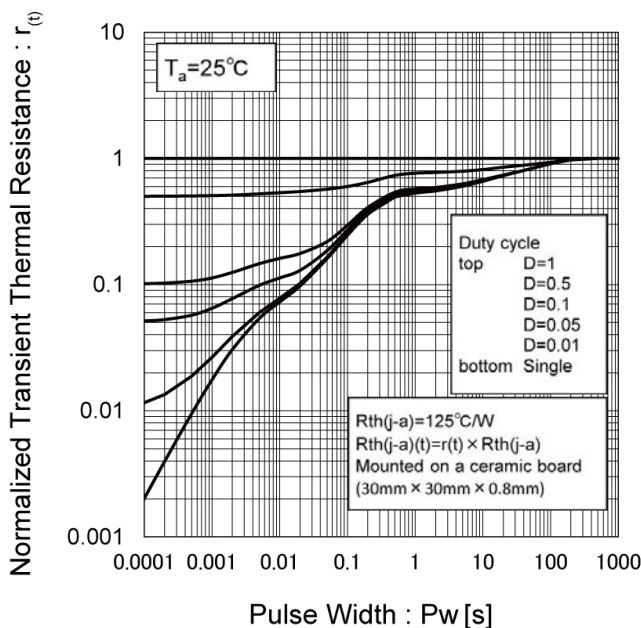
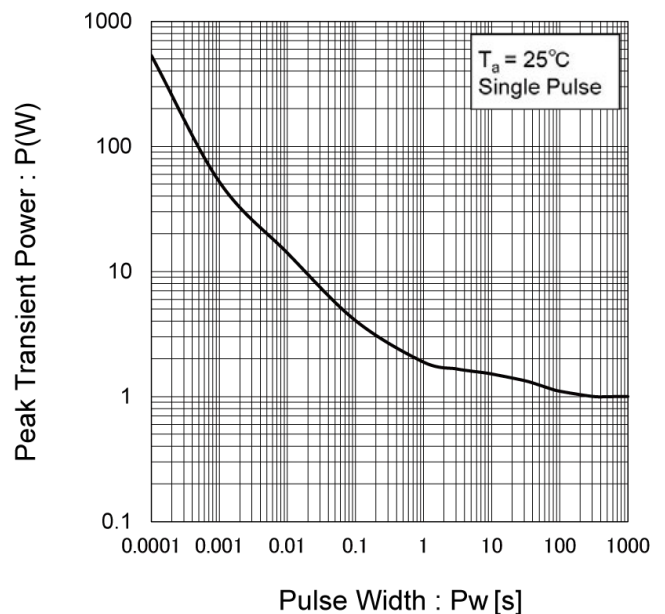


Fig.4 Single Pulse Maximum Power dissipation



● Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

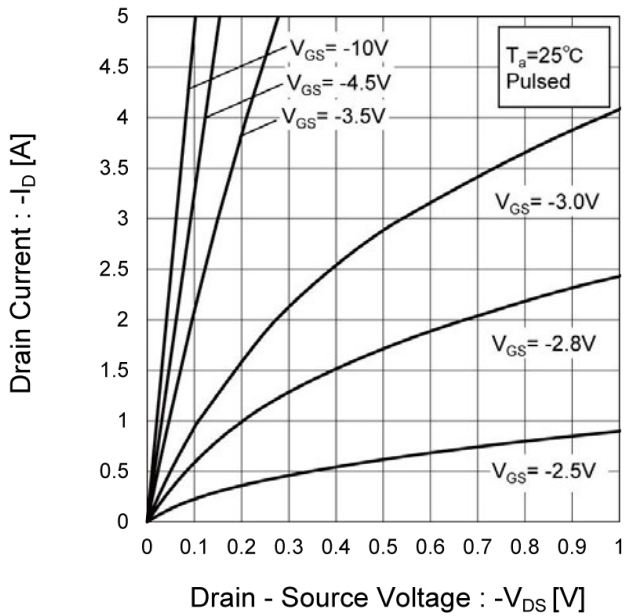


Fig.6 Typical Output Characteristics(II)

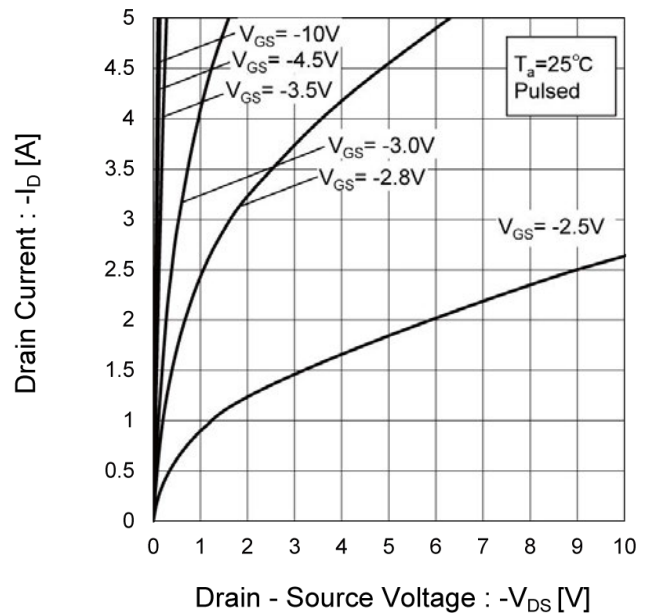
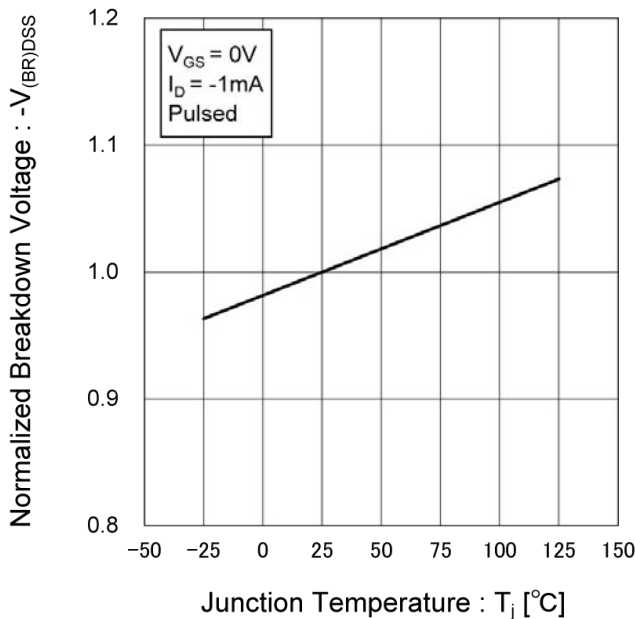


Fig.7 Breakdown Voltage vs. Junction Temperature



● Electrical characteristic curves

Fig.8 Typical Transfer Characteristics

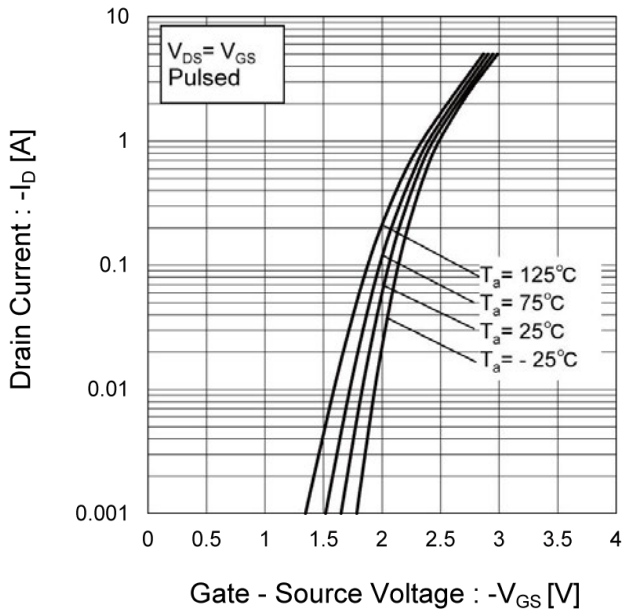


Fig.9 Gate Threshold Voltage vs. Junction Temperature

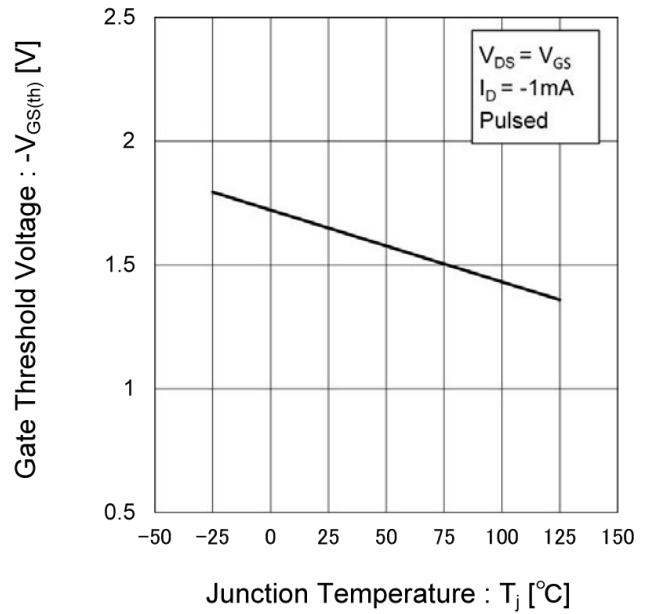
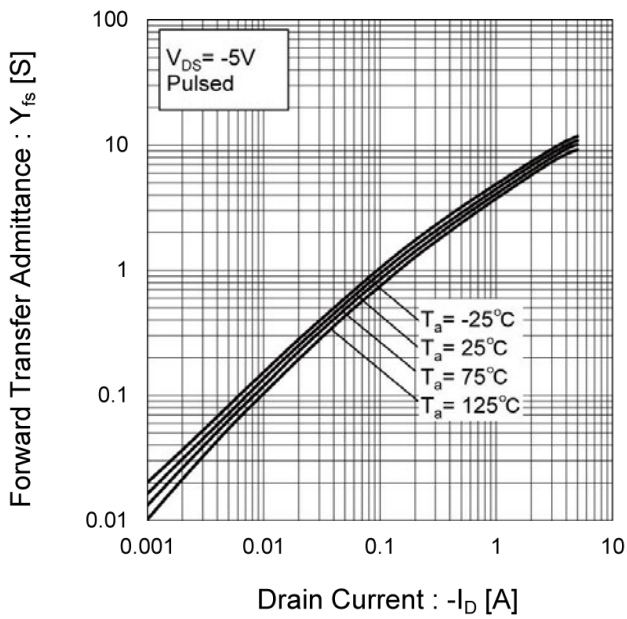


Fig.10 Forward Transfer Admittance vs. Drain Current



● Electrical characteristic curves

Fig.11 Drain Current Derating Curve



Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

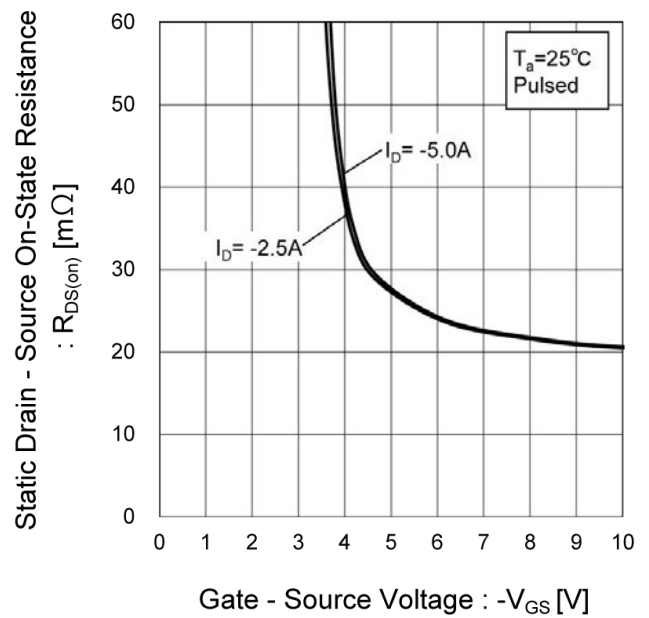
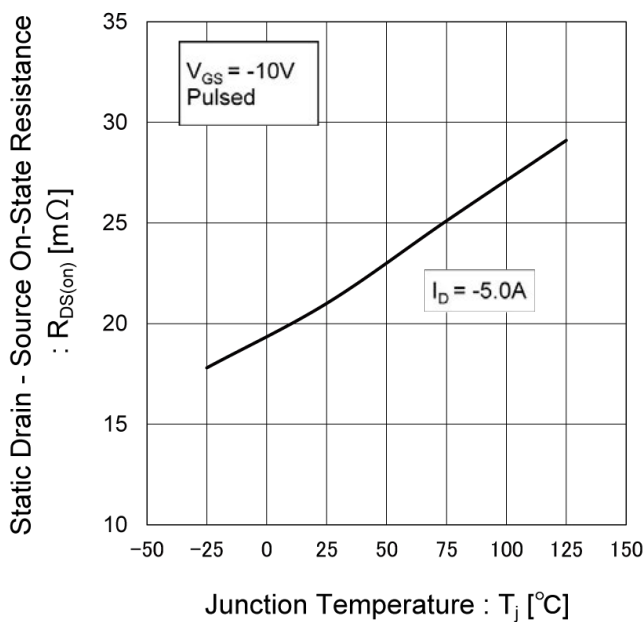


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



● Electrical characteristic curves

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

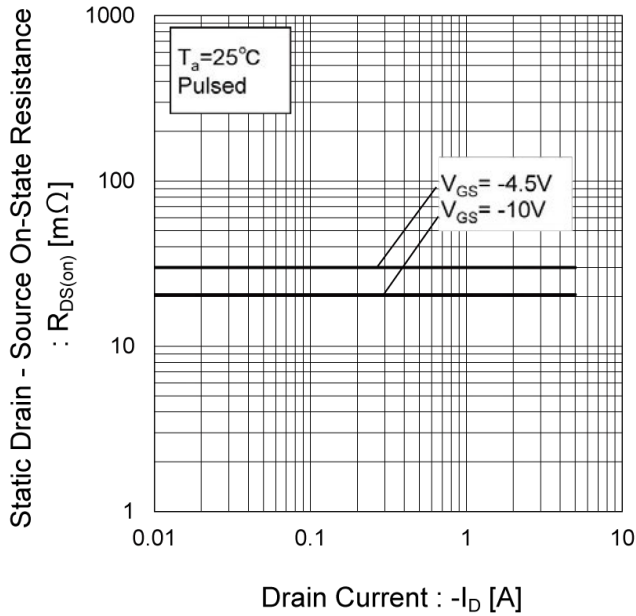


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

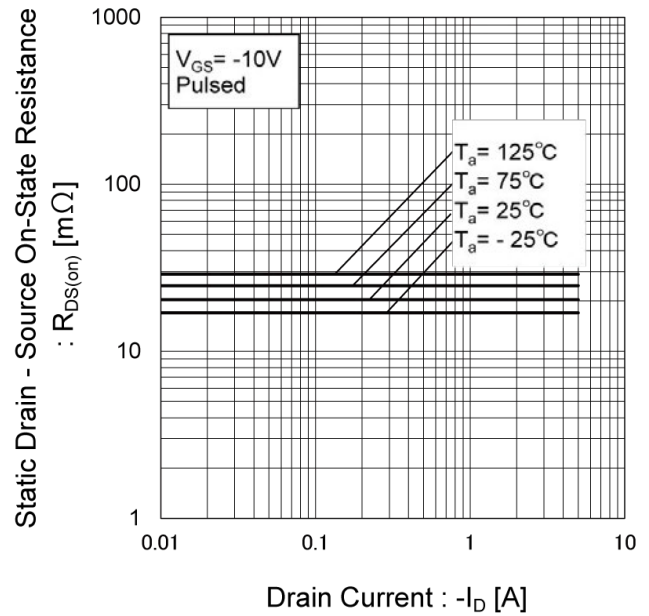
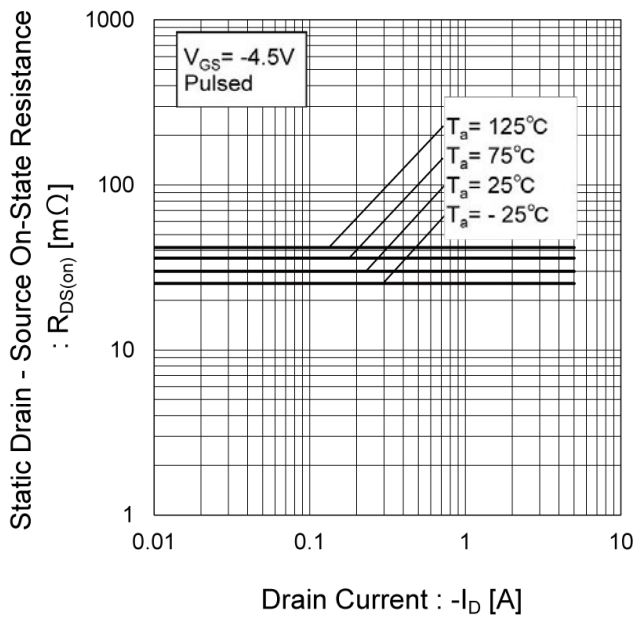


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



● Electrical characteristic curves

Fig.17 Typical Capacitance vs. Drain - Source Voltage

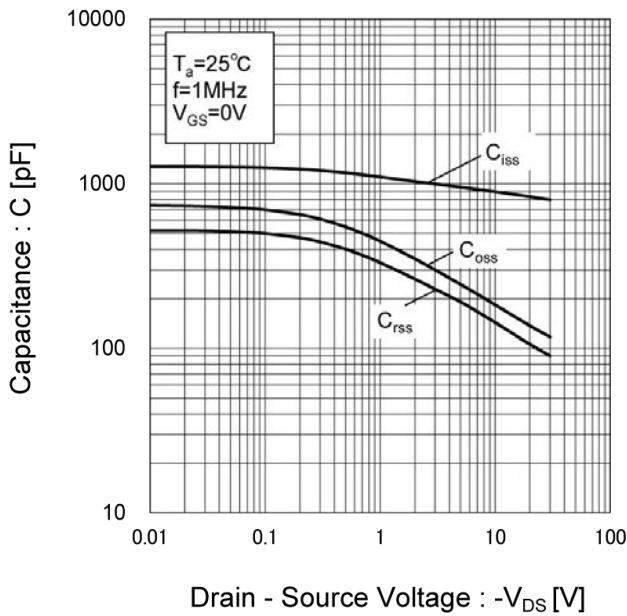


Fig.18 Switching Characteristics

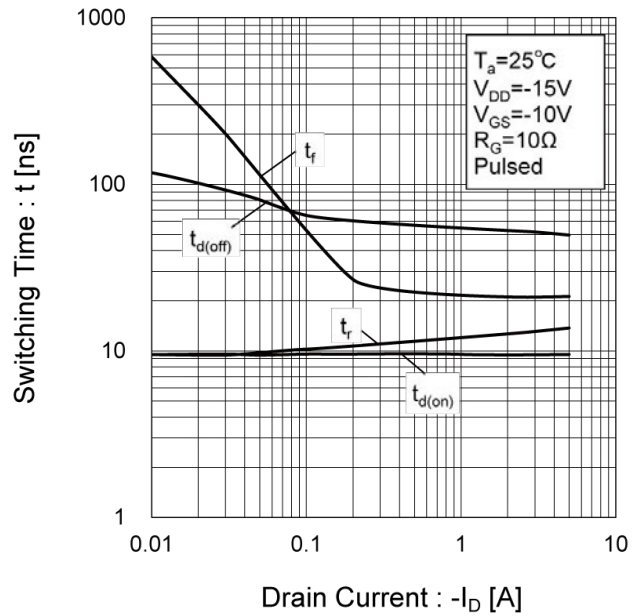


Fig.19 Dynamic Input Characteristics

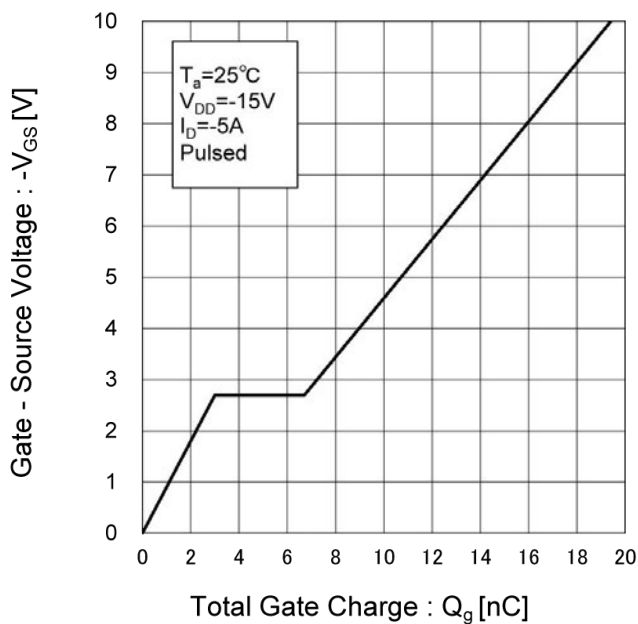
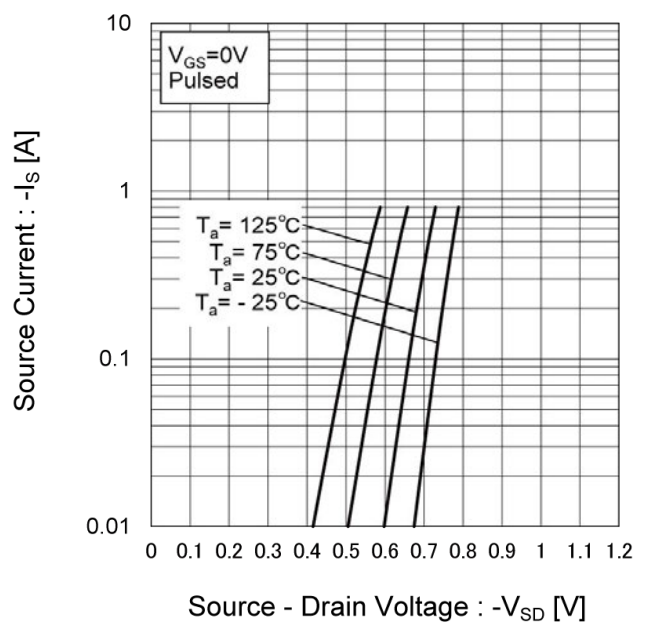


Fig.20 Source Current vs. Source Drain Voltage



● Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

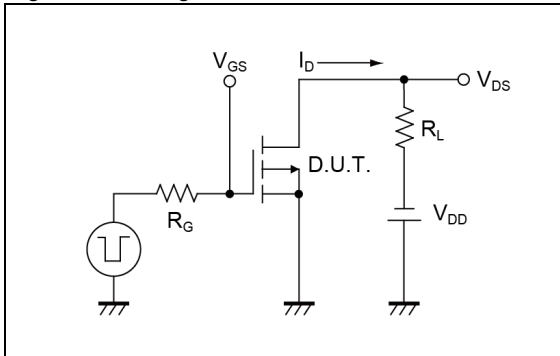


Fig.1-2 Switching Waveforms

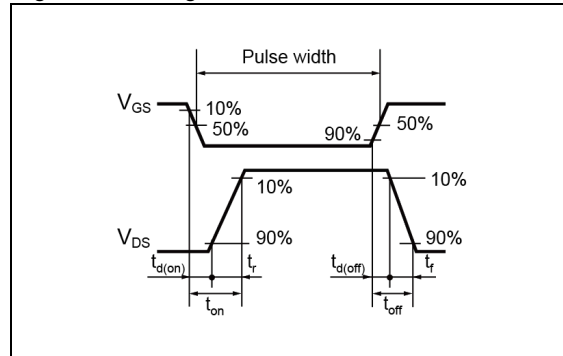


Fig.2-1 Gate Charge Measurement Circuit

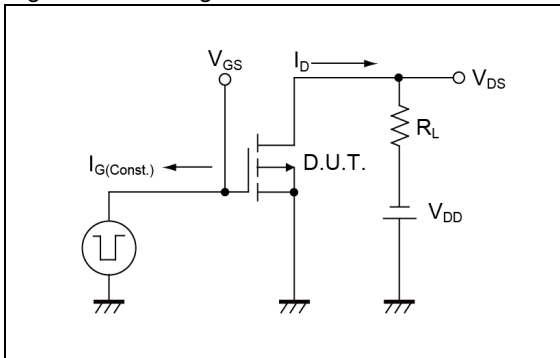


Fig.2-2 Gate Charge Waveform

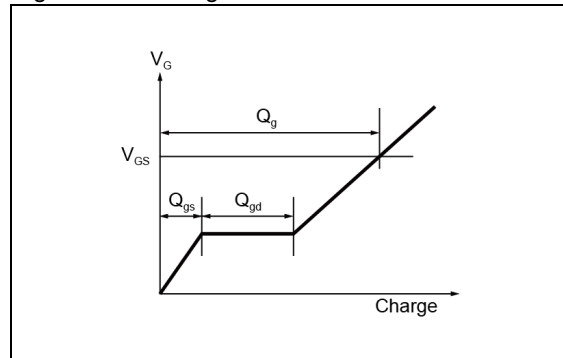


Fig.3-1 Avalanche Measurement Circuit

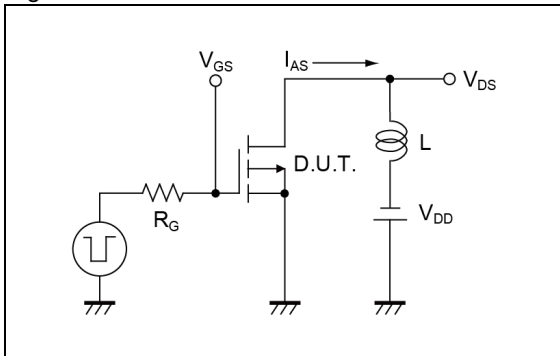
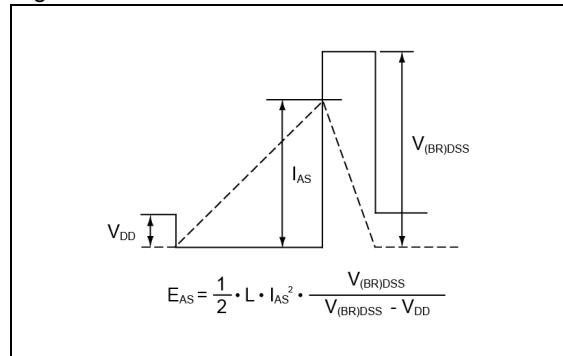


Fig.3-2 Avalanche Waveform

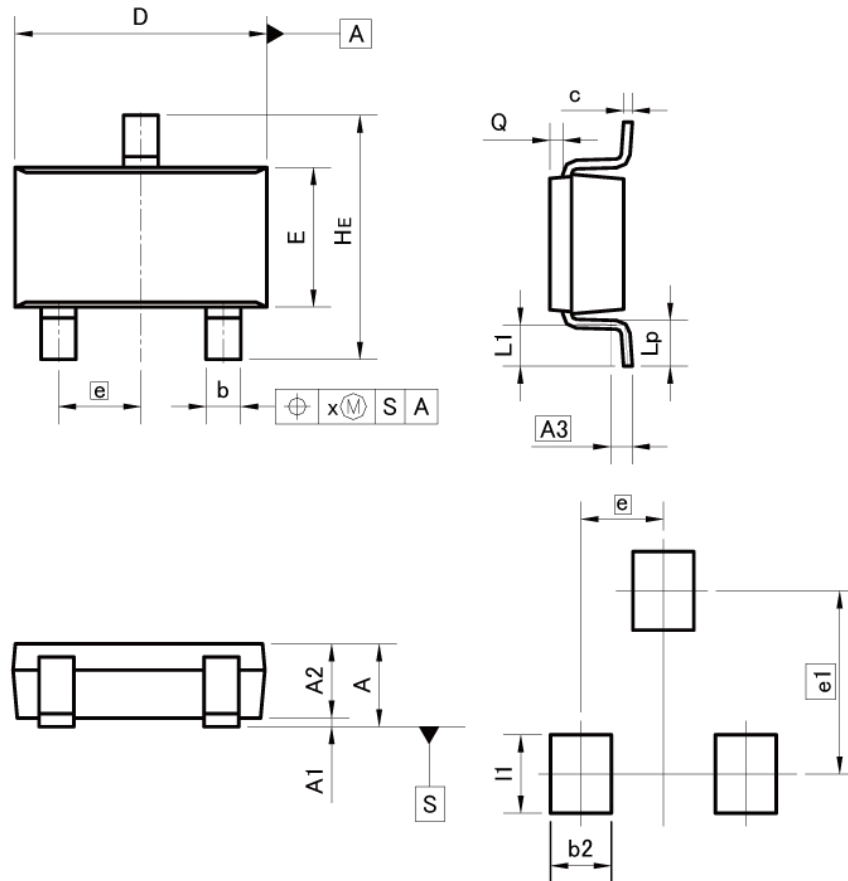


● Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

●Dimensions

SOT-346T
SC-96
(TSMT3)



DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	-	1.00	-	0.039
A1	0.00	0.10	0.000	0.004
A2	0.75	0.95	0.030	0.037
A3	0.25		0.010	
b	0.35	0.50	0.014	0.020
c	0.10	0.26	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
e	0.95		0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.010
x	-	0.20	-	0.008

DIM	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
b2	-	0.70	-	0.028
e1	2.10		0.083	
l1	-	0.90	-	0.035

Dimension in mm/inches

Notice

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JAPAN	USA	EU	CHINA
CLASS III	CLASS III	CLASS II b	CLASS III
CLASS IV		CLASS III	

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 - Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - Sealing or coating our Products with resin or other coating materials
 - Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.) ; or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - Use of the Products in places subject to dew condensation
- The Products are not subject to radiation-proof design.
- Please verify and confirm characteristics of the final or mounted products in using the Products.
- In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of Ionizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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