## Nch 30V 4A Small Signal MOSFET

| V <sub>DSS</sub>           | 30V   |
|----------------------------|-------|
| R <sub>DS(on)</sub> (Max.) | 50mΩ  |
| I <sub>D</sub>             | ±4.0A |
| P <sub>D</sub>             | 1.25W |

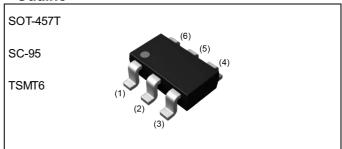
# ● Features

- 1) Low on resistance
- 2) Built-in G-S Protection Diode
- 3) Small Surface Mount Package (TSMT6)
- 4) Pb-free lead plating; RoHS compliant

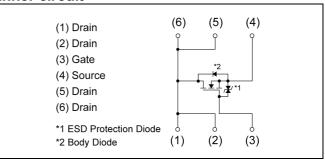
# Application

DC/DC converters

### Outline



## ●Inner circuit



Packaging specifications

|      | Packing                   | Embossed<br>Tape |
|------|---------------------------|------------------|
|      | Reel size (mm)            | 180              |
| Туре | Tape width (mm)           | 8                |
|      | Basic ordering unit (pcs) | 3000             |
|      | Taping code               | TCR              |
|      | Marking                   | XQ               |

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

| Parameter  | Symbol             | Value       | Unit |
|--|--------------------|-------------|------|
| Drain - Source voltage                           | V <sub>DSS</sub>   | 30          | V    |
| Continuous drain current                         | I <sub>D</sub>     | ±4.0        | Α    |
| Pulsed drain current                             | I <sub>DP</sub> *1 | ±12         | Α    |
| Gate - Source voltage                            | V <sub>GSS</sub>   | ±20         | V    |
| Dower discinction                                | P <sub>D</sub> *2  | 1.25        | W    |
| Power dissipation                                | P <sub>D</sub> *3  | 0.95        | W    |
| Junction temperature                             | T <sub>j</sub>     | 150         | °C   |
| Operating junction and storage temperature range | T <sub>stg</sub>   | -55 to +150 | °C   |

## ●Thermal resistance

| Deremeter                                      | Cymbol               | Values |      |      | Lloit |
|--|----------------------|--------|------|------|-------|
| Parameter                                      | Symbol               | Min.   | Тур. | Max. | Unit  |
| The world reciptors of investigation amplicant | R <sub>thJA</sub> *2 | -      | -    | 100  | °C/W  |
| Thermal resistance, junction - ambient         | R <sub>thJA</sub> *3 | -      | -    | 132  | °C/W  |

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

| Davanastan                                     | Symbol Conditions                         |   | Values |       |      | l lait |
|--|---|---|--------|-------|------|--------|
| Parameter                                      |   |   | Min.   | Тур.  | Max. | Unit   |
| Drain - Source breakdown voltage               | V <sub>(BR)DSS</sub>                      | V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA    | 30     | -     | -    | V      |
| Breakdown voltage temperature coefficient      | $\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$ | I <sub>D</sub> = 1mA<br>referenced to 25°C    | -      | 34.15 | -    | mV/°C  |
| Zero gate voltage<br>drain current             | I <sub>DSS</sub>                          | V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V   | -      | -     | 1    | μA     |
| Gate - Source leakage current                  | I <sub>GSS</sub>                          | $V_{GS} = \pm 20V, V_{DS} = 0V$               | 1      | -     | ±10  | μΑ     |
| Gate threshold voltage                         | $V_{GS(th)}$                              | V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA   | 1.0    | ı     | 2.5  | V      |
| Gate threshold voltage temperature coefficient | $\frac{\Delta  V_{GS(th)}}{\Delta  T_j}$  | I <sub>D</sub> = 1mA<br>referenced to 25°C    | -      | -2.34 | -    | mV/°C  |
|  |   | V <sub>GS</sub> = 10V, I <sub>D</sub> = 4.0A  | -      | 35    | 50   |        |
| Static drain - source on - state resistance    | R <sub>DS(on)</sub> *4                    | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.0A | -      | 45    | 65   | mΩ     |
|  |   | V <sub>GS</sub> = 4.0V, I <sub>D</sub> = 4.0A | -      | 50    | 70   |        |
| Gate resistance $R_G$ $f = 1MHz$ , op          |   | f = 1MHz, open drain                          | -      | 2     | -    | Ω      |
| Forward Transfer<br>Admittance                 | Y <sub>fs</sub>  *4                       | V <sub>DS</sub> = 10V, I <sub>D</sub> = 4.0A  | 1.5    | -     | -    | S      |

<sup>\*1</sup> Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

<sup>\*2</sup> Mounted on a ceramic board (30×30×0.8mm)

<sup>\*3</sup> Mounted on a FR4 (25×25×0.8mm)

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C)

| Daramatar                    | Cymahal                | Conditions                                  | Values |      |      | Unit |  |
|------------------------------|------------------------|---|--------|------|------|------|--|
| Parameter                    | Symbol                 | Conditions                                  | Min.   | Тур. | Max. | Unit |  |
| Input capacitance            | C <sub>iss</sub>       | V <sub>GS</sub> = 0V                        | -      | 180  | -    | _    |  |
| Output capacitance           | C <sub>oss</sub>       | V <sub>DS</sub> = 10V                       | -      | 70   | -    | pF   |  |
| Reverse transfer capacitance | C <sub>rss</sub>       | f = 1MHz                                    | 1      | 35   | -    |      |  |
| Turn - on delay time         | t <sub>d(on)</sub> *4  | V <sub>DD</sub> ≃ 15V,V <sub>GS</sub> = 10V | 1      | 10   | -    |      |  |
| Rise time                    | t <sub>r</sub> *4      | I <sub>D</sub> = 2.0A                       | -      | 28   | -    | no   |  |
| Turn - off delay time        | t <sub>d(off)</sub> *4 | $R_L \simeq 7.5\Omega$                      |        | 24   | -    | ns   |  |
| Fall time                    | t <sub>f</sub> *4      | $R_G = 10\Omega$                            | -      | 7    | -    |      |  |

# • Gate charge characteristics $(T_a = 25^{\circ}C)$

|                      | \ a                   | ,                                       |        |      |      |        |
|----------------------|-----------------------|---|--------|------|------|--------|
| Parameter            | Cumbal                | Conditions                              | Values |      |      | l leit |
| raiametei            | rameter Symbol Condit |   | Min.   | Тур. | Max. | Unit   |
| Total gate charge    | Qg*4                  | V <sub>DD</sub> ≃ 15V.                  | -      | 3.3  | -    |        |
| Gate - Source charge | Q <sub>gs</sub> *4    | $V_{DD} \simeq 15V$ ,<br>$I_D = 4.0A$ , | -      | 1.0  | -    | nC     |
| Gate - Drain charge  | Q <sub>gd</sub> *4    | $V_{GS} = 5V$                           | -      | 1.0  | -    |        |

# ●Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

| Darameter                  | Symbol             | Conditions                                  | Values |      |      | Unit  |
|----------------------------|--------------------|---|--------|------|------|-------|
| Parameter                  | Symbol             | Conditions                                  | Min.   | Тур. | Max. | Offic |
| Continuous forward current | I <sub>S</sub>     | T = 25°C                                    | -      | -    | 1.0  | Α     |
| Pulse forward current      | I <sub>SP</sub> *1 | T <sub>a</sub> = 25°C                       | -      | -    | 12   | Α     |
| Forward voltage            | V <sub>SD</sub> *4 | V <sub>GS</sub> = 0V, I <sub>S</sub> = 4.0A | -      | -    | 1.2  | V     |

Fig.1 Power Dissipation Derating Curve

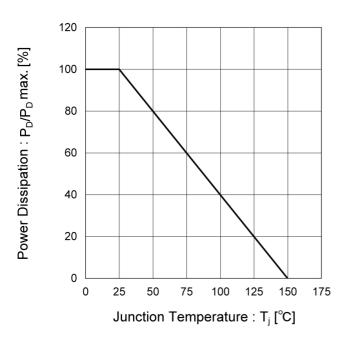
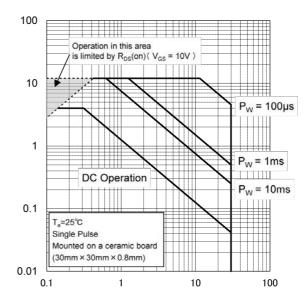


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage :  $V_{DS}[V]$ 

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

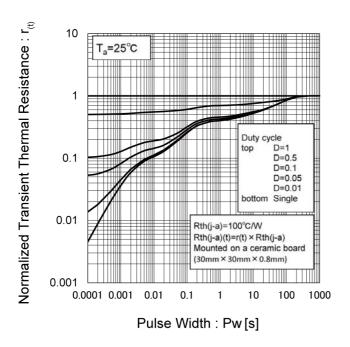
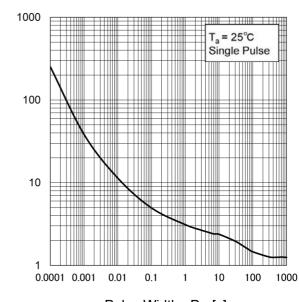


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width : Pw [s]

Peak Transient Power : P(W)

Drain Current : I<sub>D</sub> [A]

### • Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)

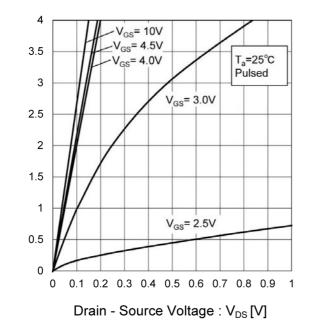
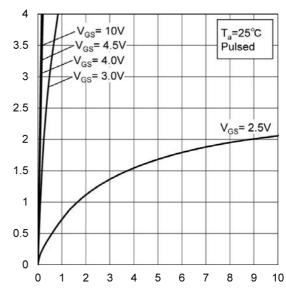


Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs.

Junction Temperature

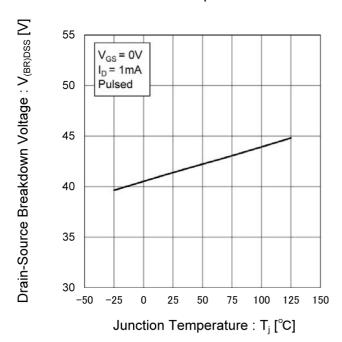


Fig.8 Typical Transfer Characteristics

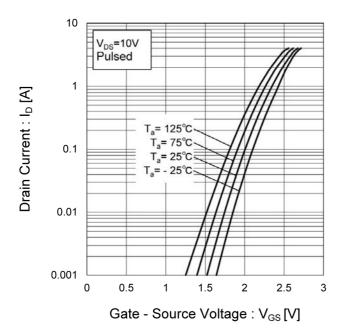
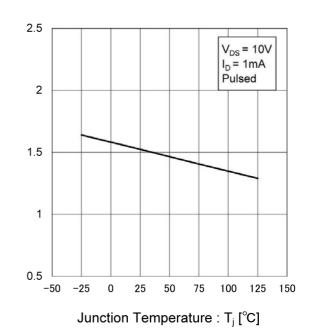


Fig.9 Gate Threshold Voltage vs. Junction Temperature



Gate Threshold Voltage :  $V_{GS(th)}\left[V\right]$ 

Fig.10 Forward Transfer Admittance vs.
Drain Current

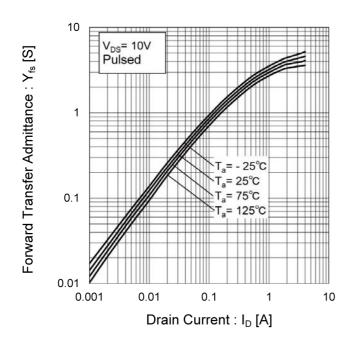


Fig.11 Drain Current Derating Curve

120 100 Drain Current Dissipation 80 : I<sub>D</sub>/I<sub>D</sub>max. [%] 60 40 20 0 -25 0 25 50 75 100 125 150 Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

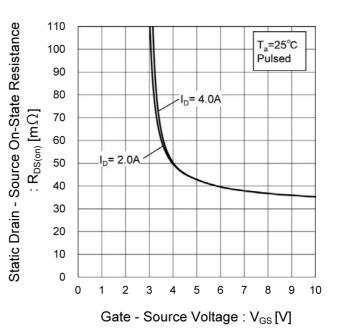


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

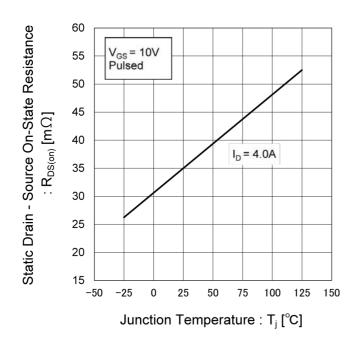


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

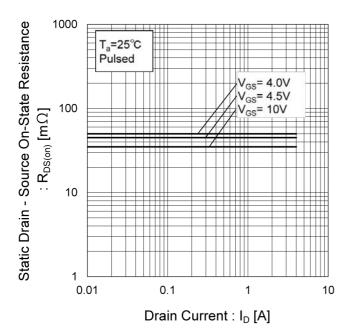


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

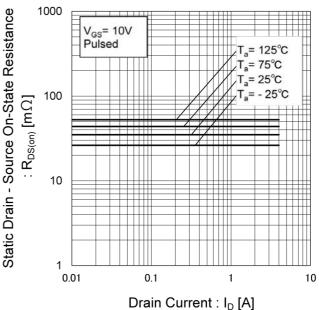


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

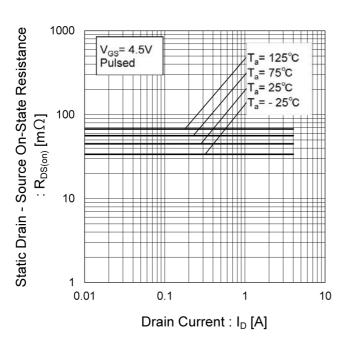


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

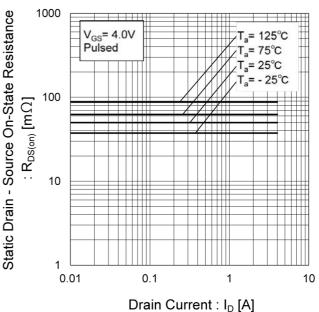


Fig.18 Typical Capacitance vs.

Drain - Source Voltage

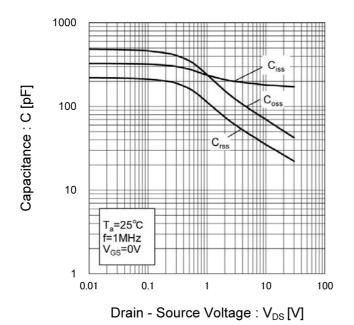


Fig.19 Switching Characteristics

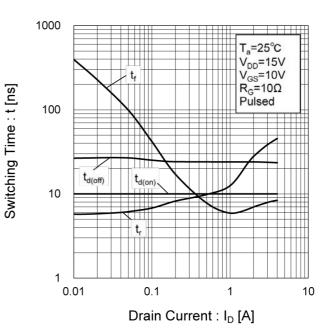


Fig.20 Dynamic Input Characteristics

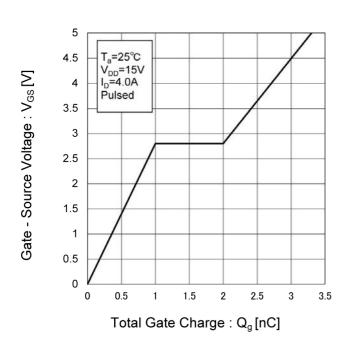
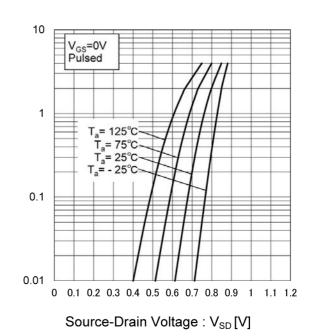


Fig.21 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

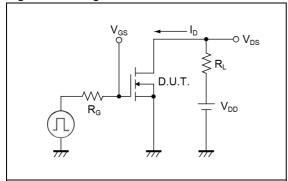


Fig.2-1 Gate Charge Measurement Circuit

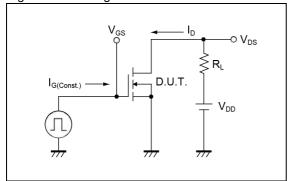


Fig.1-2 Switching Waveforms

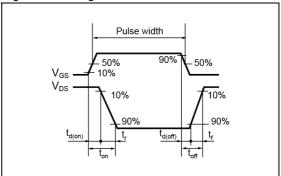
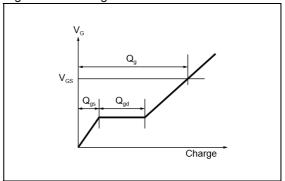


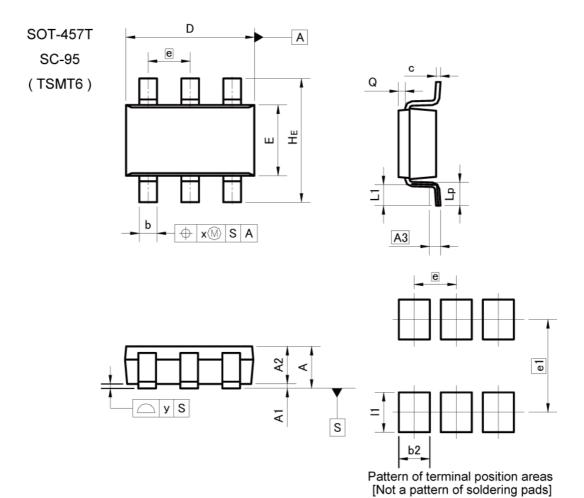
Fig.2-2 Gate Charge Waveform



### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

## Dimensions



| DIM | MILIM           | ETERS | INC   | HES   |
|-----|-----------------|-------|-------|-------|
| DIM | MIN             | MAX   | MIN   | MAX   |
| Α   | # <del>**</del> | 1.00  | =     | 0.039 |
| A1  | 0.00            | 0.10  | 0.000 | 0.004 |
| A2  | 0.75            | 0.95  | 0.030 | 0.037 |
| A3  | 0.              | 25    | 0.0   | 10    |
| b   | 0.35            | 0.50  | 0.014 | 0.020 |
| С   | 0.10            | 0.26  | 0.004 | 0.010 |
| D   | 2.80            | 3.00  | 0.110 | 0.118 |
| E   | 1.50            | 1.80  | 0.059 | 0.071 |
| е   | 0.95            |       | 0.0   | 37    |
| HE  | 2.60            | 3.00  | 0.102 | 0.118 |
| L1  | 0.30            | 0.60  | 0.012 | 0.024 |
| Lp  | 0.40            | 0.70  | 0.016 | 0.028 |
| Q   | 0.05            | 0.25  | 0.002 | 0.010 |
| x   | 877             | 0.20  | =     | 0.008 |
| у   | <del>(e</del>   | 0.10  |       | 0.004 |

| DIM | MILIM           | MILIMETERS |                | HES   |
|-----|-----------------|------------|----------------|-------|
| DIM | MIN             | MAX        | MIN            | MAX   |
| b2  |                 | 0.70       | -              | 0.028 |
| e1  | 2.              | 10         | 0.0            | 083   |
| 11  | 8 <del>2-</del> | 0.90       | <del>=</del> : | 0.035 |

Dimension in mm/inches



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| CLASSIV | CLASSIII | CLASSIII   | CLASSIII |

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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