

RQ6E045RP

## Pch -30V -4.5A Small Signal MOSFET

## Datasheet

V <sub>DSS</sub>	-30V
R <sub>DS(on)</sub> (Max.)	35mΩ
I <sub>D</sub>	±4.5A
P <sub>D</sub>	1.25W

## Features

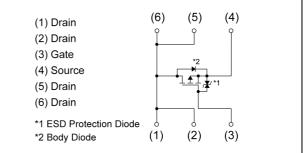
Application

Switching

- 1) Low on resistance
- 2) Built-in G-S protection diode
- 3) Small surface mount package(TSMT6)
- 4) Pb-free lead plating ; RoHS compliant

● Outline	
SOT-457T	
SC-95	
TSMT6	(1)

#### ●Inner circuit



## Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TR
	Marking	UB

## • Absolute maximum ratings (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	-30	V
Continuous drain current	Ι <sub>D</sub>	±4.5	Α
Pulsed drain current	I*1	±18	А
Gate - Source voltage	V <sub>GSS</sub>	±20	V
Dower discipation	P <sub>D</sub> *2	1.25	W
Power dissipation	P <sub>D</sub> *3	0.95	W
Junction temperature	Tj	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	S°

#### •Thermal resistance

Deremeter	Sympol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres, innetion, empiont	$R_{thJA}^{*2}$	-	-	100	°C/W
Thermal resistance, junction - ambient	$R_{thJA}^{*3}$	-	-	132	°C/W

## •Electrical characteristics (T<sub>a</sub> = 25°C)

Deremeter	Symbol Conditions -		Values			1.1
Parameter			Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = -1mA$		-30	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	-24.1	-	mV/°C
Zero gate voltage drain current	$I_{DSS}$ $V_{DS}$ = 30V, $V_{GS}$ = 0V		-	-	-1	μA
Gate - Source leakage current $I_{GSS}$ $V_{GS} = \pm 20$		$V_{GS}$ = ±20V, $V_{DS}$ = 0V	-	-	±10	μA
Gate threshold voltage	$V_{GS(th)}$ $V_{DS}$ = -10V, $I_D$ = -1mA		-1.0	-	-2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = -1mA referenced to 25°C	-	3.3	-	mV/°C
		V <sub>GS</sub> = -10V, I <sub>D</sub> = -4.5A	-	25	35	
Static drain - source on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -2.2A	-	34	48	mΩ
		V <sub>GS</sub> = -4.0V, I <sub>D</sub> = -2.2A	-	38	53	
Gate resistance	R <sub>G</sub> f = 1MHz, open drain		-	14	-	Ω
Forward Transfer Admittance	Y <sub>fs</sub>   <sup>*4</sup>	V <sub>DS</sub> = -10V, I <sub>D</sub> = -4.5A	3.5	-	-	S

\*1 Pw $\leq$ 10µs , Duty cycle $\leq$ 1%

\*2 Mounted on a ceramic board (30×30×0.8mm)

- \*3 Mounted on a FR4 (25×25×0.8mm)
- \*4 Pulsed



# • Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Cump of	Conditions		Linit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	1350	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = -10V	-	180	-	pF
Reverse transfer capacitance	C <sub>rss</sub>	C <sub>rss</sub> f = 1MHz		180	-	
Turn - on delay time	$t_{d(on)}^{*4}$ V <sub>DD</sub> ~ -15V,V <sub>GS</sub> = -10V		-	10	-	
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = -2.2A	-	35	-	20
Turn - off delay time	$t_{d(off)}^{*4}$ $R_L \simeq 6.8\Omega$		-	110	-	ns
Fall time	t <sub>f</sub> *4	R <sub>G</sub> = 10Ω	-	65	-	

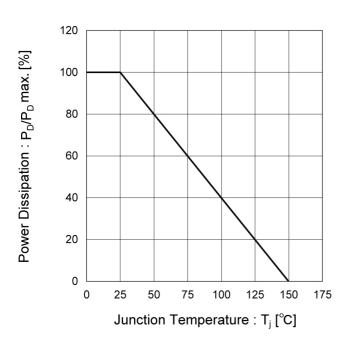
## • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Symbol Condutoris		Тур.	Max.	Unit
Total gate charge	$Q_g^{*4}$	V <sub>DD</sub> ≃ -15V,	-	14	-	
Gate - Source charge	Q <sub>gs</sub> *4	I <sub>D</sub> = -4.5A, V <sub>GS</sub> = -5V	-	3.5	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	V <sub>GS</sub> = -5V	-	4.2	-	

# •Body diode electrical characteristics (Source-Drain) ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	Conditions	Values			Unit
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	۱ <sub>s</sub>	$T = 25^{\circ}$	-	-	-1	А
Pulse forward current	$I_{SP}^{*1}$	T <sub>a</sub> = 25°C	-	-	-18	А
Forward voltage	$V_{SD}^{*4}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = -4.5A	-	-	-1.2	V





## Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

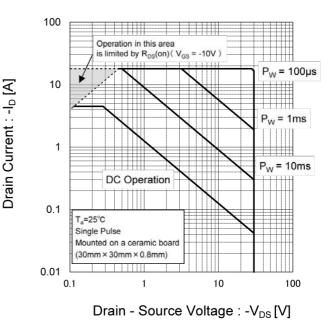
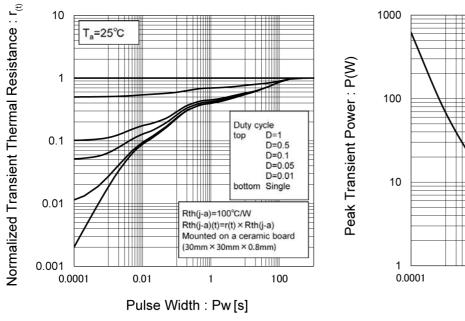
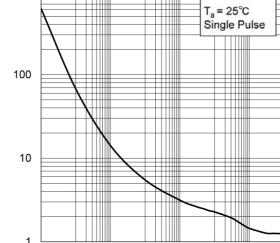


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power dissipation





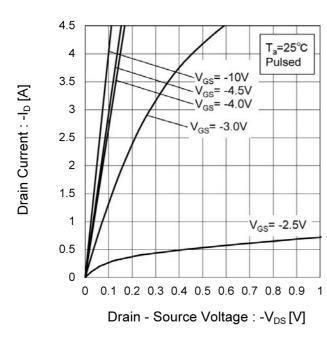
Pulse Width : Pw [s]

1

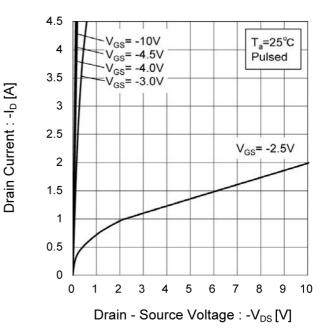


0.01

100

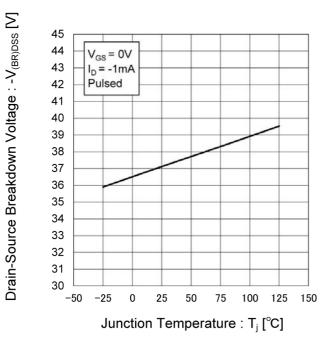


#### Fig.5 Typical Output Characteristics(I)



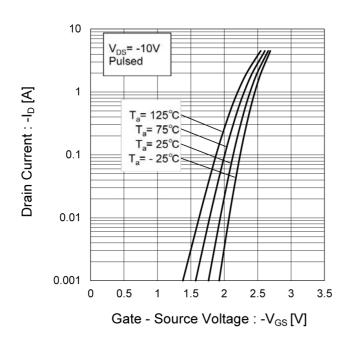
## Fig.6 Typical Output Characteristics(II)

Fig.7 Breakdown Voltage vs. Junction Temperature



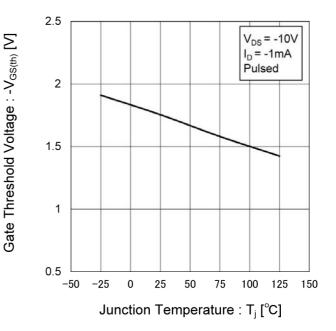




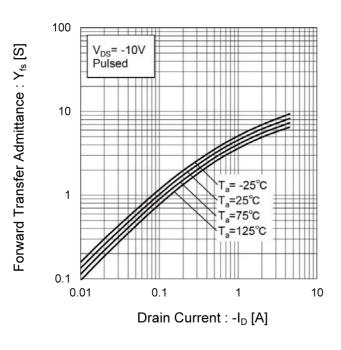


## Fig.8 Typical Transfer Characteristics

Fig.9 Gate Threshold Voltage vs. Junction Temperature



## Fig.10 Forward Transfer Admittance vs. Drain Current





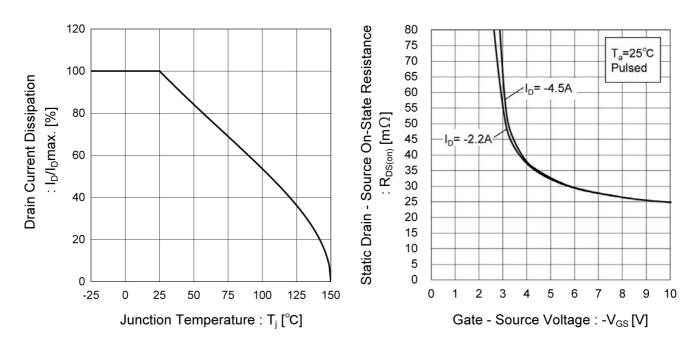
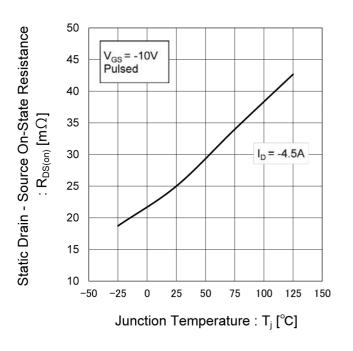


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature







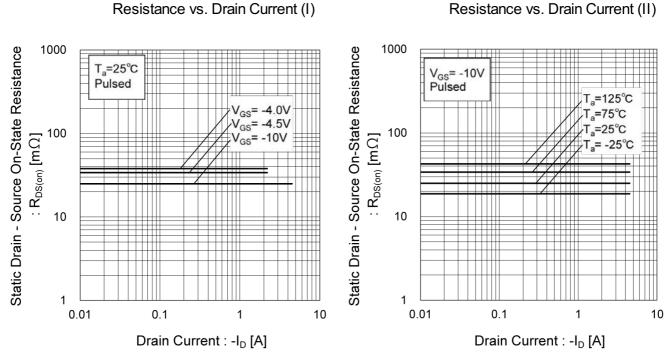
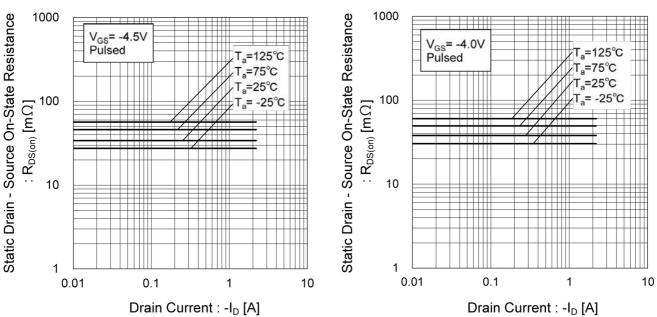


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

> Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

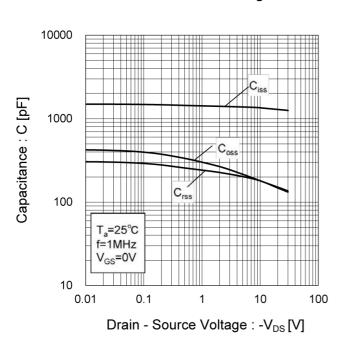
Fig.15 Static Drain - Source On - State



Resistance vs. Drain Current (III)

Fig.16 Static Drain - Source On - State





## Fig.18 Typical Capacitance vs. Drain - Source Voltage

Fig.19 Switching Characteristics

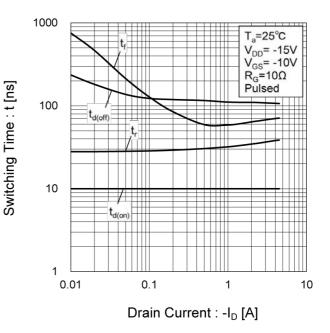
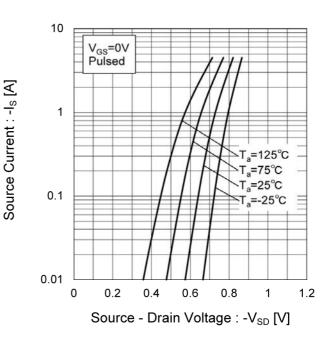


Fig.20 Dynamic Input Characteristics

5 4.5 Gate - Source Voltage : -V<sub>GS</sub> [V] 4 3.5 3 2.5 2 1.5 T<sub>a</sub>=25°C V<sub>DD</sub>= -15V 1 I<sub>D</sub>= -4.5A Pulsed 0.5 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 0 Total Gate Charge : Q<sub>q</sub> [nC]

Fig.21 Source Current vs. Source Drain Voltage





#### Measurement circuits



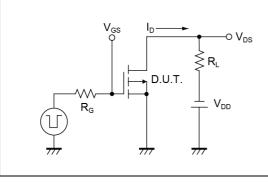


Fig.2-1 Gate Charge Measurement Circuit

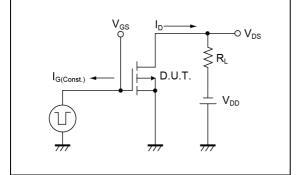
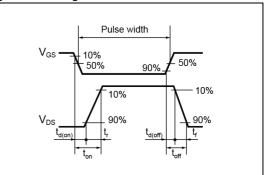
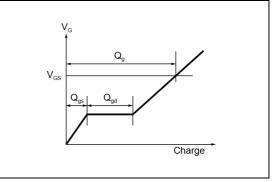


Fig.1-2 Switching Waveforms







## Notice

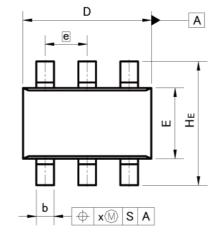
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

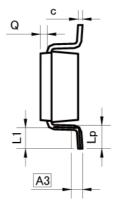


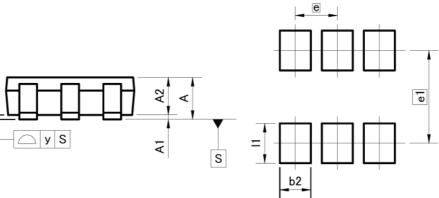


## Dimensions









Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
A	8 <b>4</b>	1.00	-	0.039
A1	0.00	0.10	0.000	0.004
A2	0.75	0.95	0.030	0.037
A3	0.	25	0.0	)10
b	0.35	0.50	0.014	0.020
С	0.10	0.26	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.010
x		0.20	-	0.008
У	÷	0.10	÷	0.004
	MILIM	ETERS	INC	HES
DIM -	MIN	MAX	MIN	MAX
b2	]	0.70	-	0.028
e1	2.	10	0.0	083

Dimension in mm/inches

-

11



0.90

-



0.035

# Notice

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  - [h] Use of the Products in places subject to dew condensation
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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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