

Pch -30V -10A Power MOSFET

V_{DSS}	-30V
$R_{DS(on)}(Max.)$	12.6m $Ω$
I _D	-10A
P_D	2.0W

RRH100P03

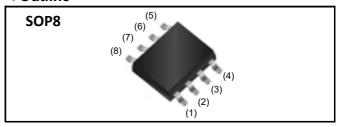
Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (SOP8).
- 4) Pb-free lead plating; RoHS compliant

Application

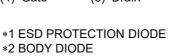
DC/DC Converter

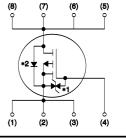
Outline



•Inner circuit

- (1) Source(2) Source
- (5) Drain
- (3) Source
- (6) Drain (7) Drain
- (4) Gate
- (7) Drain (8) Drain





Packaging specifications

	Packaging	Taping
	Reel size (mm)	330
Type	Tape width (mm)	12
Туре	Basic ordering unit (pcs)	2,500
	Taping code	ТВ
	Marking	RRH100P03

● **Absolute maximum ratings**(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	-30	V
Continuous drain current	I _D *1	±10	А
Pulsed drain current	I _{D,pulse} *2	±40	А
Gate - Source voltage	V_{GSS}	±20	V
Avalanche energy, single pulse	E _{AS} *3	0.8	mJ
Dower dissination	P _D *4	2.0	W
Power dissipation	P _D *5	0.65	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	−55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
raiailletei	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	62.5	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	192	°C/W

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
r al allietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = -1mA$	-30	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	1	-25	1	mV/°C
Zero gate voltage drain current	I _{DSS}	$V_{DS} = -30V, V_{GS} = 0V$	ı	-	-1	μА
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±10	μΑ
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	1	-2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	-	3.9	-	mV/°C
		$V_{GS} = -10V, I_D = -10A$	-	9.0	12.6	
Static drain - source	R _{DS(on)} *6	$V_{GS} = -4.5V, I_{D} = -5A$	-	12.5	17.5	mΩ
on - state resistance		$V_{GS} = -4.0V, I_D = -5A$	-	14.0	19.6	11122
		V _{GS} = -10V, I _D = -10A, T _j =125°C	ı	14.0	20.0	
Gate input resistannce	R_{G}	f = 1MHz, open drain	-	3.0	-	Ω
Transconductance	${\sf g_{fs}}^{*6}$	$V_{DS} = -10V, I_{D} = -10A$	13	26	-	S

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw $\leq 10 \mu s, \, Duty \, cycle \leq 1\%$

^{*3} L \simeq 10 μ H, V_{DD} = -15V, Rg = 25 Ω , starting T_i = 25°C

^{*4} Mounted on a ceramic board (30×30×0.8mm)

^{*5} Mounted on a FR4 (20×20×0.8mm)

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	3600	-	
Output capacitance	C_{oss}	V _{DS} = -10V	-	450	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	450	-	
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq -15V, V_{GS} = -10V$	ı	25	-	
Rise time	t _r *6	$I_D = -5A$	-	60	-	no
Turn - off delay time	${\rm t_{d(off)}}^{*6}$	$R_L = 3.0\Omega$	-	150	-	ns
Fall time	t _f *6	$R_G = 10\Omega$	-	100	-	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai illetei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	$Q_g^{^{*6}}$	$V_{DD}^{\sim} -15V, I_{D} = -10A$ $V_{GS} = -5V$	-	39	-	
Total gate charge	Q_{g}	$V_{DD}^{\sim} -15V, I_{D} = -10A$ $V_{GS} = -10V$	-	68	-	nC
Gate - Source charge	Q _{gs} *6	$V_{DD} \simeq -15V, I_{D} = -10A$	-	8.5	-	
Gate - Drain charge	Q _{gd} *6	V _{GS} = -5V	-	13.5	-	

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions	Conditions	Values			Unit
r arameter	Symbol Conditions –		Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _a = 25°C	-	-	-1.6	Α
Forward voltage	V _{SD} *6	$V_{GS} = 0V, I_s = -10A$	ı	ı	-1.2	V
Reverse recovery time	t _{rr} *6	I _S = -10A	1	40	80	ns
Reverse recovery charge	Q _{rr} *6	di/dt = 100A / μs	-	35	70	μС

^{*6} Pulsed

Fig.1 Power Dissipation Derating Curve

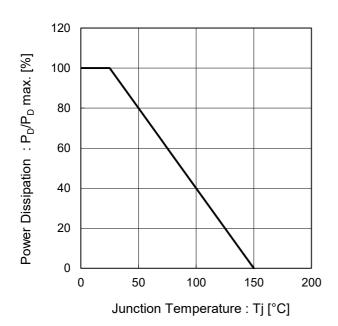


Fig.2 Maximum Safe Operating Area

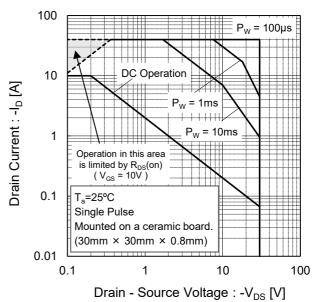
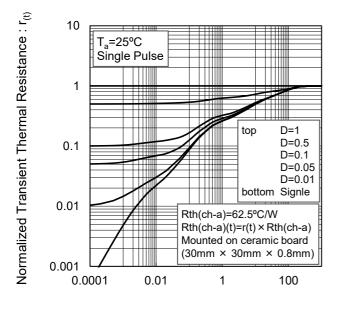
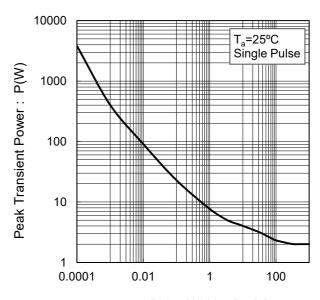


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width : P_W [s]

Fig.4 Single Pulse Maxmum Power dissipation



Pulse Width : P_W [s]

Fig.5 Avalanche Current vs Inductive Load

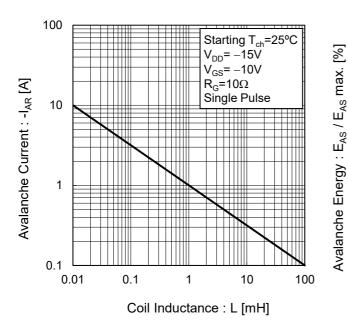


Fig.6 Avalanche Energy Derating Curve vs Junction Temperature

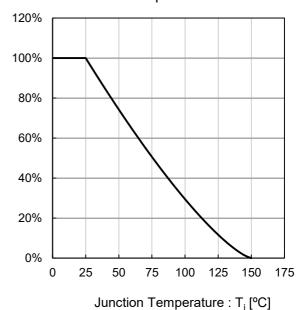


Fig.7 Typical Output Characteristics(I)

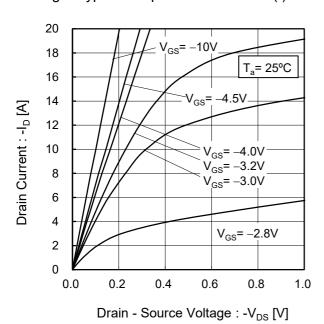
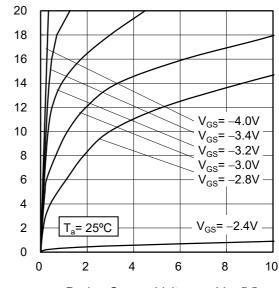


Fig.8 Typical Output Characteristics(II)



Drain Current: -l_D [A]

Fig.9 Breakdown Voltage

vs. Junction Temperature

Very Source Breakdown Voltage 40

Very Source Br

Fig.10 Typical Transfer Characteristics

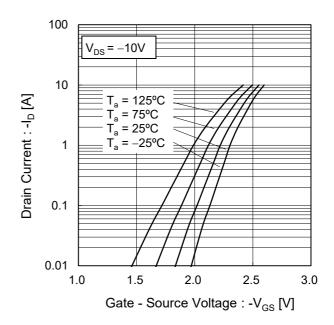


Fig.11 Gate Threshold Voltage vs. Junction Temperature

Junction Temperature : T_i [°C]

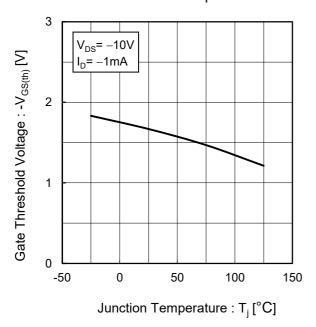


Fig.12 Transconductance vs. Drain Current

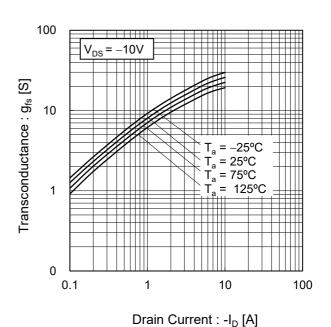


Fig.13 Drain CurrentDerating Curve

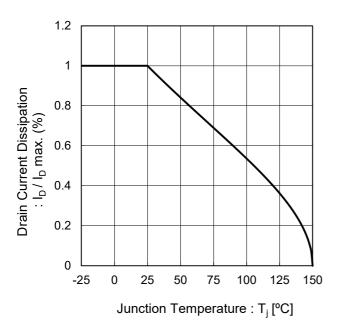
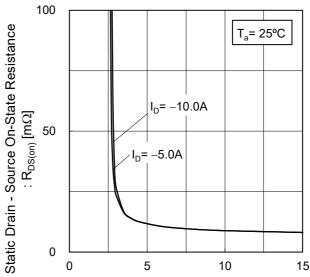


Fig.14 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : -V_{GS} [V]

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(I)

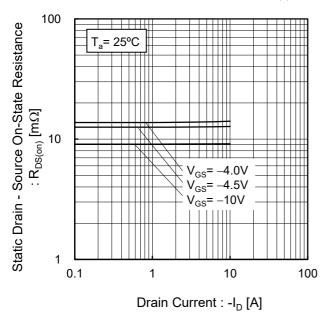
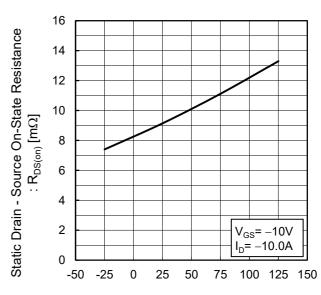


Fig.16 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_i [°C]

100

•Electrical characteristic curves

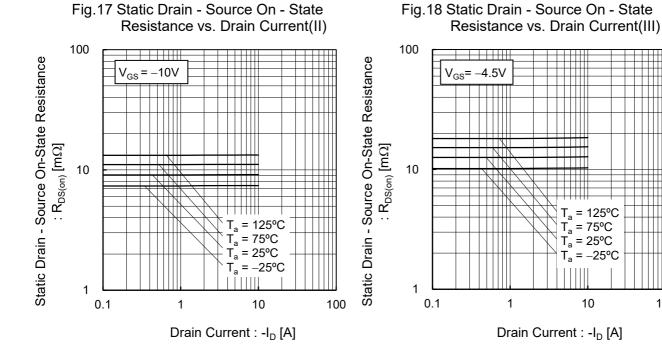


Fig.19 Static Drain - Source On - State Resistance vs. Drain Current(IV)

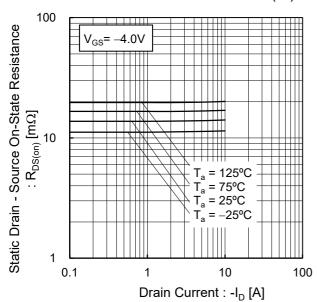


Fig.20 Typical Capacitance vs. Drain - Source Voltage 10000 +++++ C_{iss} Capacitance: C [pF] 1000 C_{rss} $\mathsf{C}_{\mathsf{oss}}$ 100 T_a = 25°C f=1MHz V_{GS}=0V 10 0.01 0.1 10 100 Drain - Source Voltage : -V_{DS} [V]

10000 $T_a = 25^{\circ}C$ $V_{DD} = -15V$ $V_{GS} = -10V$ $R_{G} = 10\Omega$

Fig.21 Switching Characteristics

Fig.22 Dynamic Input Characteristics

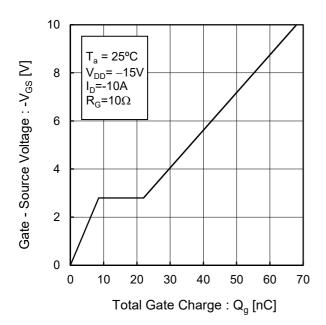
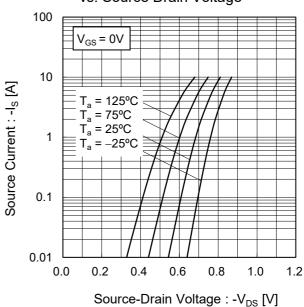


Fig.23 Source Current vs. Source Drain Voltage

Drain Current : -I_D [A]



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

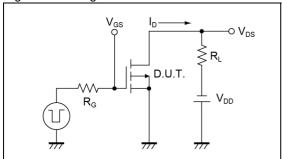


Fig.2-1 Gate Charge Measurement Circuit

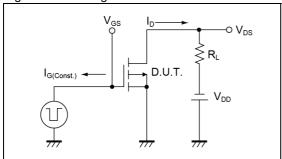


Fig.3-1 Avalanche Measurement Circuit

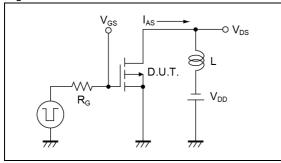


Fig.1-2 Switching Waveforms

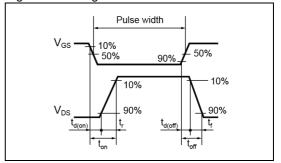


Fig.2-2 Gate Charge Waveform

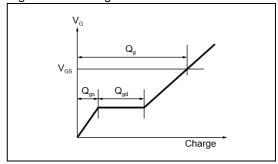
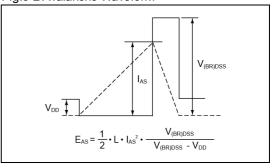
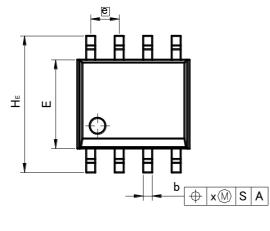


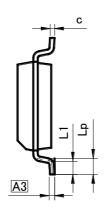
Fig.3-2 Avalanche Waveform

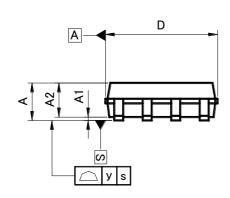


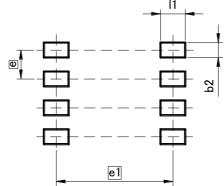
●Dimensions (Unit : mm)

SOP8









Patterm of terminal position areas

DIM	MILIMETERS		INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	-	1.75	_	0.069	
A1	0.	15	0.0	006	
A2	1.40	1.60	0.055	0.063	
A3	0.2	25	0.0	01	
b	0.30	0.50	0.012	0.02	
С	0.10	0.30	0.004	0.012	
D	4.80	5.20	0.189	0.205	
Е	3.75	4.05	0.148	0.159	
е	1.2	27	0.05		
HE	5.70	6.30	0.224	0.248	
L1	0.50	0.70	0.02	0.028	
Lp	0.65	0.85	0.026	0.033	
х	0.15		0.006		
у	0.	10	0.0	004	

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2	_	0.65	_	0.026
e1	5.15		0.2	03
l1	_	1.15	_	0.045

Dimension in mm/inches

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CLASSⅢ	CLASSⅢ	CLASS II b	CLASSIII
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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