

Pch -30V -14A Power MOSFET

V_{DSS}	-30V
R _{DS(on)} (Max.)	7m Ω
I _D	-14A
P_D	2.0W

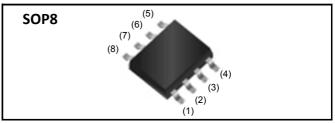
● Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (SOP8).
- 4) Pb-free lead plating; RoHS compliant

Application

DC/DC Converter

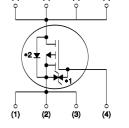
Outline



•Inner circuit

- (1) Source (2) Source
- (5) Drain(6) Drain
- (3) Source
- (7) Drain
- (4) Gate
- (8) Drain





Packaging specifications

	Packaging	Taping
	Reel size (mm)	330
Typo	Tape width (mm)	12
Туре	Basic ordering unit (pcs)	2,500
	Taping code	ТВ
	Marking	RRH140P03

●Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	-30	V
Continuous drain current	I _D *1	±14	А
Pulsed drain current	I _{D,pulse} *2	±56	А
Gate - Source voltage	V_{GSS}	±20	V
Avalanche energy, single pulse	E _{AS} *3	1.6	mJ
Dower dissination	P _D *4	2.0	W
Power dissipation	P _D *5	0.65	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	−55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farametei	Зуппоп	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	62.5	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	192	°C/W

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit	
r ai ai i letei	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = -1mA$	-30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	1	-25	1	mV/°C	
Zero gate voltage drain current	I _{DSS}	$V_{DS} = -30V, V_{GS} = 0V$	ı	-	-1	μА	
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±10	μА	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	-	-2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	-	3.9	-	mV/°C	
		V _{GS} =-10V, I _D =-14A	-	5.0	7.0		
Static drain - source	R _{DS(on)} *6	V _{GS} =-4.5V, I _D =-7A	-	6.7	9.4	mΩ	
on - state resistance		V _{GS} =-4.0V, I _D =-7A	-	7.3	10.2	11122	
		V _{GS} = -10V, I _D = -14A, T _j =125°C	ı	7.5	10.5		
Gate input resistannce	R_{G}	f = 1MHz, open drain	-	3.0	-	Ω	
Transconductance	${\sf g_{fs}}^{*6}$	$V_{DS} = -10V, I_{D} = -14A$	20	40	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10 $\mu s, \ Duty \ cycle \leq$ 1%

^{*3} L \simeq 10 μ H, V_{DD} = -15V, Rg = 25 Ω , starting T_i = 25 $^{\circ}$ C

^{*4} Mounted on a ceramic board (30×30×0.8mm)

^{*5} Mounted on a FR4 (20×20×0.8mm)

•Electrical characteristics($T_a = 25^{\circ}C$)

Parameter	Symbol	Conditions	Values			Unit
r ai ai ii etei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	8000	-	
Output capacitance	C_{oss}	V _{DS} = -10V	-	1000	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	ı	1000	ı	
Turn - on delay time	${t_{d(on)}}^{*6}$	$V_{DD} \simeq -15V$, $V_{GS} = -10V$	ı	32	-	
Rise time	t _r *6	$I_D = -7A$	ı	80	-	ne
Turn - off delay time	${\rm t_{d(off)}}^{*6}$	$R_L = 2.1\Omega$	ı	360	-	ns
Fall time	t_f^{*6}	$R_G = 10\Omega$	-	200	-	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	${\sf Q_g}^{*6}$	$V_{DD} = -15V, I_D = -14A$ $V_{GS} = -5V$	-	80	1	
Total gate charge	Qg	$V_{DD}^{\sim} -15V, I_{D} = -14A$ $V_{GS} = -10V$	-	150	-	nC
Gate - Source charge	Q _{gs} *6	$V_{DD}^{2} - 15V, I_{D} = -14A$ $V_{GS} = -5V$	-	18	-	
Gate - Drain charge	Q _{gd} *6	$V_{GS} = -5V$	-	30	-	

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Darameter	smeter Symbol Conditions -		Values			Unit
- Faranietei			Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _a = 25°C	-	-	-1.6	Α
Forward voltage	V _{SD} *6	$V_{GS} = 0V, I_{s} = -14A$	_	-	-1.2	V
Reverse recovery time	t _{rr} *6	I _S = -14A	-	50	100	ns
Reverse recovery charge	Q _{rr} *6	di/dt = 100A / μs	-	50	100	μС

^{*6} Pulsed

Fig.1 Power Dissipation Derating Curve

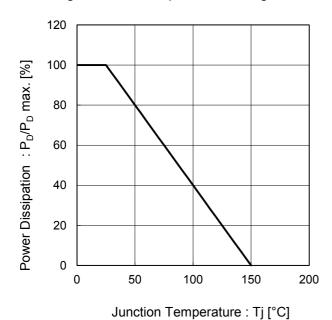
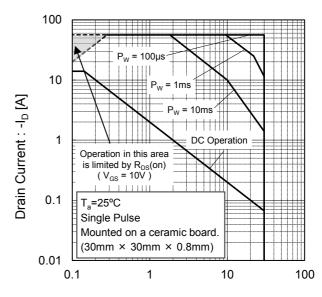
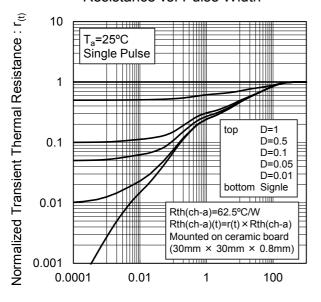


Fig.2 Maximum Safe Operating Area



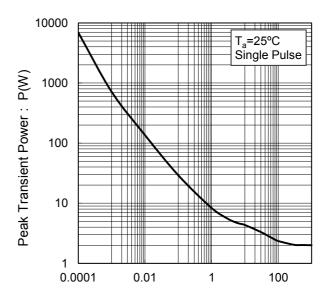
Drain - Source Voltage : -V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width: Pw [s]

Fig.4 Single Pulse Maxmum Power dissipation



Pulse Width: Pw [s]

Fig.5 Avalanche Current vs Inductive Load

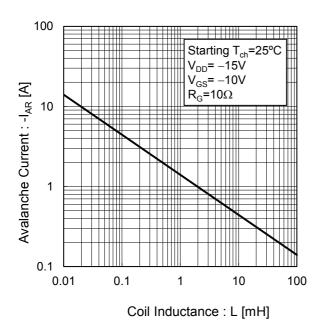
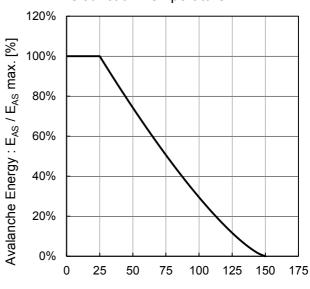


Fig.6 Avalanche Energy Derating Curve vs Junction Temperature



Junction Temperature : T_i [°C]

Fig.7 Typical Output Characteristics(I)

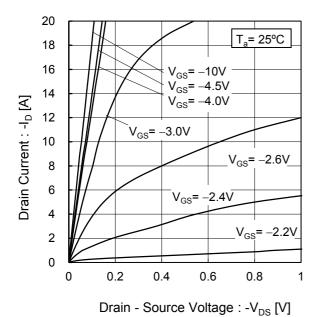
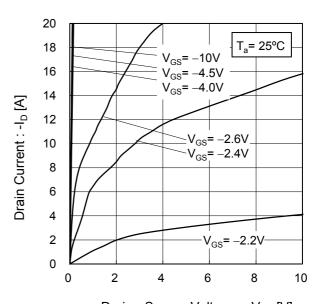


Fig.8 Typical Output Characteristics(II)



Drain - Source Voltage : -V_{DS} [V]

Fig.9 Breakdown Voltage
vs. Junction Temperature

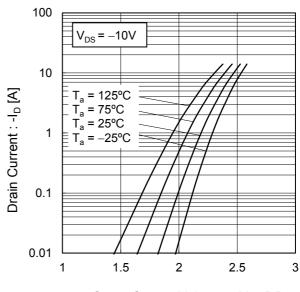
V_{GS}=0V

I_D= -1mA

40

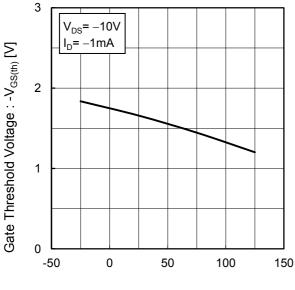
Junction Temperature: T_i [°C]

Fig.10 Typical Transfer Characteristics



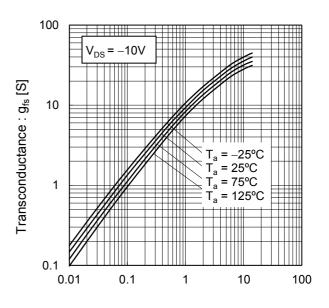
Gate - Source Voltage : -V_{GS} [V]

Fig.11 Gate Threshold Voltage vs. Junction Temperature



Junction Temperature : T_j [°C]

Fig.12 Transconductance vs. Drain Current



Drain Current : -I_D [A]

Fig.13 Drain Current Derating Curve

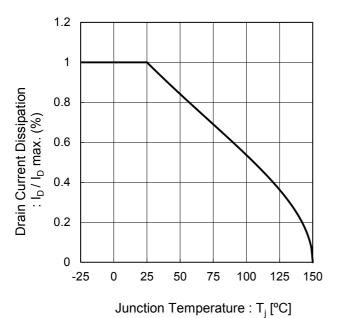
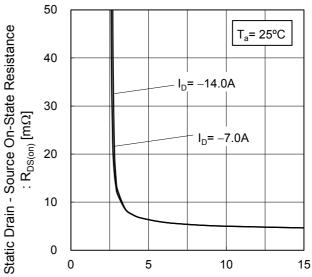


Fig.14 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : -V_{GS} [V]

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(I)

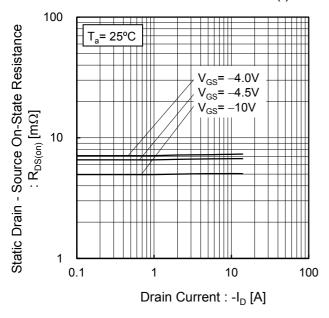
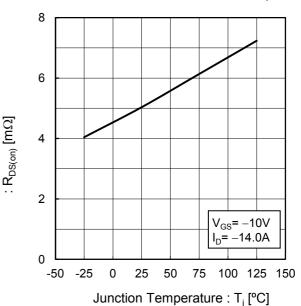


Fig.16 Static Drain - Source On - State Resistance vs. Junction Temperature



Static Drain - Source On-State Resistance

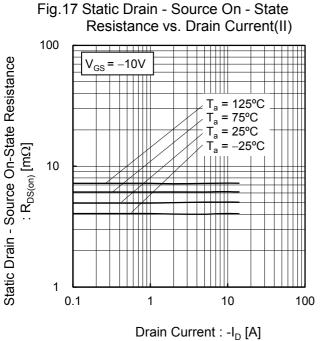


Fig.18 Static Drain - Source On - State
Resistance vs. Drain Current(III)

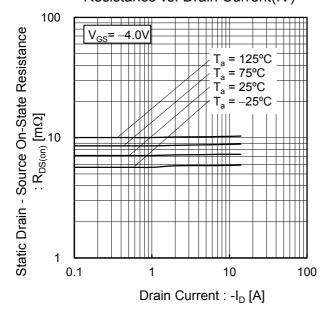
To a state

Resistance vs. Drain Current(III)

To a state

To a

Fig.19 Static Drain - Source On - State Resistance vs. Drain Current(IV)



100

0.01

0.1

•Electrical characteristic curves

Drain Current : -I_D [A]

Fig.22 Dynamic Input Characteristics

10

Drain - Source Voltage : - V_{DS} [V]

100

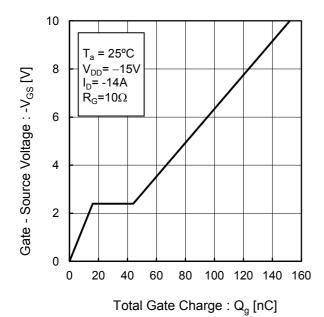


Fig.23 Source Current vs. Source Drain Voltage 100 $V_{GS} = 0V$ 10 Source Current : -I_S [A] = 125°C = 75°C = 25°C 0.1 -25°C 0.01 0.5 1 0 1.5 Source-Drain Voltage: -V_{DS} [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

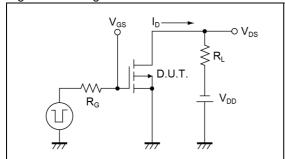


Fig.2-1 Gate Charge Measurement Circuit

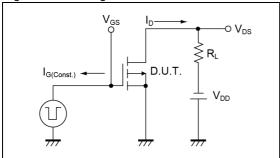


Fig.3-1 Avalanche Measurement Circuit

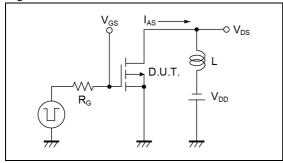


Fig.1-2 Switching Waveforms

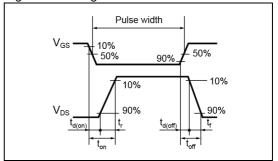


Fig.2-2 Gate Charge Waveform

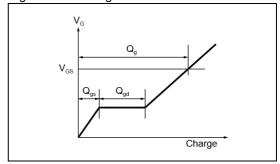
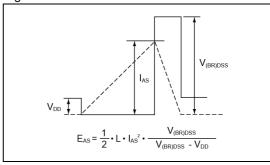
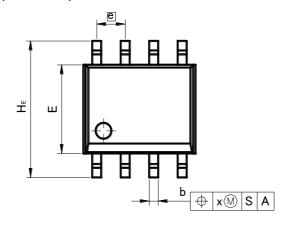


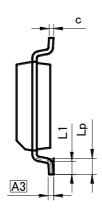
Fig.3-2 Avalanche Waveform

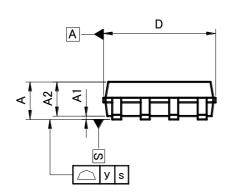


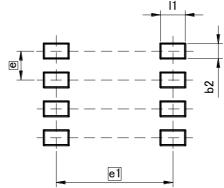
●Dimensions (Unit: mm)











Patterm of terminal position areas

DIM	MILIMI	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	-	1.75	_	0.069	
A1	0.	15	0.0	006	
A2	1.40	1.60	0.055	0.063	
A3	0.2	25	0.0	01	
b	0.30	0.50	0.012	0.02	
С	0.10	0.30	0.004	0.012	
D	4.80	5.20	0.189	0.205	
Е	3.75	4.05	0.148	0.159	
е	1.2	27	0.0	05	
HE	5.70	6.30	0.224	0.248	
L1	0.50	0.70	0.02	0.028	
Lp	0.65	0.85	0.026	0.033	
х	0.15		0.006		
у	0.	10	0.0	004	

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2	_	0.65	_	0.026
e1	5.15		0.2	03
l1	_	1.15	_	0.045

Dimension in mm/inches

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CLASSⅢ	CLASSⅢ	CLASS II b	CLASSⅢ
CLASSIV	CLASSIII	CLASSⅢ	CLASSIII

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- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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 exceeding the recommended storage time period.
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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