## Nch 30V 40A Middle Power MOSFET

V <sub>DSS</sub>	30V
R <sub>DS(on)</sub> (Max.)	3.2mΩ
I <sub>D</sub>	±40A
P <sub>D</sub>	30W

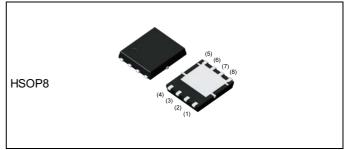
# ● Features

- 1) Low on resistance.
- 2) High Power small mold Package (HSOP8).
- 3) Pb-free lead plating; RoHS compliant.
- 4) Halogen Free.

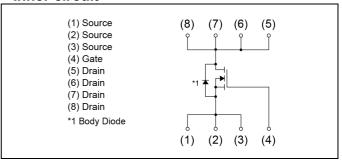
# Application

Switching

### Outline



## ●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	RS1E240BN

# ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V <sub>DSS</sub>	30	V	
Continuous dusin suument	T <sub>c</sub> = 25°C	I <sub>D</sub> *1	±40	Α
Continuous drain current	T <sub>a</sub> = 25°C	I <sub>D</sub>	±24	Α
Pulsed drain current	I <sub>DP</sub> *2	±96	А	
Gate - Source voltage	$V_{GSS}$	±20	V	
Avalanche current, single pulse		I <sub>AS</sub> *3	40	Α
Avalanche energy, single pulse		E <sub>AS</sub> *3	140	mJ
		P <sub>D</sub> *1	30	W
Power dissipation		P <sub>D</sub> *4	3	W
Junction temperature	T <sub>j</sub>	150	°C	
Operating junction and storage to	T <sub>stg</sub>	-55 to +150	°C	

## ●Thermal resistance

Dorameter	Currente e l	Values			1.1:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R <sub>thJC</sub> *1	-	-	4.2	°C/W
Thermal resistance, junction - ambient	R <sub>thJA</sub> *4	-	-	41.7	°C/W

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davanastan	Current ed	Conditions		Values		Unit	
Parameter	Symbol Conditions		Min.	Тур.	Max.	UTIIL	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0V$ , $I_D = 1mA$	30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to 25°C	-	21	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1	μA	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS} = \pm 16V, V_{DS} = 0V$	-	-	±100	nA	
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	1.0	1	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-3	-	mV/°C	
Static drain - source	D *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 24A	-	2.3	3.2	m0	
on - state resistance	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 24A	-	3.3	4.6	mΩ	
Gate resistance	$R_{G}$	f=1MHz, open drain	-	1.2	-	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 5V, I <sub>D</sub> = 24A	18	-	-	S	

<sup>\*1</sup> Tc=25°C, Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw  $\leq$  10 $\mu$ s, Duty cycle  $\leq$  1%

<sup>\*3</sup> L  $\simeq$  0.05mH,  $V_{DD}$  = 24V,  $R_G$  = 25 $\Omega$ , STARTING  $T_j$  = 25 $^{\circ}$ C Fig.3-1,3-2

<sup>\*4</sup> Mounted on a Cu Board (40×40×0.8mm)

<sup>\*5</sup> Pulsed

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatar	Symbol Conditions		Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	3900	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15V	-	450	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	350	-		
Turn - on delay time	t <sub>d(on)</sub> *4	$V_{DD} \simeq 15V, V_{GS} = 10V$	-	25	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 12A	1	70	1	no	
Turn - off delay time	$t_{d(off)}^{*4}$	R <sub>L</sub> ≃ 1.25Ω		115	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	26	-		

# ● Gate charge characteristics (T<sub>a</sub> = 25°C)

	` u	,					
Parameter	Cymah al	Conditions		Values			Unit
Parameter	Symbol Conditions		OHS	Min.	Тур.	Max.	Offic
Total gate charge	O *4		V <sub>GS</sub> = 10V	-	70	-	
Total gate charge	Q <sub>g</sub> *4	V <sub>DD</sub> ≃ 15V I <sub>D</sub> = 24A		-	35	-	<b>"</b> C
Gate - Source charge	Q <sub>gs</sub> *4		V <sub>GS</sub> = 4.5V	-	10	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4			-	14	-	

# ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
- Farameter	Symbol	ol Conditions		Тур.	Max.	UIIIL	
Continuous forward current	I <sub>S</sub>	T = 25°C	-	-	2.5	Α	
Pulse forward current	I <sub>SP</sub> *2	T <sub>a</sub> = 25℃	-	-	96	Α	
Forward voltage	V <sub>SD</sub> *4	V <sub>GS</sub> = 0V, I <sub>S</sub> = 2.5A	-	-	1.2	V	

Fig.1 Power Dissipation Derating Curve

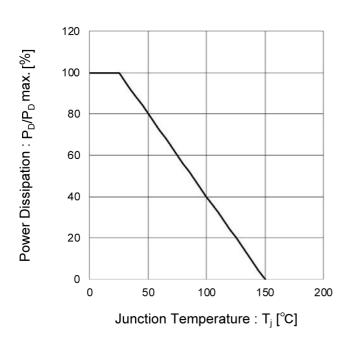
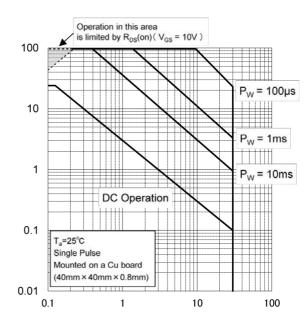


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage: V<sub>DS</sub>[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

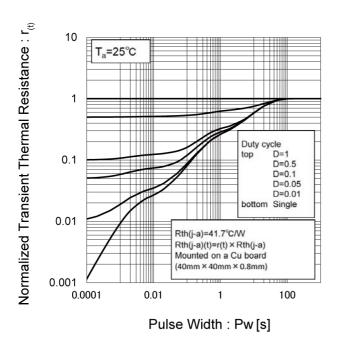


Fig.4 Single Pulse Maximum Power dissipation

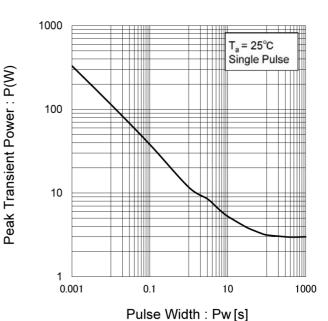


Fig.5 Typical Output Characteristics(I)

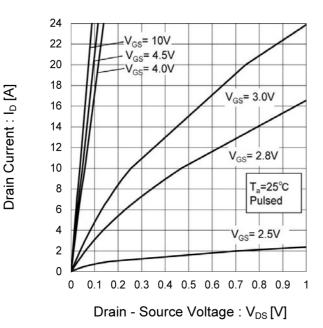
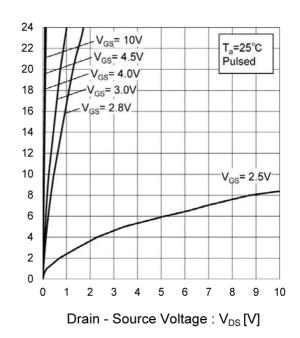


Fig.6 Typical Output Characteristics(II)



Drain Current : I<sub>D</sub> [A]

Fig.7 Breakdown Voltage vs. Junction

Temperature

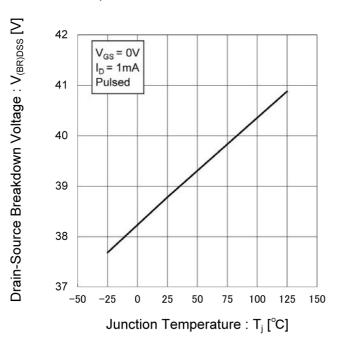


Fig.8 Typical Transfer Characteristics

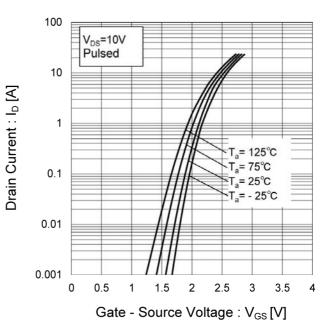


Fig.9 Gate Threshold Voltage vs. Junction Temperature

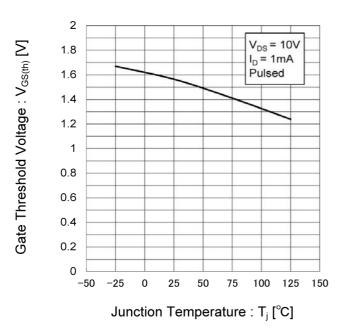


Fig.10 Forward Transfer Admittance vs. Drain Current

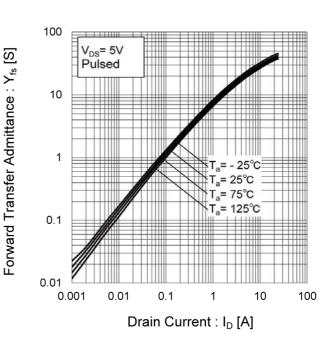


Fig.11 Drain Current Derating Curve

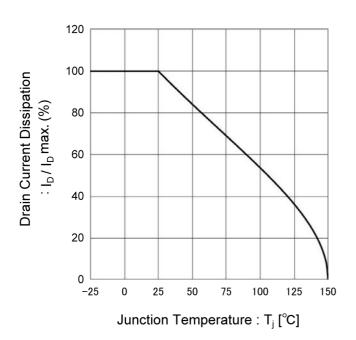


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

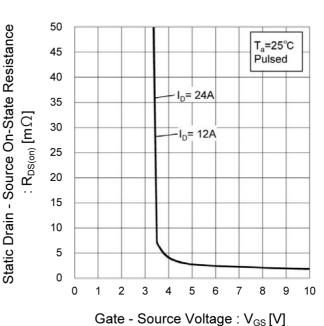


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

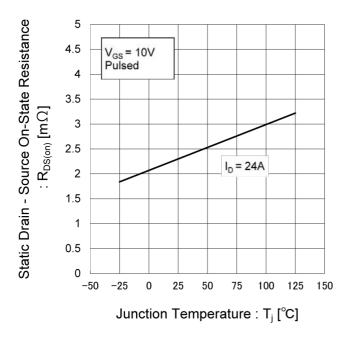


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

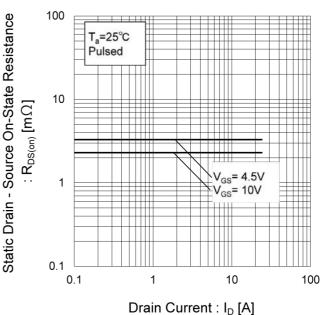


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

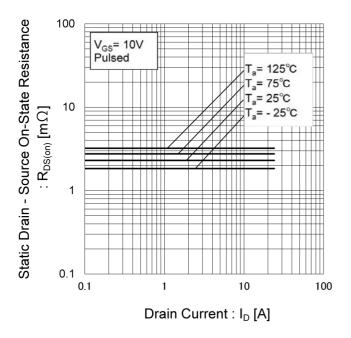


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

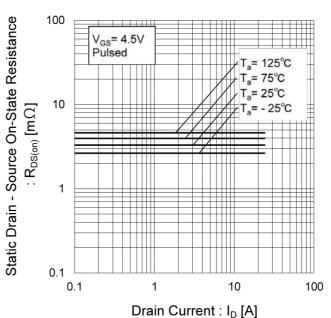


Fig.17 Typical Capacitance vs. Drain - Source Voltage

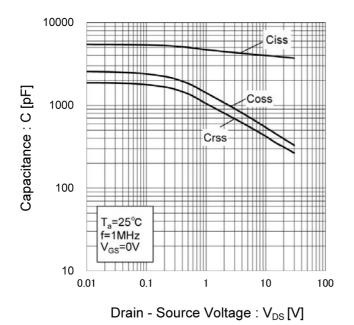


Fig.18 Switching Characteristics

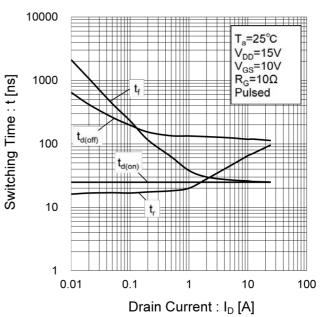


Fig.19 Dynamic Input Characteristics

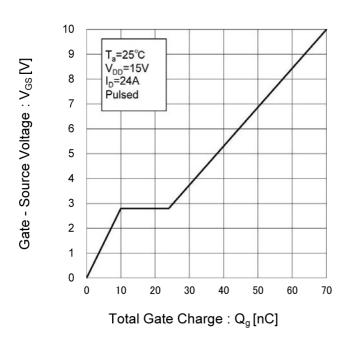
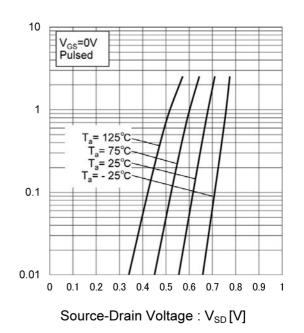


Fig.20 Source Current vs. Source Drain Voltage



Source Current : Is [A]

### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

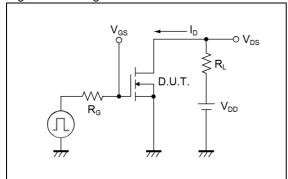


Fig.1-2 Switching Waveforms

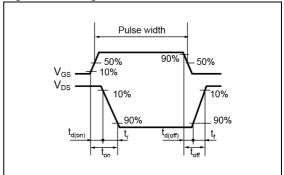


Fig.2-1 Gate Charge Measurement Circuit

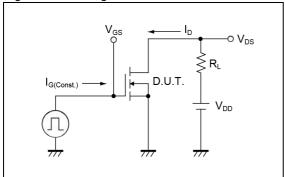


Fig.2-2 Gate Charge Waveform

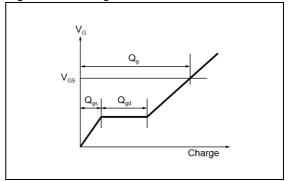


Fig.3-1 Avalanche Measurement Circuit

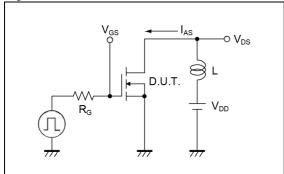
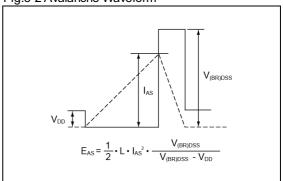


Fig.3-2 Avalanche Waveform

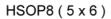


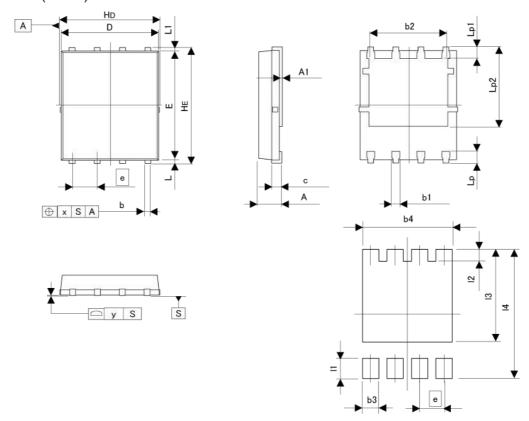
#### Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



# Dimensions





Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.24	0.42	0.009	0.017
b1	0.29	0.49	0.011	0.019
b2	3.81	4.21	0.150	0.166
С	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.60	5.80	0.220	0.228
е	1.	27	0.0	)50
HD	4.90	5.10	0.193	0.201
HE	5.90	6.10	0.232	0.240
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.50	0.70	0.020	0.028
Lp1	0.52	0.72	0.020	0.028
Lp2	3.92	4.32	0.154	0.170
х	- 0	0.10		0.004
У	-	0.10	-	0.004

DIM	MILIMETERS		MILIMETERS		INC	HES
DIW	MIN	MAX	MIN	MAX		
b3	15	0.59	P(#)	0.023		
b4	¥	4.21	(4)	0,166		
11	2	0.80	(12)	0.031		
12	-	0.82	-	0.032		
13	*	4.32	0+1	0.170		
14	2	6.10	7.0	0.240		

Dimension in mm/inches



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  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### **Precautions Regarding Application Examples and External Circuits**

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
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- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
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Rev.001

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