

RS1G150MN

Nch 40V 43A Power MOSFET

Datasheet

V _{DSS}	40V
R _{DS(on)} (Max.)	10.6mΩ
I _D	±43A
P _D	25W

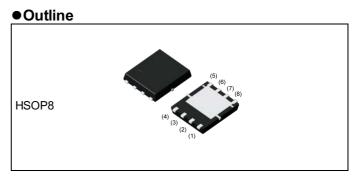
Features

- 1) Low on resistance
- 2) High power small mold package (HSOP8)
- 3) Pb-free lead plating ; RoHS compliant
- 4) Halogen free

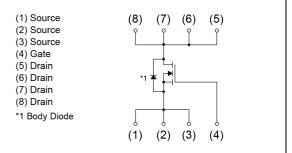
Application

Switching

5) 100% Rg and UIS tested



Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Basic ordering unit (pcs)	2500
	Taping code	ТВ
	Marking	RS1G150MN

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

	5			
Parameter	Symbol	Value	Unit	
Drain - Source voltage		V _{DSS}	40	V
Continuous drein surrent	$T_c = 25^{\circ}C$	۱ _D *1	±43	А
Continuous drain current	T _a = 25°C	Ι _D	±15	А
Pulsed drain current	۱ _{DP} *2	±60	А	
Gate - Source voltage	V _{GSS}	±20	V	
Avalanche current, single pulse	I _{AS} *3	15	А	
Avalanche energy, single pulse	E_{AS}^{*3}	17	mJ	
Dowor dissinction	P _D ^{*1}	25	W	
Power dissipation		P _D ^{*4}	3.0	W
Junction temperature		Tj	150	°C
Operating junction and storage te	T _{stg}	-55 to +150	°C	

Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}^{*1}	-	-	5.0	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*4}	-	-	41.7	°C/W

•Electrical characteristics (T_a = 25°C)

Deverseter	Currente e l	Candiliana	Values			1.1:4
Parameter	Symbol	ymbol Conditions		Тур.	Max.	Unit
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	40	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$			27.3	-	mV/°C
Zero gate voltage drain current	I _{DSS}	I_{DSS} $V_{DS} = 40V, V_{GS} = 0V$		-	1	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$		-	±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} = 10V, I _D = 1mA	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_{j}} I_{D} = 1 \text{mA}$ referenced to 25°C		-	-4.6	-	mV/°C
Static drain - source	D *5	V _{GS} = 10V, I _D = 15A	-	7.6	10.6	
on - state resistance	${\sf R}_{\sf DS(on)}^{*5}$	V _{GS} = 4.5V, I _D = 15A	-	10.2	13.3	mΩ
Gate resistance	R _G	R _G f=1MHz, open drain		2.4	-	Ω
Forward Transfer Admittance	Y _{fs} * ⁵	^{*5} V _{DS} = 10V, I _D = 15A		-	-	S

*1 T_c=25°C, Limited only by maximum temperature allowed.

*2 Pw \leq 10µs , Duty cycle \leq 1%

*3 L \simeq 0.1mH, V_{DD} = 20V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2

- *4 Mounted on a Cu board (40×40×0.8mm)
- *5 Pulsed



• Electrical characteristics ($T_a = 25^{\circ}C$)

Parameter	Symbol	Conditions	Values			Unit	
	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C _{iss}	V _{GS} = 0V	-	930	-		
Output capacitance	C _{oss}	V _{DS} = 20V	-	205	-	pF	
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	48	-		
Turn - on delay time	$t_{d(on)}^{*5}$	$V_{DD} \simeq 20V, V_{GS} = 10V$	-	12.7	-		
Rise time	t _r *5	I _D = 7.5A	-	5.7	-		
Turn - off delay time	t _{d(off)} *5	$R_L \simeq 2.7\Omega$	-	31.0	-	ns	
Fall time	t _f *5	R _G = 10Ω	-	4.8	-		

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Sumbol	Conditions		Values			l leit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gata charge	○ *5		V _{GS} = 10V	-	15.0	-	
Total gate charge	Q_g^{*5}	$V_{DD} \simeq 20V$		-	7.2	-	-0
Gate - Source charge	Q_{gs}^{*5}	I _D = 15A	V _{GS} = 4.5V	-	3.3	-	nC
Gate - Drain charge	Q_{gd}^{*5}			-	2.0	-	

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Deremeter	Sumbol	Conditions	Values			l lucit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Continuous forward current	I _S	$T = 25^{\circ}$	-	-	2.5	А
Pulse forward current	I _{SP} ^{*2}	T _a = 25°C	-	-	60	А
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 2.5A	-	-	1.2	V



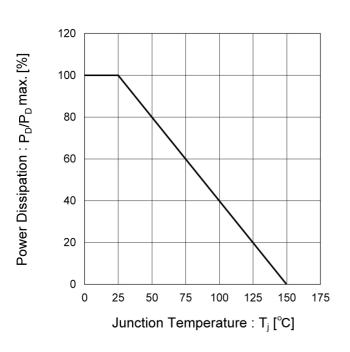


Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

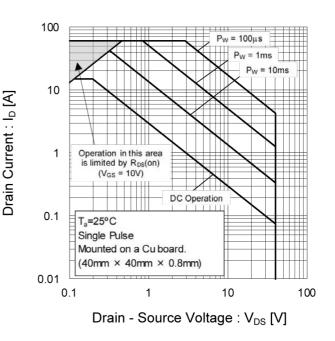
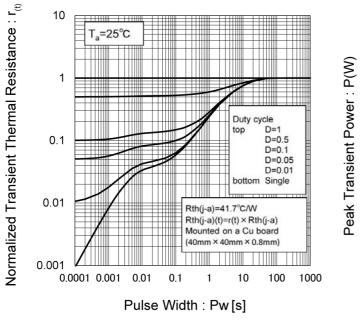
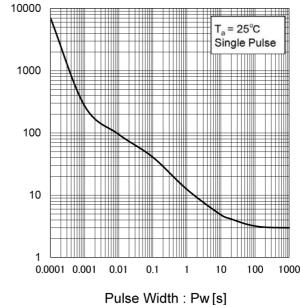


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Fig.4 Single Pulse Maximum Power dissipation







• Electrical characteristic curves

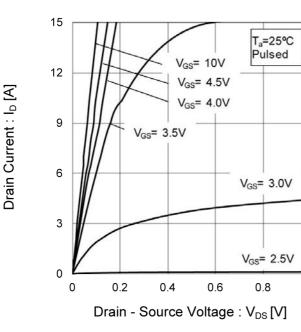
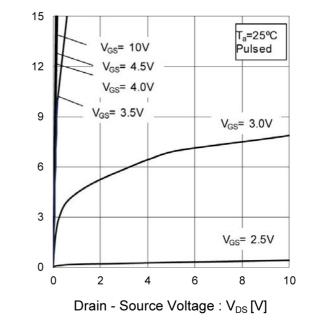


Fig.5 Typical Output Characteristics(I)



Drain Current : I_D [A]

1

Fig.6 Typical Output Characteristics(II)

Fig.7 Breakdown Voltage vs. Junction Temperature



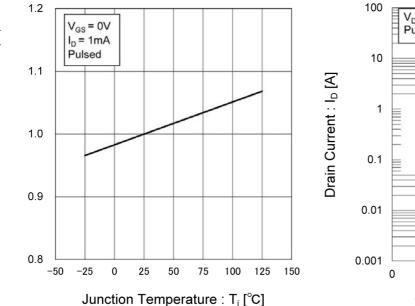
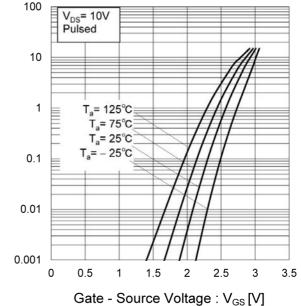


Fig.8 Typical Transfer Characteristics





• Electrical characteristic curves

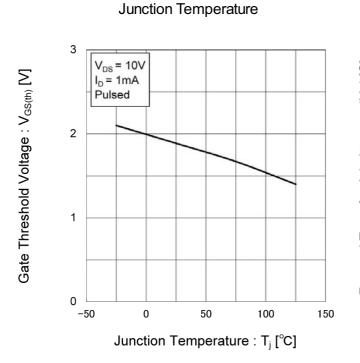


Fig.9 Gate Threshold Voltage vs.

Fig.10 Forward Transfer Admittance vs. Drain Current

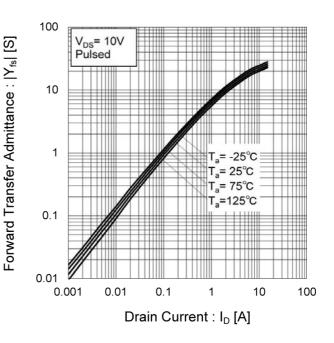
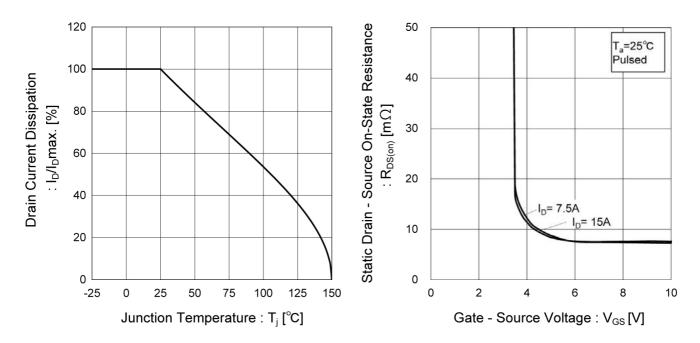


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage





•Electrical characteristic curves

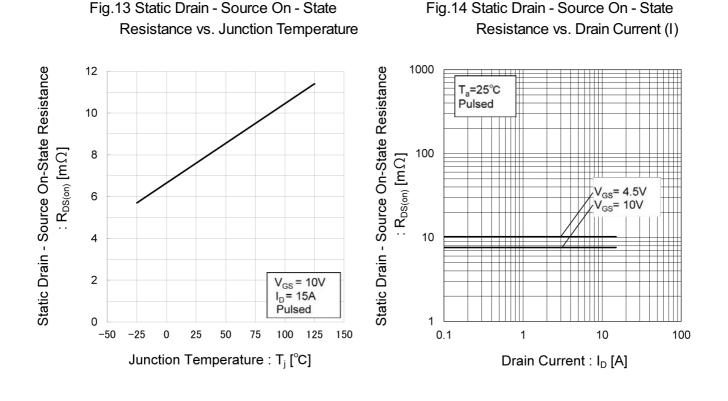
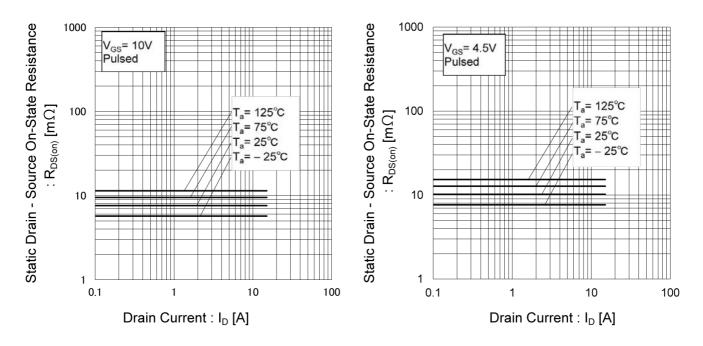


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)



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• Electrical characteristic curves

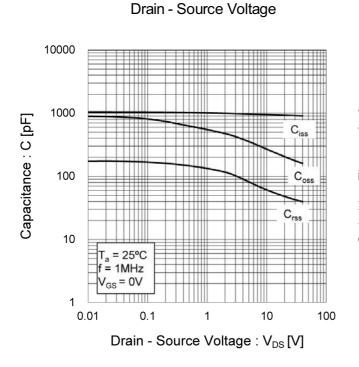


Fig.17 Typical Capacitance vs.

Fig.18 Switching Characteristics

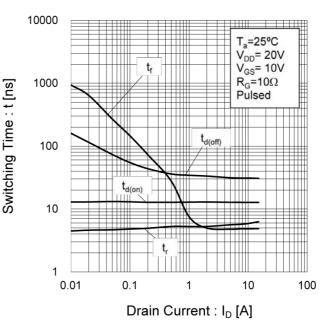


Fig.19 Dynamic Input Characteristics

Gate - Source Voltage : V_{GS} [V]

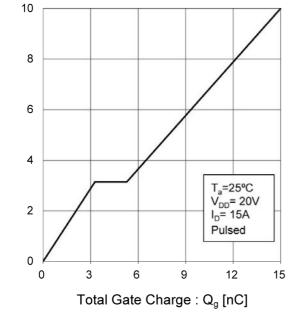
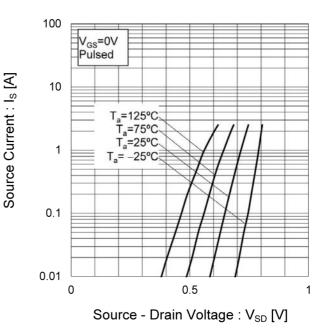


Fig.20 Source Current vs. Source Drain Voltage





Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

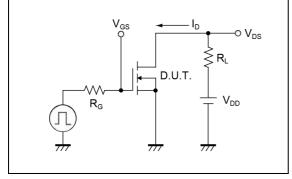


Fig.2-1 Gate Charge Measurement Circuit

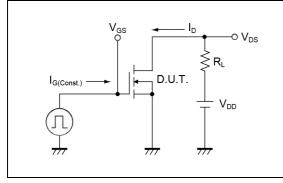


Fig.3-1 Avalanche Measurement Circuit

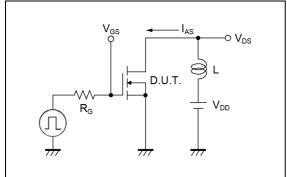


Fig.1-2 Switching Waveforms

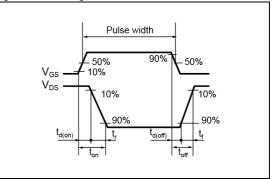


Fig.2-2 Gate Charge Waveform

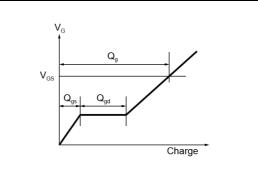
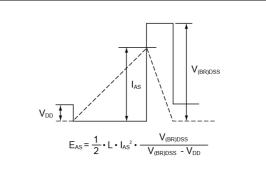


Fig.3-2 Avalanche Waveform



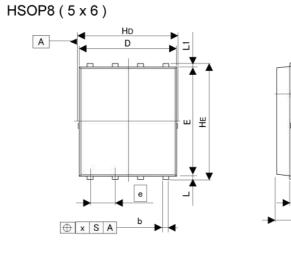
Notice

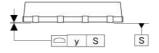
This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

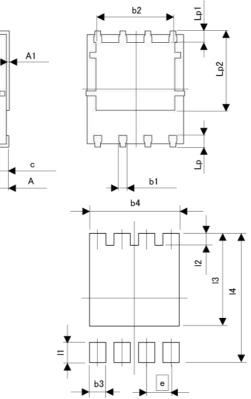




Dimensions







Pattern of terminal position areas [Not a pattern of soldering pads]

	MILIMETERS		INC	HES
	MIN	MAX	MIN	MAX
A	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.24	0.42	0.009	0.017
b1	0.29	0.49	0.011	0.019
b2	3.81	4.21	0.150	0.166
С	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.60	5.80	0.220	0.228
e	1.	27	0.0)50
HD	4.90	5.10	0.193	0.201
HE	5.90	6.10	0.232	0.240
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.50	0.70	0.020	0.028
Lp1	0.52	0.72	0.020	0.028
Lp2	3.92	4.32	0.154	0.170
x		0.10	-	0.004
у	-	0.10		0.004
	MILIME	TERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b3	17	0.59	25	0.023
b4	н (4.21	(m)	0.166
11		0.80	1 (2)	0.031
12	-	0.82		0.032
13	×.	4.32	0ec	0.170
14	2	6.10) in 1	0.240

Dimension in mm/inches



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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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