V_{DSS}	60V
R _{DS(on)} (Max.)	59mΩ
I _D	±4.5A
P _D	2.0W

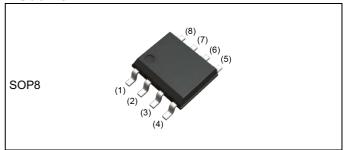
● Features

- 1) Low on resistance
- 2) Small surface mount package (SOP8)
- 3) Pb-free lead plating; RoHS compliant

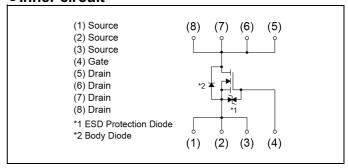
Application

Switching

Outline



•Inner circuit



Packaging specifications

	Packing	Embossed Tape					
	Reel size (mm)	330					
Туре	Tape width (mm)	12					
	Basic ordering unit (pcs)	2500					
	Taping code	ТВ					
	Marking	RS3L045GN					

● **Absolute maximum ratings** (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	60	V
Continuous drain current	I _D	±4.5	А
Pulsed drain current	I _{DP} *1	±18	А
Gate - Source voltage	V_{GSS}	±20	V
Avalanche current, single pulse	I _{AS} *2	4.5	А
Avalanche energy, single pulse	E _{AS} *2	7.7	mJ
Down discipation	P _D *3	2.0	W
Power dissipation	P _D *4	1.4	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Doromotor	Cymah al	Values			l limit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres innetion, ambient	R _{thJA} *3	-	-	62.5	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	89.2	°C/W

● Electrical characteristics (T_a = 25°C)

Davanastav	Cymaele ed	Conditions	Values			1.124	
Parameter	Parameter Symbol Conditions -		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	60	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	60	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V	-	-	10	μА	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±10	μA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 50\mu A$	1.3	-	2.7	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I _D = 1mA referenced to 25°C	-	-5.6	-	mV/°C	
		V _{GS} = 10V, I _D = 4.5A	-	43	59		
Static drain - source on - state resistance	R _{DS(on)} *5	$V_{GS} = 6.0V, I_D = 4.5A$	1	47	66	mΩ	
		$V_{GS} = 4.5V, I_D = 4.5A$	-	62	92		
Gate resistance R_G $f = f$		f = 1MHz, open drain	-	1.9	-	Ω	
Forward Transfer $ Y_{fs} ^{*5}$ $V_{DS} = 5V$,		V _{DS} = 5V, I _D = 4.5A	3.4	-	-	S	

^{*1} Pw \leq 10 μ s , Duty cycle \leq 1%

^{*2} L \simeq 0.5mH, V_{DD} = 30V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*3} Mounted on a ceramic board (30×30×0.8mm)

^{*4} Mounted on a Cu board (40×40×0.8mm)

^{*5} Pulsed

● Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Conditions		Unit			
Parameter			Min.	Тур.	Max.	UIIIL	
Input capacitance	C _{iss}	V _{GS} = 0V	1	285	1		
Output capacitance	C _{oss}	V _{DS} = 30V	ı	55	1	pF	
Reverse transfer capacitance	sfer capacitance C_{rss} $f = 1MHz$		ı	14	ı		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 30V, V_{GS} = 10V$	-	7.4	-		
Rise time	t _r *5	I _D = 2.25A	1	4.9	1	no	
Turn - off delay time	t _{d(off)} *5	R _L ~ 13.3Ω		17.4	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	3.7	-		

● Gate charge characteristics (T_a = 25°C)

Doromotor	Cumbal	Conditions		Values			l limit
Parameter	Symbol			Min.	Тур.	Max.	Unit
Total gate charge	Qg*5	V _{DD} ≈ 30V	V _{GS} = 10V	-	5.6	-	
				-	3.0	-	" C
Gate - Source charge	Q _{gs} *5	I _D = 4.5A	V _{GS} = 4.5V	-	1.2	-	nC
Gate - Drain charge	Q _{gd} *5			-	1.1	-	

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
raiametei	Symbol	Symbol Conditions		Тур.	Max.	Offic
Continuous forward current	I _S	T = 25°C	-	-	1.67	Α
Pulse forward current	I _{SP} *1	T _a = 25°C	-	-	18	Α
Forward voltage	V _{SD} *5	V _{GS} = 0V, I _S = 1.67A	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

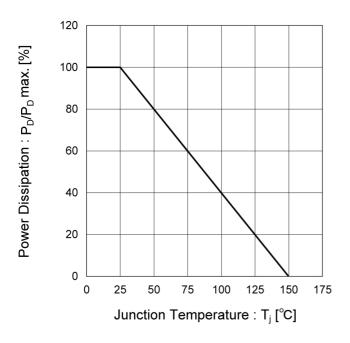
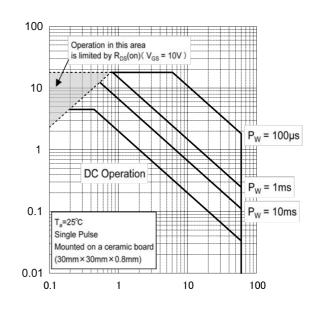


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : $V_{DS}[V]$

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

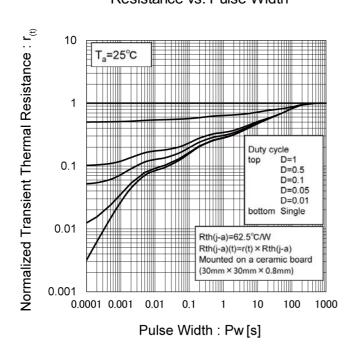


Fig.4 Single Pulse Maximum Power dissipation

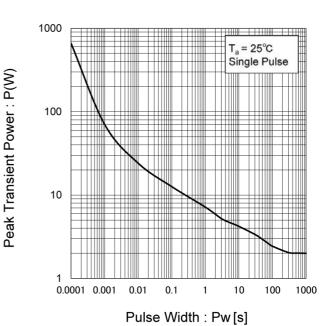


Fig.5 Typical Output Characteristics(I)

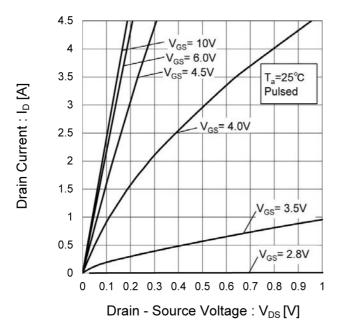
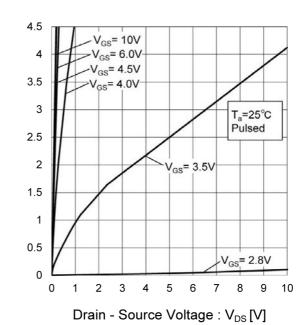


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.7 Breakdown Voltage vs.
Junction Temperature

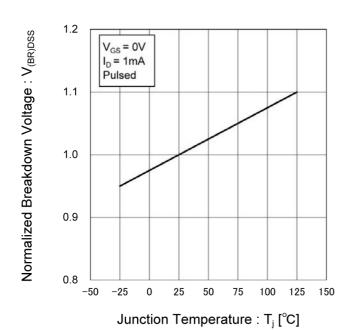


Fig.8 Typical Transfer Characteristics

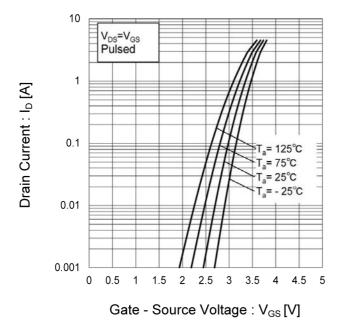
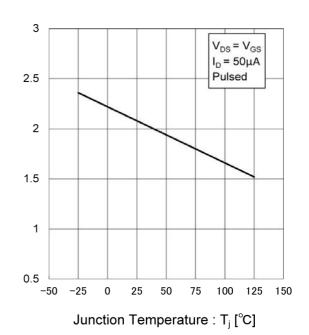


Fig.9 Gate Threshold Voltage vs.
Junction Temperature



Gate Threshold Voltage: VGS(th) [V]

Fig.10 Forward Transfer Admittance vs.
Drain Current

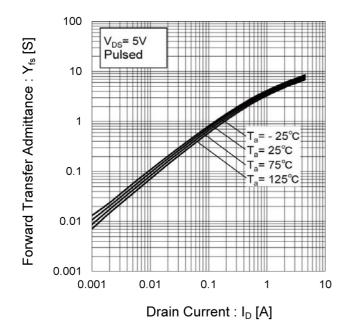


Fig.11 Drain Current Derating Curve

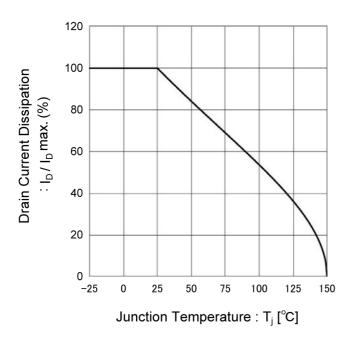


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

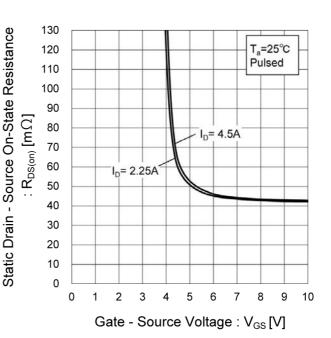


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature

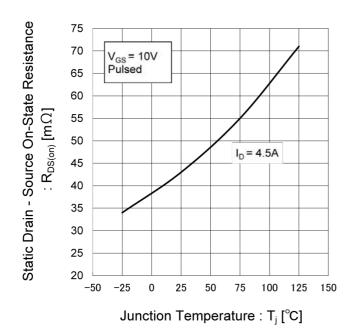


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

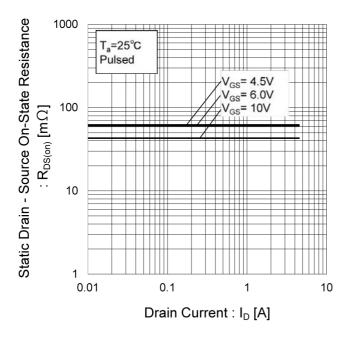


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

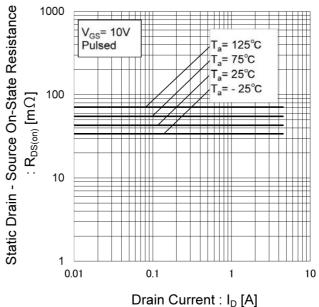


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

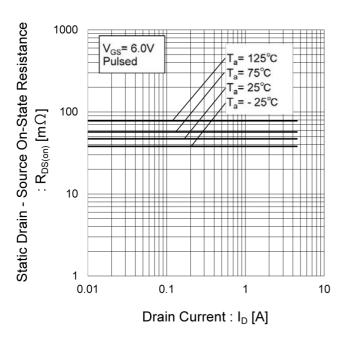


Fig.17 Static Drain - Source On - State Resistance vs. Drain Current (IV)

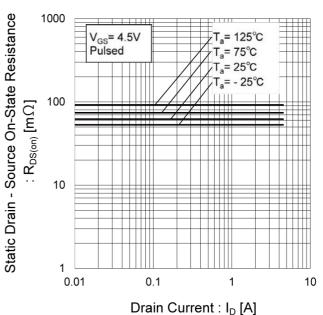


Fig.18 Typical Capacitance vs.

Drain - Source Voltage

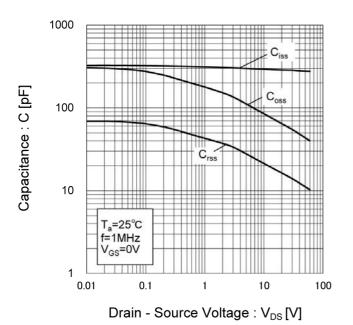


Fig.19 Switching Characteristics

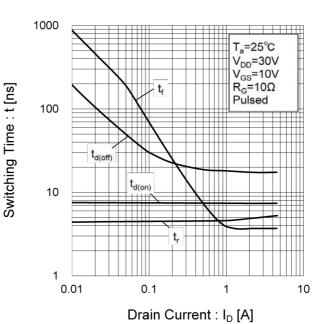


Fig.20 Dynamic Input Characteristics

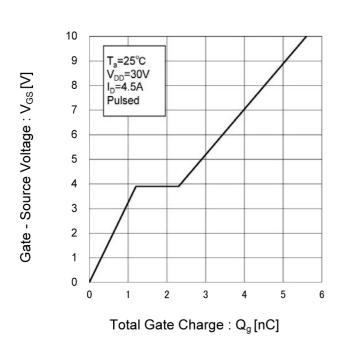
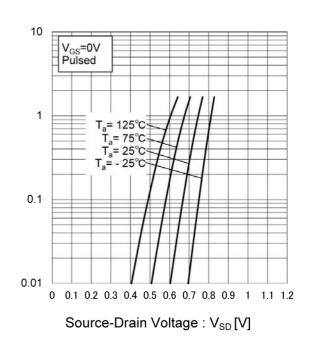


Fig.21 Source Current vs.

Source Drain Voltage



Source Current : Is [A]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

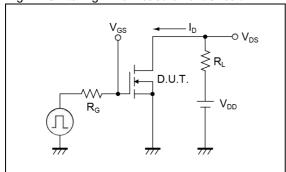


Fig.2-1 Gate Charge Measurement Circuit

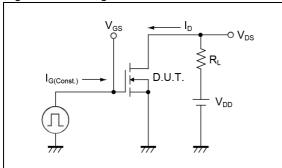


Fig.3-1 Avalanche Measurement Circuit

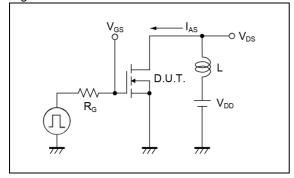


Fig.1-2 Switching Waveforms

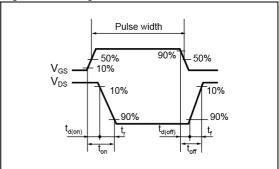


Fig.2-2 Gate Charge Waveform

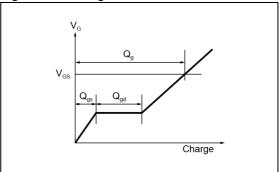
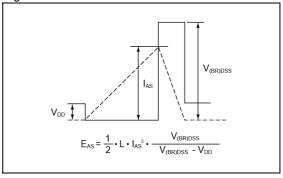
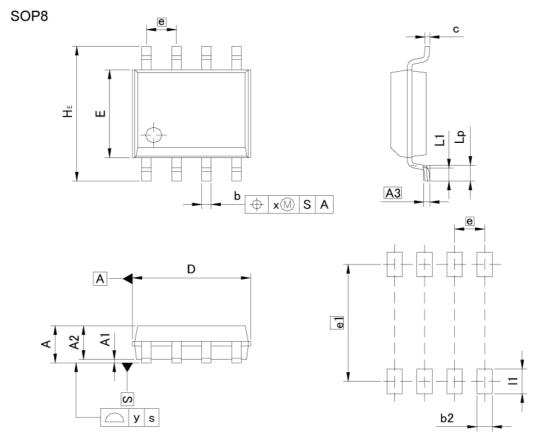


Fig.3-2 Avalanche Waveform



Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

D114	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
Α	₩.	1.75	=:	0.069	
A1	0.	15	0.0	06	
A2	1.40	1.60	0.055	0.063	
A3	0.	25	0.0	10	
b	0.30	0.50	0.012	0.020	
С	0.10	0.30	0.004	0.012	
D	4.80	5.20	0.189	0.205	
E	3.75	4.05	0.148	0.159	
е	1.	27	0.050		
HE	5.70	6.30	0.224	0.248	
L1	0.40	0.60	0.016	0.024	
Lp	0.65	0.85	0.026	0.033	
x	0.	15	0.0	06	
у	0.	0.10		0.004	
<u></u>	MILIM	ETERS	INC	HES	
DIM	MIN	MAX	MIN	MAX	
b2	-2	0.65	#2	0.026	

Dimension in mm/inches

e 1



0.045

0.203

1.15

5.15

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JAPAN	USA	EU	CHINA
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CLASSIV	CLASSIII	CLASSIII	CLASSIII

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PGA-E Rev.003

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