

V _{DSS}	80V
R _{DS(on)} (Max.)	3.3mΩ
I _D	±135A
P _D	104W

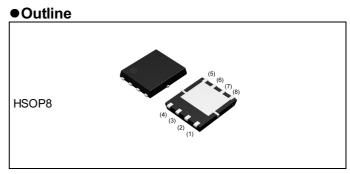
Features

- 1) Low on resistance
- 2) High power package (HSOP8)
- 3) Pb-free plating ; RoHS compliant
- 4) Halogen free

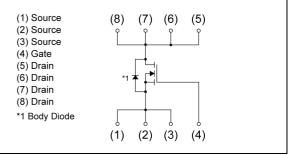
Application

Switching

5) 100% Rg and UIS tested



●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	TB1
	Marking	RS6N120BH

• Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Para	meter	Symbol	Value	Unit	
Drain - Source voltage		V _{DSS}	80	V	
Continuous dusis sumont	Silicon limit (V _{GS} =10V)	۱ _D *1	±135	А	
Continuous drain current	$T_c = 25^{\circ}C (V_{GS} = 10V)$	۱ _D *2	±120	А	
Pulsed drain current		I _{DP} *3	±540	А	
Gate - Source voltage		V _{GSS}	±20	V	
Avalanche current, single pulse		I_{AS}^{*4}	30	А	
Avalanche energy, single p	ulse	E_{AS}^{*4}	74	mJ	
		P _D *2	104	W	
Power dissipation		P _D ^{*5}	3.0	W	
Junction temperature		Tj	150	°C	
Operating junction and storage temperature range		T _{stg}	-55 to +150	°C	

Thermal resistance

Deremeter	Sumbol	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}^{*2}	-	-	1.2	°C/W
Thermal resistance, junction - ambient	R_{thJA}^{*5}	-	-	41.7	°C/W

•Electrical characteristics (T_a = 25°C)

Devenantes	Currente e l	Symbol		Values			
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		80	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}} I_{D} = 1 mA$ referenced to 25°C		58	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 80V, V _{GS} = 0V	-	-	5	μA	
Gate - Source leakage current	I_{GSS} $V_{GS} = \pm 20V, V_{DS} = 0V$		-	-	±500	nA	
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 1mA$	2.0	-	4.0	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	$\Delta V_{GS(th)}$ I _D = 1mA		-5.0	-	mV/°C	
Static drain - source	D *6	V _{GS} = 10V, I _D = 90A	-	2.8	3.3		
on - state resistance	${R_{DS(on)}}^{*6}$	V _{GS} = 6V, I _D = 60A	-	3.5	4.9	mΩ	
Gate resistance	R _G	-	-	1.3	-	Ω	
Forward Transfer Admittance	Y _{fs} * ⁶			-	-	S	

*1 Limited by silicon chip capability.

- *2 T_c=25°C, Limited only by maximum temperature allowed.
- *3 Pw $\leq 10 \mu s$, Duty cycle $\leq 1\%$
- *4 L \simeq 0.1mH, V_{DD} = 40V, R_G = 25 Ω , Starting T_j = 25°C Fig.3-1,3-2
- *5 Mounted on a Cu board (40×40×0.8mm)
- *6 Pulsed



•Electrical characteristics (T_a = 25°C)

Deremeter	Symbol Conditions		Values			Linit
Parameter			Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	3420	-	
Output capacitance	C _{oss}	V _{DS} = 40V	-	1020	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	35	-	
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 40V, V_{GS}$ = 10V	-	32	-	
Rise time	t _r *6	I _D = 50A	-	47	-	
Turn - off delay time	t _{d(off)} *6	$R_L \simeq 0.8\Omega$	-	73	-	ns
Fall time	t _f *6	R _G = 10Ω	-	35	-	

• Gate charge characteristics ($T_a = 25^{\circ}C$)

Deremeter	Sumbol	Conditions		Values			Unit	
Parameter	Symbol Conditions		UNS	Min.	Тур.	Max.	Onit	
Total gate charge	Q_{g}^{*6}	V _{DD} ≃ 40V	V _{GS} = 10V	-	53.0	-		
				-	33.0	-	nC	
Gate - Source charge	Q_{gs}^{*6}	I _D = 50A	V _{GS} = 6V	-	16.0	-	nc	
Gate - Drain charge	Q _{gd} *6			-	10.1	-		

•Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Doromotor	Symbol Conditions		Values			Unit
Parameter			Min.	Тур.	Max.	Unit
Continuous forward current	ا _S *2		-	-	85	А
Pulse forward current	ا _{SP} *3	-	-	-	540	А
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = 85A	-	-	1.2	V
Reverse recovery time	t _{rr} *6	I _S = 50A, V _{GS} =0V	-	68	-	ns
Reverse recovery charge	Q _{rr} *6	di/dt = 100A/µs	_	115	-	nC



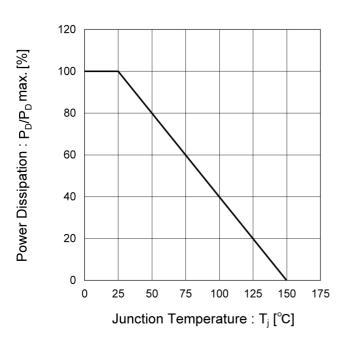
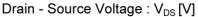


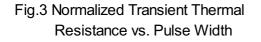
Fig.1 Power Dissipation Derating Curve

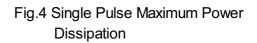
Operation in this area is limited by R_{DS}(on) (V_{GS} = 10V) 1000 100 10 P_w = 100µs 1 $P_W = 1ms$ T_a=25°C P_w = 10ms Single Pulse 0.1 100 0.1 10 1

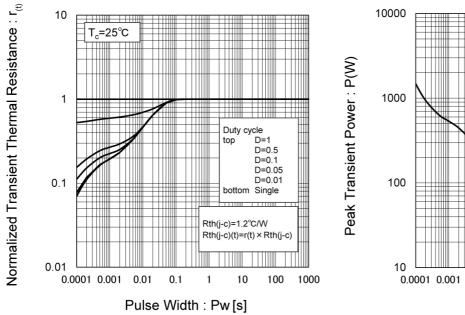
Drain Current : I_D [A]

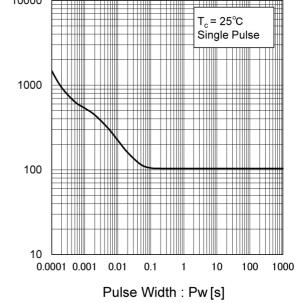
Fig.2 Maximum Safe Operating Area













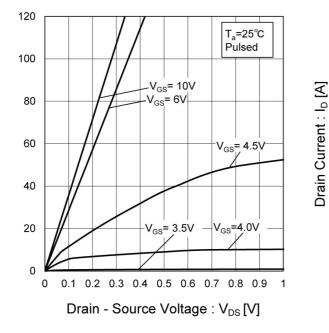
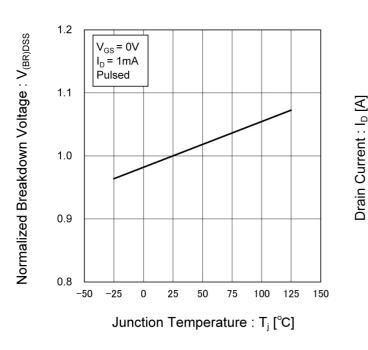


Fig.5 Typical Output Characteristics(I)

Fig.7 Normalized Breakdown Voltage vs. Junction Temperature





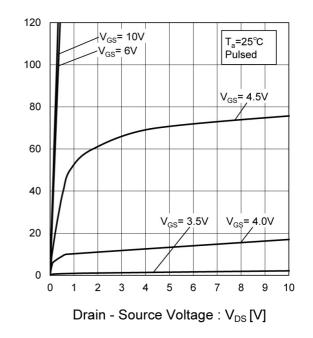
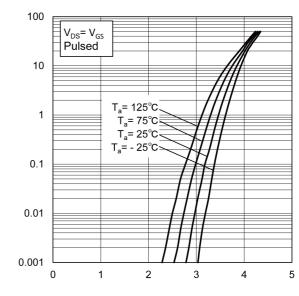


Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : V_{GS} [V]

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• Electrical characteristic curves

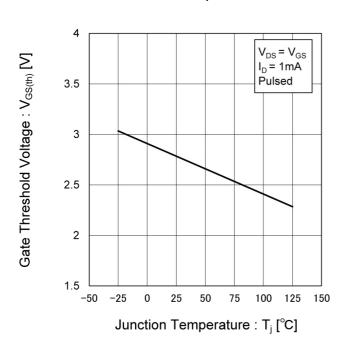


Fig.9 Gate Threshold Voltage vs.

Junction Temperature

Fig.10 Forward Transfer Admittance vs. Drain Current

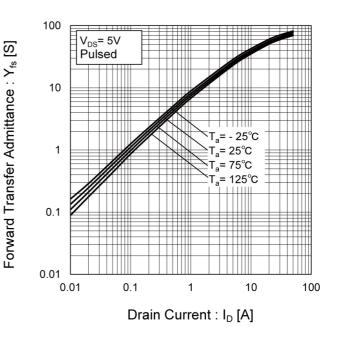
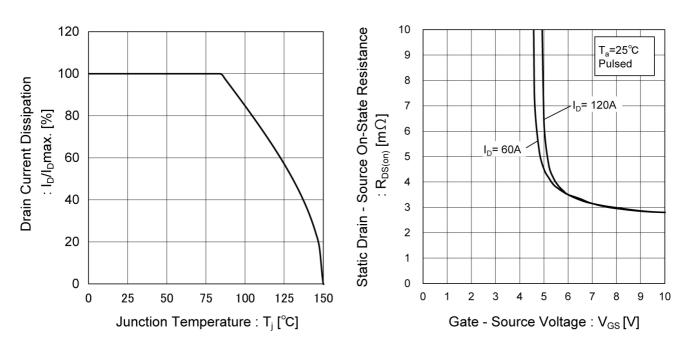


Fig.11 Drain Current Derating Curve

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage





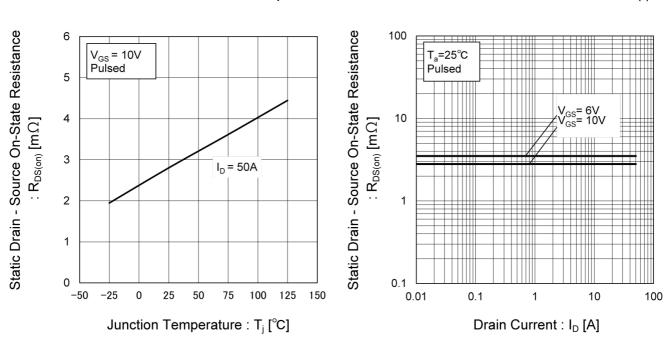


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature Fig.14 Static Drain - Source On - State Resistance vs. Drain Current (I)

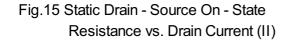
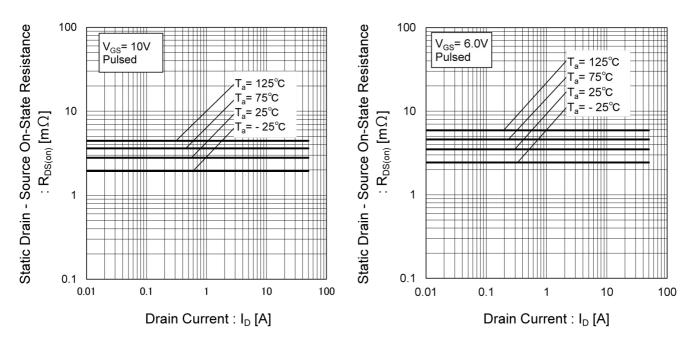


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)





• Electrical characteristic curves

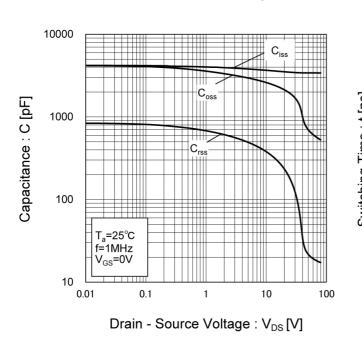


Fig.17 Typical Capacitances vs. Drain - Source Voltage

Fig.18 Switching Characteristics

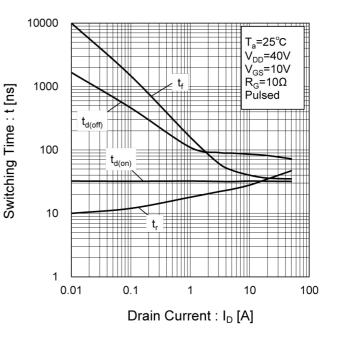


Fig.19 Typical Gate Charge

Gate - Source Voltage : V_{GS} [V]

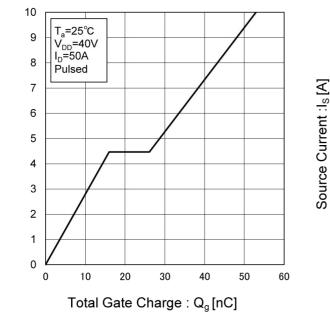
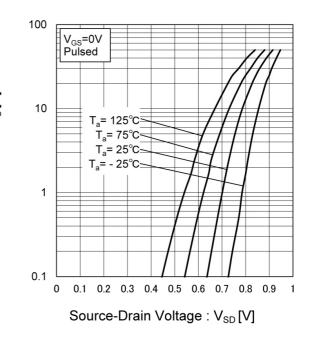


Fig.20 Source Current vs. Source Drain Voltage



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Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

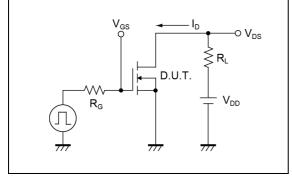


Fig.2-1 Gate Charge Measurement Circuit

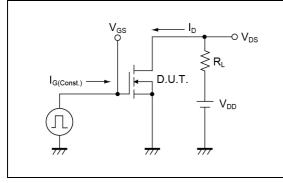


Fig.3-1 Avalanche Measurement Circuit

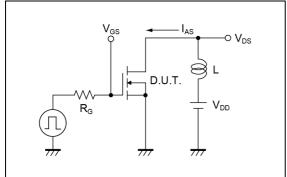


Fig.1-2 Switching Waveforms

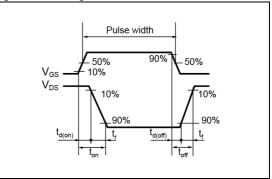


Fig.2-2 Gate Charge Waveform

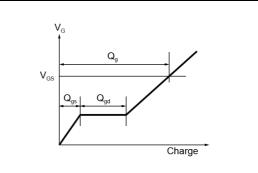
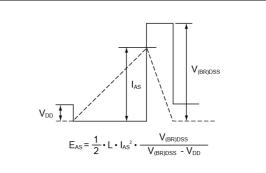


Fig.3-2 Avalanche Waveform



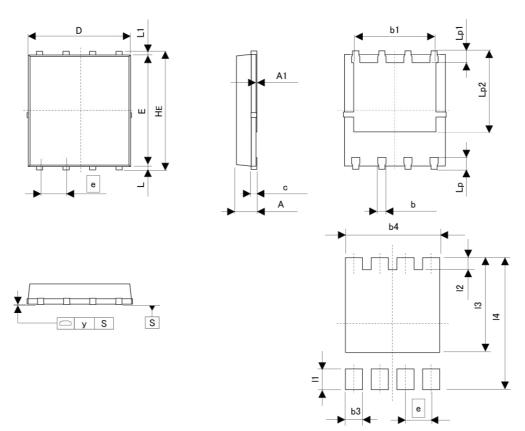
Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



Dimensions

HSOP8 (TB1)



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	MILIMETERS INCH		HES	
DIVI	MIN	MAX	MIN	MAX
A	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.33	0.42	0.013	0.017
b1	3.61	3.96	0.142	0.156
с	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.70	5.80	0.224	0.228
е	1.:	27	0.0)50
HE	5.90	6.10	0.232	0.240
L	0.06	0.20	0.002	0.008
L1	0.06	0.20	0.002	0.008
Lp	0.51	0.71	0.020	0.028
Lp1	0.41	0.61	0.016	0.024
Lp2	3.79	4.39	0.149	0.173
DIM	MILIME	MILIMETERS		HES
Divi	MIN	MAX	MIN	MAX
b3	-	0.68	-	0.027
b4	-	4.06	-	0.160
1	-	0.81	-	0.032
12	-	0.71	-	0.028
13	-	4.49	-	0.177
4	-	6.20	-	0.244

Dimension in mm/inches



Notice

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1. Our Products are designed and manufactured for application in ordinary electronic equipment (such as AV equipment, OA equipment, telecommunication equipment, home electronic appliances, amusement equipment, etc.). If you intend to use our Products in devices requiring extremely high reliability (such as medical equipment (^{Note 1)}, transport equipment, traffic equipment, aircraft/spacecraft, nuclear power controllers, fuel controllers, car equipment including car accessories, safety devices, etc.) and whose malfunction or failure may cause loss of human life, bodily injury or serious damage to property ("Specific Applications"), please consult with the ROHM sales representative in advance. Unless otherwise agreed in writing by ROHM in advance, ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of any ROHM's Products for Specific Applications.

(Note1) Medical Equipment Classification of the Specific Applications

JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSI	CLASS II b	CLASSII
CLASSⅣ	CLASSII	CLASSⅢ	CLASSI

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
- 2. You agree that application notes, reference designs, and associated data and information contained in this document are presented only as guidance for Products use. Therefore, in case you use such information, you are solely responsible for it and you must exercise your own independent verification and judgment in the use of such information contained in this document. ROHM shall not be in any way responsible or liable for any damages, expenses or losses incurred by you or third parties arising from the use of such information.

Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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