

RS6P100BH

Nch 100V 100A Power MOSFET

V _{DSS}	100V
R _{DS(on)} (Max.)	5.9mΩ
I _D	±100A
P_D	104W

- 1) Low on resistance
- 2) High power package (HSOP8)
- 3) Pb-free plating; RoHS compliant
- 4) Halogen free

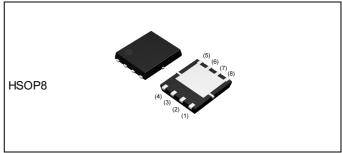
Features

5) 100% Rg and UIS tested

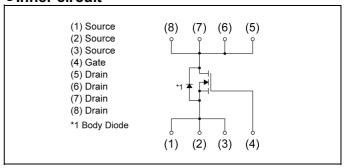
Application

Switching

Outline



•Inner circuit



Packaging specifications

	, <u>, , , , , , , , , , , , , , , , , , </u>	t
	Packing	Embossed Tape
	Reel size (mm)	330
Type	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	TB1
	Marking	RS6P100BH

ullet Absolute maximum ratings (T_a = 25°C ,unless otherwise specified)

Parameter	Symbol	Value	Unit	
Drain - Source voltage		V _{DSS}	100	V
Continuous drain current	V _{GS} = 10V	I _D *1	±100	А
Pulsed drain current		I _{DP} *2	±400	А
Gate - Source voltage		V_{GSS}	±20	V
Avalanche current, single pulse		I _{AS} *3	50	А
Avalanche energy, single pulse		E _{AS} *4	73	mJ
Davier die einstiere		P _D *1	104	W
Power dissipation		P _D *5	3.0	W
Junction temperature	T _j	150	°C	
Operating junction and storage temper	ature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Doromotor	Curah al	Values			I India
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC} *1	-	-	1.2	°C/W
Thermal resistance, junction - ambient	R _{thJA} *5	-	-	41.7	°C/W

● Electrical characteristics (T_a = 25°C)

Davanastan	Cymaele ed	Conditions	Values			l limit	
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit	
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	100	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	62.3	-	mV/°C	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	5	μA	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	1	1	±500	nA	
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 1mA$	2	-	4	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I _D = 1mA referenced to 25°C	-	-4.5	-	mV/°C	
Static drain - source	D *6	V _{GS} = 10V, I _D = 90A	-	4.5	5.9	O	
on - state resistance	R _{DS(on)} *6	V _{GS} = 6V, I _D = 50A	-	5.8	8.7	mΩ	
Gate resistance	R_{G}	-	-	1.4	-	Ω	
Forward Transfer Admittance	Y _{fs} *6	V _{DS} = 5V, I _D = 50A	35	-	-	S	

^{*1} T_c =25°C, Limited only by maximum temperature allowed.

^{*2} Pw≤ 10µs , Duty cycle≤ 1%

^{*3} L \simeq 0.01mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*4} L \simeq 0.1mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_j = 25 $^{\circ}$ C Fig.3-1,3-2

^{*5} Mounted on a Cu board (40×40×0.8mm)

^{*6} Pulsed

● Electrical characteristics (T_a = 25°C)

Darameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V	-	2880	-	
Output capacitance	C _{oss}	V _{DS} = 50V	-	550	-	pF
Reverse transfer capacitance	C _{rss}	f = 1MHz	-	23	-	
Turn - on delay time	t _{d(on)} *6	$V_{DD} \simeq 50V, V_{GS} = 10V$	-	30	-	
Rise time	t _r *6	I _D = 50A	-	35	-	
Turn - off delay time	t _{d(off)} *6	$R_L \simeq 1\Omega$	-	69	-	ns
Fall time	t _f *6	$R_G = 10\Omega$	-	30	-	

• Gate charge characteristics $(T_a = 25^{\circ}C)$

Darameter	Cymahal	Conditions		Values			l limit
Parameter	Symbol Cond		Oris	Min.	Тур.	Max.	Unit
Total gate charge	O *6		V _{GS} = 10V	-	45.0	-	
Total gate charge	Q _g *6	V _{DD} ≈ 50V		-	29.0	-	~ C
Gate - Source charge	Q _{gs} *6	I _D = 50A	V _{GS} = 6V	-	10.1	-	nC
Gate - Drain charge	Q _{gd} *6			-	11.7	-	

●Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Doromotor	Symbol	Conditions	Values			Unit
- Farameter	Parameter Symbol Conditions		Min.	Тур.	Max.	Offic
Continuous forward current	I _S	T _a = 25°C	1	-	85	Α
Pulse forward current	I _{SP} *2	1 _a - 25 C	-	-	400	Α
Forward voltage	V _{SD} *6	V _{GS} = 0V, I _S = 85A	-	-	1.2	V
Reverse recovery time	t _{rr} *6	I _S = 50A, V _{GS} =0V	-	63	-	ns
Reverse recovery charge	Q _{rr} *6	di/dt = 100A/μs	-	118	-	nC

Fig.1 Power Dissipation Derating Curve

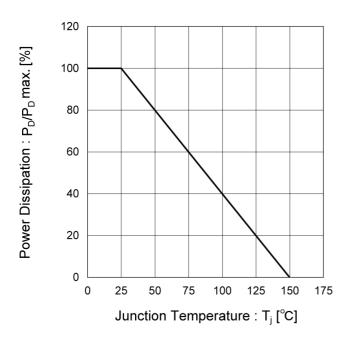
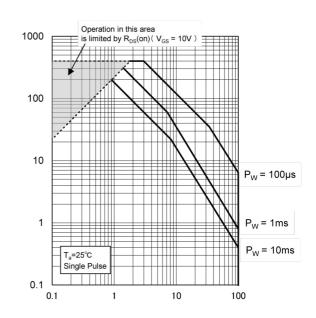


Fig.2 Maximum Safe Operating Area



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

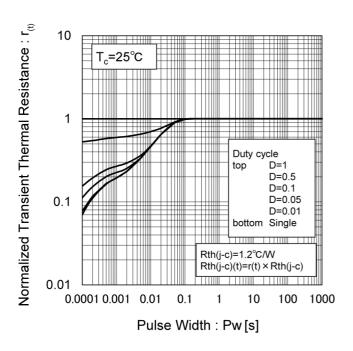


Fig.4 Single Pulse Maximum Power Dissipation

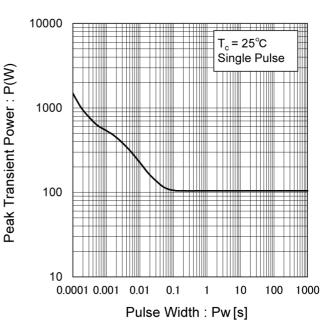


Fig.5 Typical Output Characteristics(I)

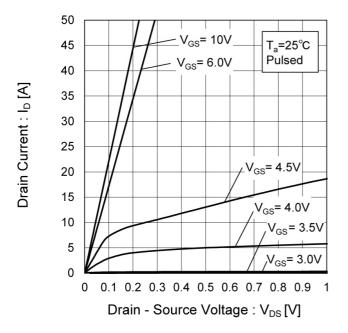
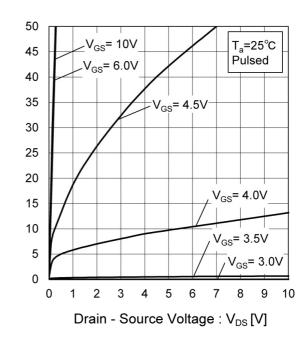


Fig.6 Typical Output Characteristics(II)



Drain Current : I_D [A]

Fig.7 Breakdown Voltage vs.

Junction Temperature

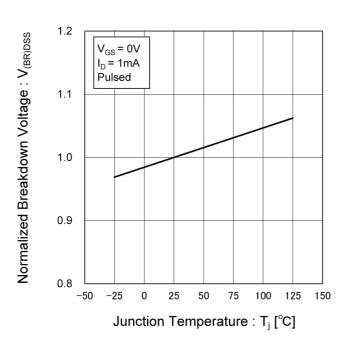


Fig.8 Typical Transfer Characteristics

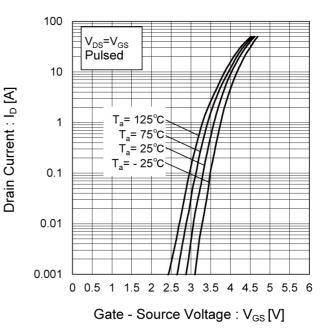


Fig.9 Gate Threshold Voltage vs.
Junction Temperature

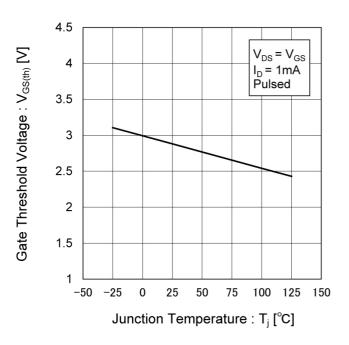


Fig.10 Forward Transfer Admittance vs.
Drain Current

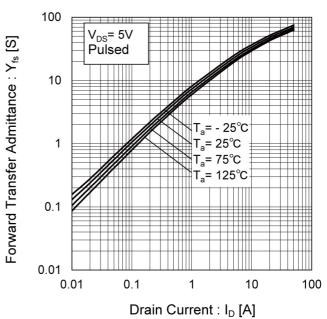


Fig.11 Drain Current Derating Curve

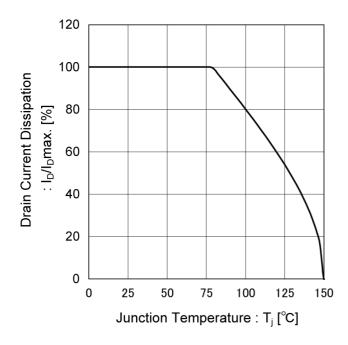


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

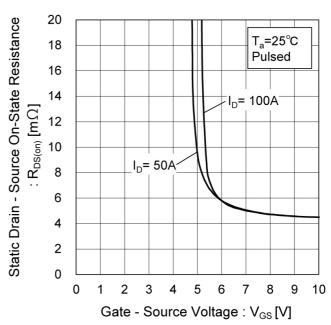


Fig.13 Static Drain - Source On - State
Resistance vs. Junction Temperature

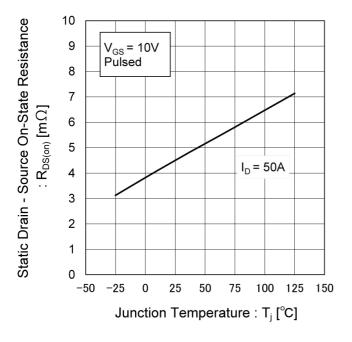


Fig.14 Static Drain - Source On - State
Resistance vs. Drain Current (I)

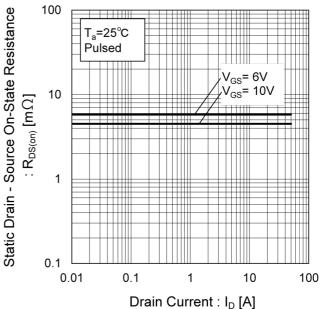


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current (II)

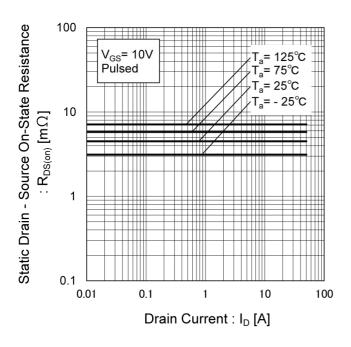


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current (III)

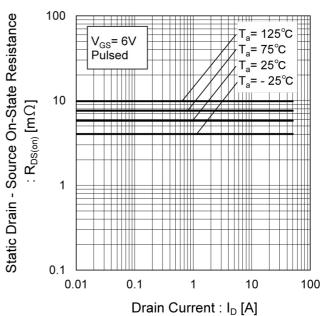


Fig.17 Typical Capacitances vs.

Drain - Source Voltage

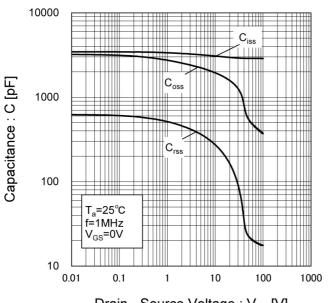
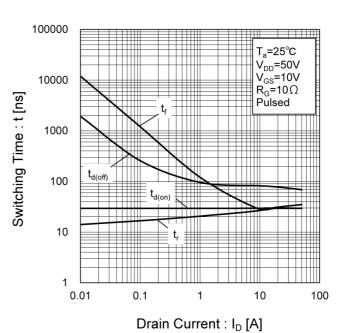


Fig.18 Switching Characteristics



Drain - Source Voltage : V_{DS} [V]

Fig.19 Typical Gate Charge

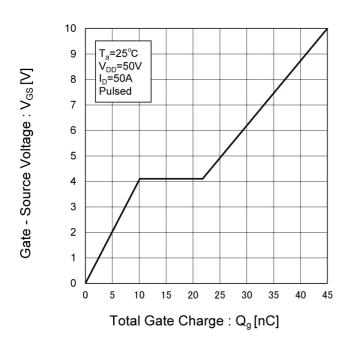
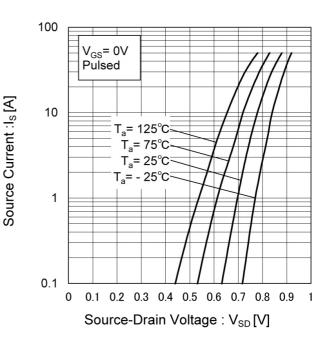


Fig.20 Source Current vs.

Source Drain Voltage



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

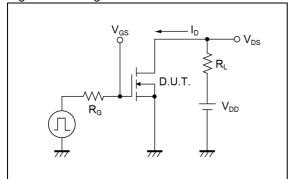


Fig.1-2 Switching Waveforms

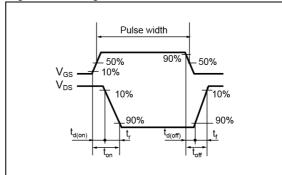


Fig.2-1 Gate Charge Measurement Circuit

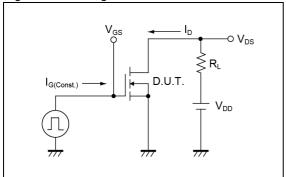


Fig.2-2 Gate Charge Waveform

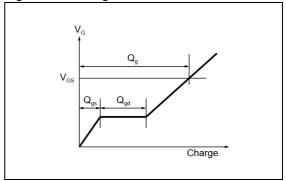


Fig.3-1 Avalanche Measurement Circuit

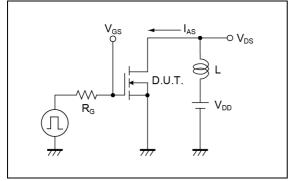
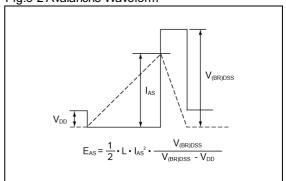


Fig.3-2 Avalanche Waveform

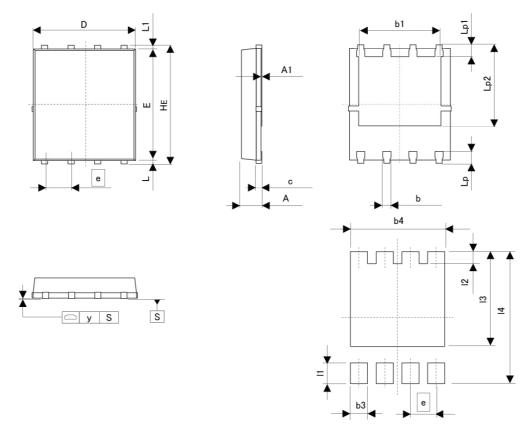


Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.

Dimensions

HSOP8 (TB1)



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIME	ETERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.33	0.42	0.013	0.017
b1	3.61	3.96	0.142	0.156
С	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.70	5.80	0.224	0.228
е	1.	27	0.0)50
HE	5.90	6.10	0.232	0.240
L	0.06	0.20	0.002	0.008
L1	0.06	0.20	0.002	0.008
Lp	0.51	0.71	0.020	0.028
Lp1	0.41	0.61	0.016	0.024
Lp2	3.79	4.39	0.149	0.173

DIM	DIM MILIME		MILIMETERS		INCHES	
DIIVI	MIN	MAX	MIN	MAX		
b3	-	0.68	-	0.027		
b4	-	4.06	-	0.160		
I1	-	0.81	-	0.032		
12	-	0.71	-	0.028		
13	-	4.49	-	0.177		
14	E-	6.20		0.244		

Dimension in mm/inches



Notice

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 DMN2990UFB-7B
IPB80P04P405ATMA2 2N7002W-G MCAC30N06Y-TP MCQ7328-TP NTMC083NP10M5L BXP7N65D BXP4N65F AOL1454G
WMJ80N60C4 BXP2N20L BXP2N65D BXT1150N10J BXT1700P06M TSM60NB380CP ROG RQ7L055BGTCR DMNH15H110SK3-13
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