

RSJ151P10

Pch 100V 15A Power MOSFET

V_{DSS}	-100V
R _{DS(on)} (Max.)	120m $Ω$
I _D	-15A
P_D	50W

● Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 4) Parallel use is easy.
- 5) Pb-free lead plating; RoHS compliant
- 6) 100% Avalanche tested

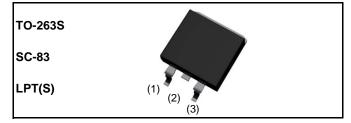
Application

Switching Power Supply

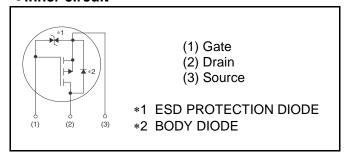
Automotive Motor Drive

Automotive Solenoid Drive

Outline



•Inner circuit



Packaging specifications

		
	Packaging	Taping
	Reel size (mm)	330
Typo	Tape width (mm)	16
Туре	Basic ordering unit (pcs)	2,500
	Taping code	TL
	Marking	RSJ151P10

• Absolute maximum ratings $(T_a = 25^{\circ}C)$

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	-100	V	
Continuous drain current	T _c = 25°C	I _D *1	±15	А
	T _c = 100°C	I _D *1	±8	А
Pulsed drain current	I _{D,pulse} *2	±30	А	
Gate - Source voltage	V_{GSS}	±20	V	
Avalanche energy, single pulse		E _{AS} *3	33	mJ
Avalanche current		I _{AR} *3	–15	А
$T_c = 25^{\circ}C$		P _D	50	W
Power dissipation $T_a = 25^{\circ}C^{*4}$		P _D	1.35	W
Junction temperature	T _j	150	°C	
Range of storage temperature	T _{stg}	-55 to +150	°C	

●Thermal resistance

Parameter	Symbol	Values			Unit
- Farameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	-	2.5	°C/W
Thermal resistance, junction - ambient *4	R_{thJA}	-	-	92.6	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

•Electrical characteristics($T_a = 25$ °C)

Parameter	Cymbol	Conditions	Values			Unit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = -1mA$	-100	ı	ı	V
		$V_{DS} = -100V, V_{GS} = 0V$	_	_	-1	
Zero gate voltage drain current	I _{DSS}	T _j = 25°C	_	_	_	μΑ
Zero gate voltage drain current	DSS	$V_{DS} = -100V, V_{GS} = 0V$	_	_	-100	
		T _j = 125°C	-			
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	1	1	±10	μΑ
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	ı	-2.5	V
		$V_{GS} = -10V, I_D = -15A$	•	85	120	
	R _{DS(on)} *5	$V_{GS} = -4.5V, I_D = -15A$	ı	95	135	
Static drain - source on - state resistance		$V_{GS} = -4.0V, I_D = -15A$	ı	100	140	$m\Omega$
		$V_{GS} = -10V, I_{D} = -15A$		155	220	
		T _j = 125°C	- 155		220	
Forward transfer admittance	g _{fs}	$V_{DS} = -10V, I_{D} = -15A$	13	26	-	S

●Electrical characteristics(T_a = 25°C)

Parameter	Symbol	Conditions		Unit			
raiametei	Syllibol	Symbol Conditions —		Тур.	Max.	Offic	
Input capacitance	C _{iss}	$V_{GS} = 0V$	-	3800	-		
Output capacitance	C _{oss}	$V_{DS} = -25V$	-	160	-	pF	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	100	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq -50V, V_{GS} = -10V$	-	30	-		
Rise time	t _r *5	$I_D = -7.5A$	-	40	-	no	
Turn - off delay time	t _{d(off)} *5	$R_L = 12\Omega$	-	165	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	95	-		

• Gate Charge characteristics ($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol Conditions –		Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*5}	$V_{DD} \simeq -50V$	-	64	-	
Gate - Source charge	Q _{gs} *5	$I_D = -15A$	-	10	-	nC
Gate - Drain charge	Q _{gd} *5	$V_{GS} = -10V$	-	10	-	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq -50V, I_D = -15A$	-	-3.1	-	V

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions -		Values			Unit	
Parameter			Min.	Тур.	Max.	Offic	
Continuous source current	I _S *1	T _c = 25°C	-	1	-15	Α	
Pulsed source current	I _{SM} *2	1 c = 25 C	-	-	-30	Α	
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = -15A$	-	-	-1.2	V	
Reverse recovery time	t _{rr} *5	I _S = −15A	-	60	-	ns	
Reverse recovery charge	Q _{rr} *5	di/dt = -100A/μs	-	145	-	μС	

^{*1} Limited only by maximum temperature allowed.

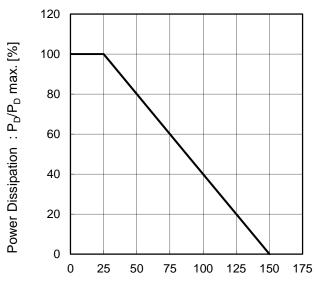
*5 Pulsed

^{*2} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*3} L $^{\simeq}$ 200 μ H, V_{DD} = -50V, Rg = 10Ω , starting T_{j} = $25^{\circ}C$

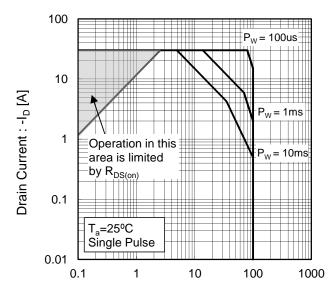
^{*4} Mounted on a epoxy PCB FR4 (20mm × 30mm × 0.8mm)

Fig.1 Power Dissipation Derating Curve



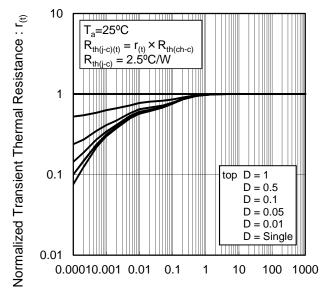
Junction Temperature : T_i [°C]

Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : -V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width : $P_W[s]$

Fig.4 Avalanche Current vs Inductive Load

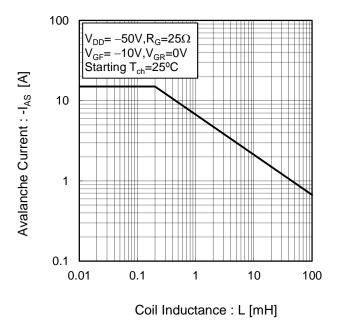
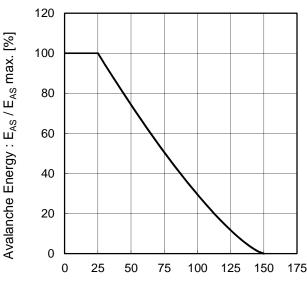
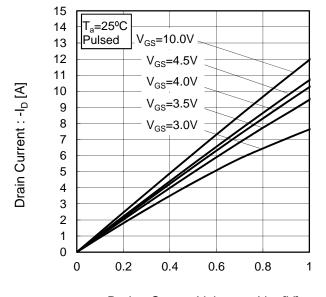


Fig.5 Avalanche Energy Derating Curve vs Junction Temperature



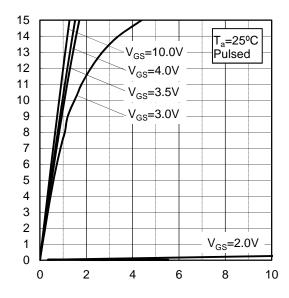
Junction Temperature : T_i [°C]

Fig.6 Typical Output Characteristics(I)



Drain - Source Voltage : $-V_{DS}[V]$

Fig.7 Typical Output Characteristics(II)



Drain - Source Voltage : -V_{DS} [V]

Drain Current: -I_D [A]

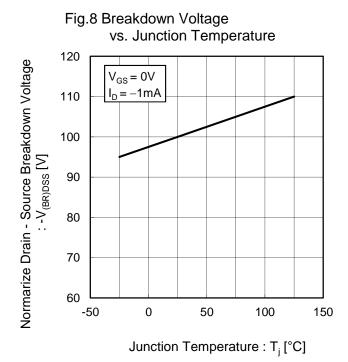
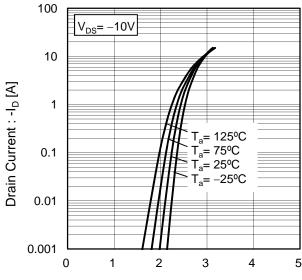


Fig.9 Typical Transfer Characteristics



Gate - Source Voltage : -V_{GS} [V]

Fig.10 Gate Threshold Voltage vs. Junction Temperature

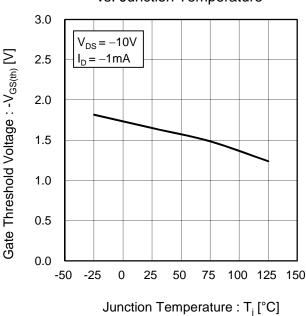
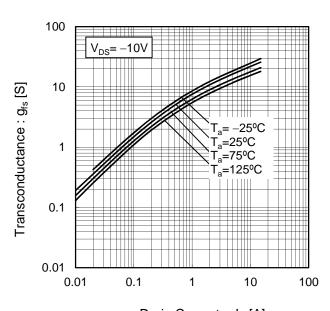


Fig.11 Transconductance vs. Drain Current



Drain Current : $-I_D$ [A]

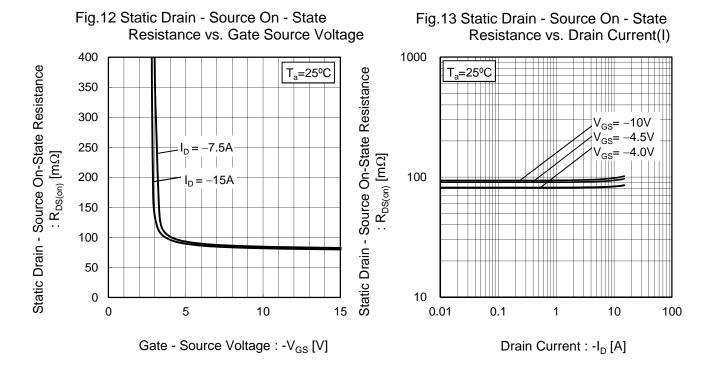
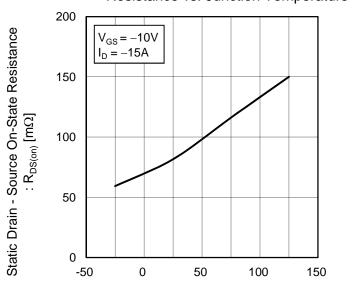


Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



Resistance vs. Drain Current(II)

1000 $V_{GS} = -10V$ $V_{GS} = -10V$

Fig.15 Static Drain - Source On - State

Fig.16 Static Drain - Source On - State
Resistance vs. Drain Current(III)

1000

V_{GS}= -4.5V

T_a=125°C

T_a=75°C

T_a=25°C

T_a=-25°C

T_a=-25°C

T_a=-25°C

T_a=-25°C

Drain Current: -ID [A]

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV) 1000 Static Drain - Source On-State Resistance =75°C =25°C –25°C $: R_{DS(on)} [m\Omega]$ 100 10 0.01 0.1 1 10 100 Drain Current : -I_D [A]

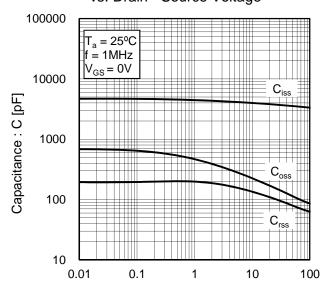
120 100 Drain Current Dissipation : I_D/I_D max. (%) 80 60 40 20 0 25 50 75 100 125 175 0 150

Junction Temperature : T_i [°C]

Fig.18 Drain Current Derating Curve

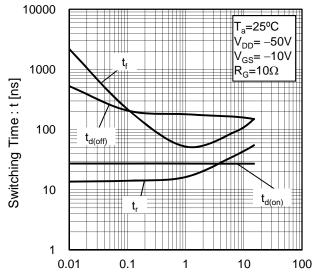


Fig.19 Typical Capacitance vs. Drain - Source Voltage



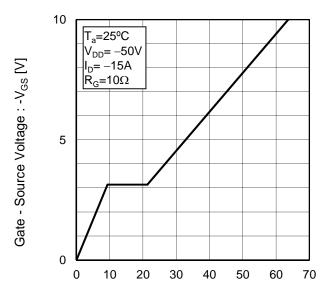
Drain - Source Voltage : - V_{DS} [V]

Fig.20 Switching Characteristics



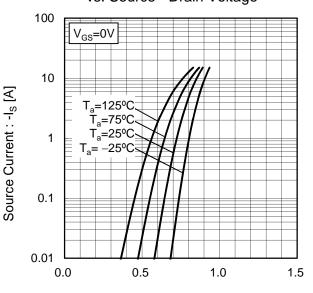
Drain Current : -I_D [A]

Fig.21 Dynamic Input Characteristics

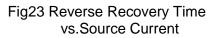


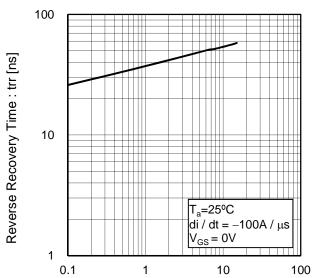
Total Gate Charge : Q_g [nC]

Fig.22 Source Current vs. Source - Drain Voltage



Source-Drain Voltage: -V_{SD} [V]





Source Current : -I_S [A]

●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

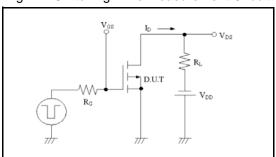


Fig.2-1 Gate Charge Measurement Circuit

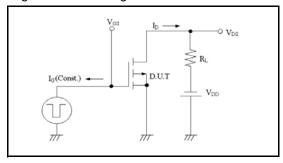


Fig.3-1 Avalanche Measurement Circuit

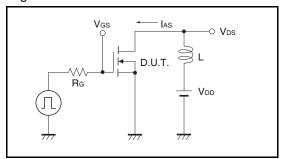


Fig.1-2 Switching Waveforms

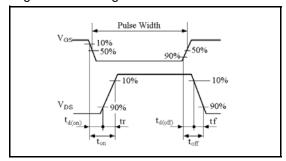


Fig.2-2 Gate Charge Waveform

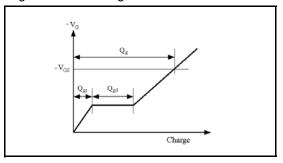
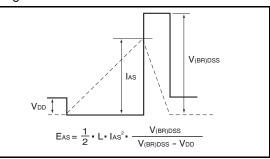
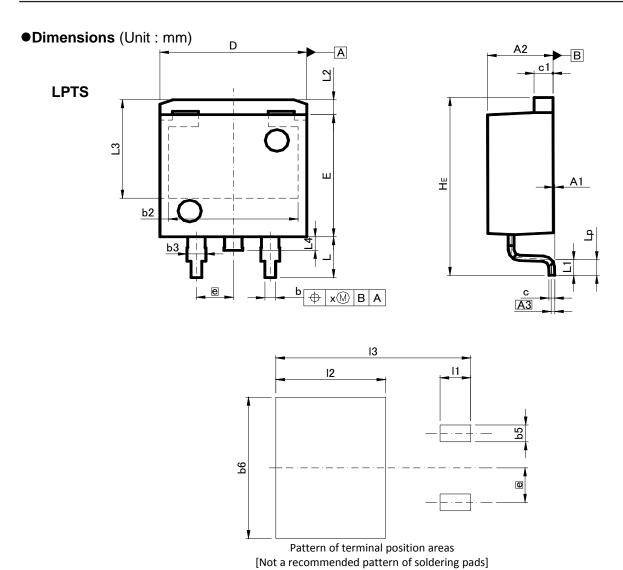


Fig.3-2 Avalanche Waveform





DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0.000	0.012
A2	4.30	4.70	0.169	0.185
A3	0.	25	0.0	110
b	0.68	0.98	0.027	0.039
b2	8.	90	0.3	50
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
E	8.80	9.20	0.346	0.362
е	2.	54	0.1	00
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.130
L1	0.90	1.50	0.035	0.059
L2	1.	10	0.0	43
L3	7.25		0.285	
L4	1.	00	0.0	39
Lp	0.90	1.50	0.035	0.059
Х	_	0.25	_	0.010

DIM	MILIMETERS IN		INC	HES
ואונט	MIN	MAX	MIN	MAX
bb	-	1.23	-	0.049
b6	_	10.40	-	0.409
11	-	2.10	-	0.083
12	-	7.55	-	0.297
13	_	13.40	_	0.528

Dimension in mm / inches

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 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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