

**AEC-Q101 Qualified** 

# 10V Drive Nch MOSFET

## RSJ400N06FRA

### Structure

Silicon N-channel MOSFET

### Features

- 1) Low on-resistance.
- 2) High current
- 3) High power Package

### Application

Switching

### Packaging specifications

	Package	Taping
Type	Code	TL
	Basic ordering unit (pieces)	1000
RSJ400N06F	0	

# ●Absolute maximum ratings (T<sub>a</sub> = 25°C)

Paramete	Symbol	Limits	Unit	
Drain-source voltage	$V_{DSS}$	60	V	
Gate-source voltage	$V_{GSS}$	±20	V	
Drain current	Continuous	$I_D$	±40	Α
Diam current	Pulsed	I <sub>DP</sub> *1	±80	Α
Source current	Continuous	I <sub>S</sub>	40	Α
(Body Diode)	Pulsed	I <sub>SP</sub> *1	80	Α
Power dissipation		P <sub>D</sub> *2	50	W
Channel temperature	Tch	150	°C	
Range of storage temporal	Tstg	-55 to +150	°C	

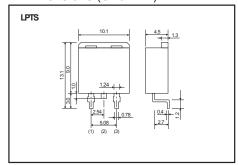
<sup>\*1</sup> Pw≤10µs, Duty cycle≤1%

### • Thermal resistance

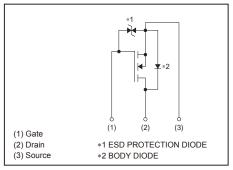
Parameter	Symbol	Limits	Unit
Channel to Case	R <sub>th (ch-c)</sub> *	2.5	°C / W

<sup>\*</sup> T<sub>c</sub>=25°C

### Dimensions (Unit : mm)



### • Inner circuit



<sup>\*2</sup> T<sub>c</sub>=25°C

## ●Electrical characteristics (T<sub>a</sub> = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	$I_{GSS}$	-	-	±10	μA	$V_{GS}=\pm20V$ , $V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	60	1	1	٧	$I_D=1mA$ , $V_{GS}=0V$
Zero gate voltage drain current	I <sub>DSS</sub>	-	1	1	μA	$V_{DS}$ =60V, $V_{GS}$ =0V
Gate threshold voltage	V <sub>GS (th)</sub>	1.0	1	3.0	٧	$V_{DS}$ =10V, $I_{D}$ =1mA
Static drain-source on-state resistance	R <sub>DS (on)</sub>	ı	11	16	mΩ	I <sub>D</sub> =40A, V <sub>GS</sub> =10V
Forward transfer admittance	I Y <sub>fs</sub> I*	14	-	-	S	I <sub>D</sub> =20A, V <sub>DS</sub> =10V
Input capacitance	C <sub>iss</sub>	-	2400	1	рF	V <sub>DS</sub> =10V
Output capacitance	C <sub>oss</sub>	1	490	-	рF	V <sub>GS</sub> =0V
Reverse transfer capacitance	$C_{rss}$	1	250	-	pF	f=1MHz
Turn-on delay time	t <sub>d(on)</sub> *	-	20	1	ns	I <sub>D</sub> =20A, V <sub>DD</sub> ≒ 30V
Rise time	t <sub>r</sub> *	-	60	1	ns	V <sub>GS</sub> =10V
Turn-off delay time	t <sub>d(off)</sub> *	1	90	-	ns	$R_L=1.5\Omega$
Fall time	t <sub>f</sub> *	1	140	-	ns	$R_G=10\Omega$
Total gate charge	Q <sub>g</sub> *	1	52	-	nC	V <sub>DD</sub> ≒ 30V
Gate-source charge	Q <sub>gs</sub> *	-	8	-	nC	I <sub>D</sub> =40A,
Gate-drain charge	Q <sub>gd</sub> *	-	15	-	nC	V <sub>GS</sub> =10V

<sup>\*</sup>Pulsed

# •Body diode characteristics (Source-Drain) ( $T_a = 25$ °C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward Voltage	V <sub>SD</sub> *	-	-	1.2	V	I <sub>s</sub> =40A, V <sub>GS</sub> =0V

<sup>\*</sup>Pulsed

### ●Electrical characteristic curves (Ta=25°C)

Fig.1 Static Drain-Source On-State Resistance vs. Drain Current

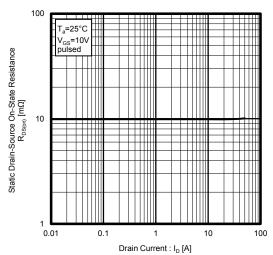


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

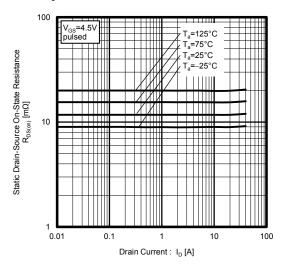


Fig.5 Forward Transfer Admittance vs. Drain Current

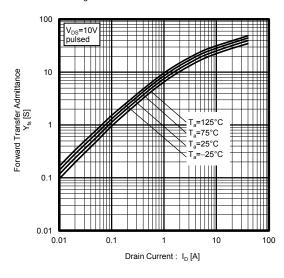


Fig.2 Static Drain-Source On-State Resistance vs. Drain Current

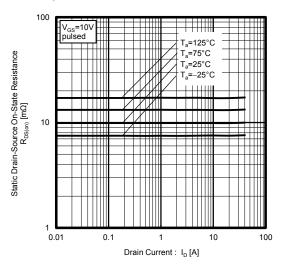


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

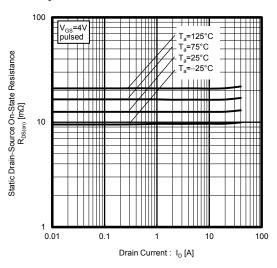
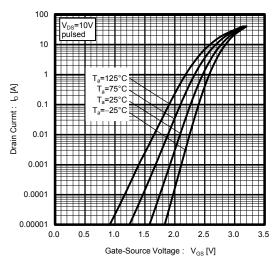
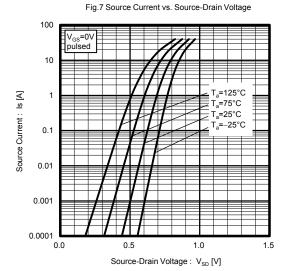
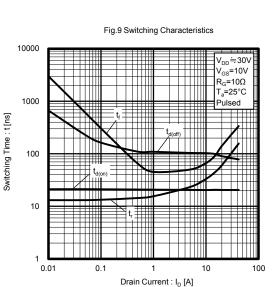
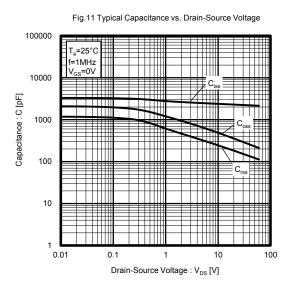


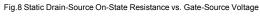
Fig.6 Typical Transfer Characteristics











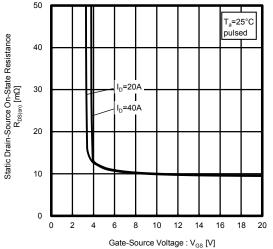


Fig.10 Dynamic Input Characteristics

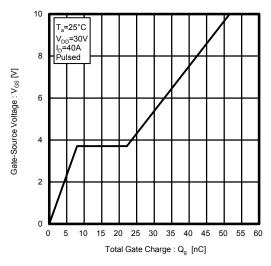
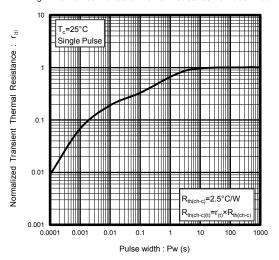


Fig.12 Normalized Transient Thermal Resistance v.s. Pulse Width



### Measurement circuits

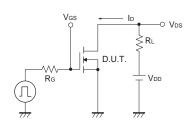


Fig.1-1 Switching Time Measurement Circuit

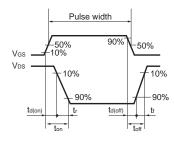


Fig.1-2 Switching Waveforms

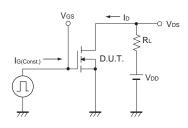


Fig.2-1 Gate Charge Measurement Circuit

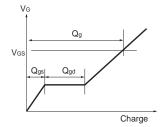


Fig.2-2 Gate Charge Waveform

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- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
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