

RSJ400N10

Nch 100V 40A Power MOSFET

V_{DSS}	100V
R _{DS(on)} (Max.)	27m $Ω$
I _D	40A
P_D	50W

Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Drive circuits can be simple.
- 4) Parallel use is easy.
- 5) Pb-free lead plating; RoHS compliant
- 6) 100% Avalanche tested

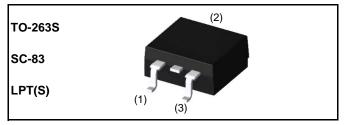
Application

Switching Power Supply

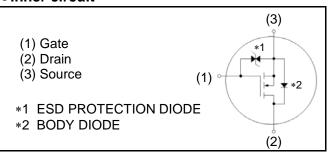
Automotive Motor Drive

Automotive Solenoid Drive

Outline



●Inner circuit



Packaging specifications

	Packaging	Taping
	Reel size (mm)	330
Typo	Tape width (mm)	16
Туре	Basic ordering unit (pcs)	2,500
	Taping code	TL
	Marking	RSJ400N10

• Absolute maximum ratings($T_a = 25$ °C)

Parameter	Symbol	Value	Unit	
Drain - Source voltage	V_{DSS}	100	V	
Continuous drain surrent	T _c = 25°C	I _D *1	±40	А
Continuous drain current	T _c = 100°C	I _D *1	±22	А
Pulsed drain current	I _{D,pulse} *2	±80	А	
Gate - Source voltage		V_{GSS}	±20	V
Avalanche energy, single pulse		E _{AS} *3	14.6	mJ
Avalanche current		I _{AR} *3	10	А
T _c = 25°C		P _D	50	W
Power dissipation $T_a = 25^{\circ}C^{*4}$		P_{D}	1.35	W
Junction temperature		T _j	150	°C
Range of storage temperature		T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - case	R_{thJC}	-	-	2.5	°C/W
Thermal resistance, junction - ambient *4	R_{thJA}	-	-	92.6	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

•Electrical characteristics($T_a = 25$ °C)

Dorometer	Cymbol	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	100	-	-	V
		$V_{DS} = 100V, V_{GS} = 0V$			1	
Zero gate voltage drain current		$T_j = 25^{\circ}C$	-	-	1	μΑ
	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V			100	
		T _j = 125°C	-	-		
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±10	μΑ
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V$, $I_D = 1mA$	1.0	-	2.5	V
		$V_{GS} = 10V, I_D = 40A$	-	19	27	
Static drain - source on - state resistance		$V_{GS} = 4.0V, I_D = 40A$	-	21	30	0
	$R_{DS(on)}^{5}$	$V_{GS} = 10V, I_D = 40A$		40	00	mΩ
		T _j = 125°C	-	42	60	
Forward transfer admittance	g _{fs}	$V_{DS} = 10V, I_D = 40A$	23	56		S

●Electrical characteristics(T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
r ai ai ii e lei	Syllibol	Symbol Conditions —		Тур.	Max.	Offic	
Input capacitance	C_{iss}	$V_{GS} = 0V$	-	3600	-		
Output capacitance	C _{oss}	V _{DS} = 25V	-	270	-	pF	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	180	-		
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 50V, V_{GS} = 10V$	-	25	-		
Rise time	t _r *5	I _D = 20A	-	80	-	no	
Turn - off delay time	t _{d(off)} *5	$R_L = 12\Omega$	-	205	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	250	-		

● Gate Charge characteristics (T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Offic
Total gate charge	Q_g^{*5}	$V_{DD} \simeq 50V$	-	90	-	
Gate - Source charge	${\sf Q_{gs}}^{*5}$	I _D = 40A	-	12	1	nC
Gate - Drain charge	${\sf Q_{gd}}^{*5}$	V _{GS} = 10V	-	18	1	
Gate plateau voltage	V _(plateau)	$V_{DD} \simeq 50V$, $I_D = 40A$	-	3.1	-	V

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Doromotor	Symbol	Conditions	Values			Unit
Parameter	Parameter Symbol Conditions		Min.	Тур.	Max.	Offic
Continuous source current	I _S *1	T _c = 25°C	-	1	40	Α
Pulsed source current	I _{SM} *2	1 c = 25 C	-	-	80	Α
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_{S} = 40A$	-	-	1.5	V
Reverse recovery time	t _{rr} *5	I _S = 40A	-	66	-	ns
Reverse recovery charge	Q _{rr} *5	di/dt = 100A/μs	-	100	-	μС

^{*1} Limited only by maximum temperature allowed.

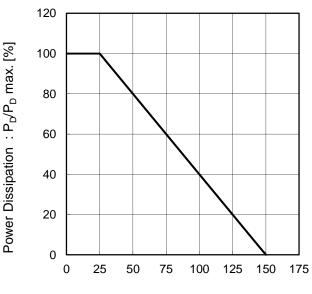
*5 Pulsed

^{*2} Pw \leq 10 μ s, Duty cycle \leq 1%

^{*3} L \simeq 200 μ H, V_{DD} = 50V, Rg = 10 Ω , starting T_j = 25°C

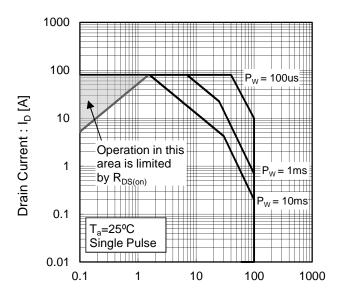
^{*4} Mounted on a epoxy PCB FR4 (27mm × 25mm × 0.8mm)

Fig.1 Power Dissipation Derating Curve



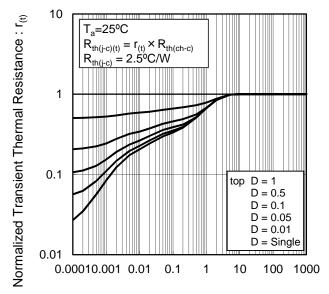
Junction Temperature : T_i [°C]

Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width : $P_W[s]$

Fig.4 Avalanche Current vs Inductive Load

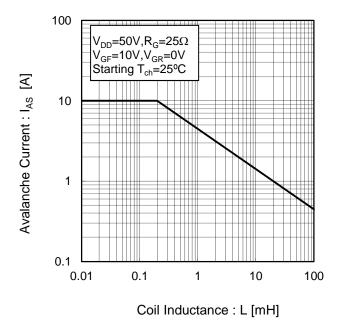
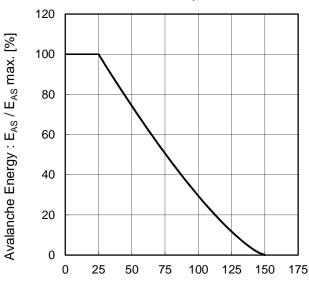
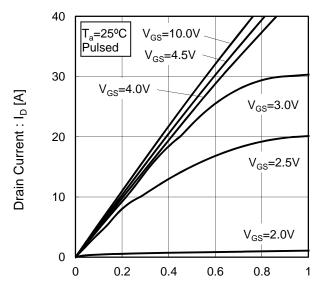


Fig.5 Avalanche Energy Derating Curve vs Junction Temperature



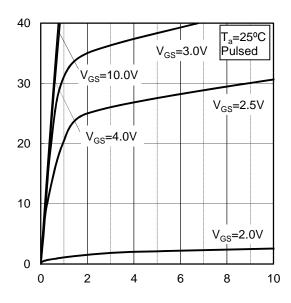
Junction Temperature : T_i [°C]

Fig.6 Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

Fig.7 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Drain Current : I_D [A]

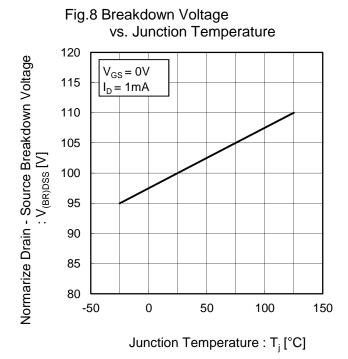
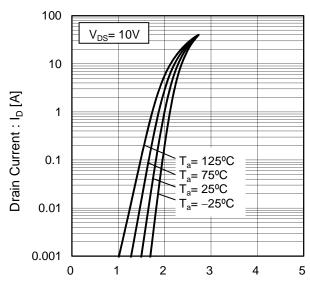


Fig.9 Typical Transfer Characteristics



Gate - Source Voltage : V_{GS} [V]

Fig.10 Gate Threshold Voltage vs. Junction Temperature

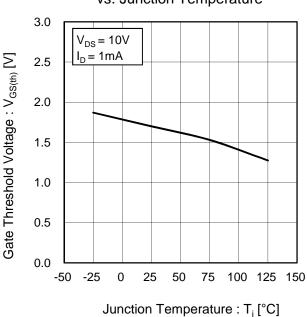
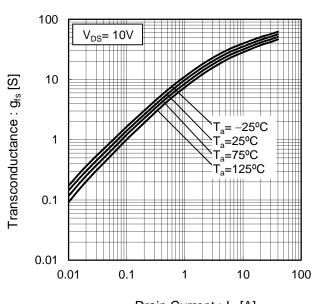


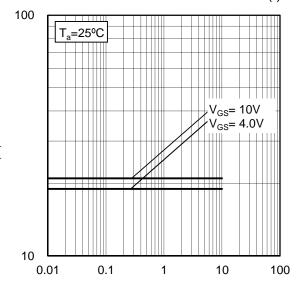
Fig.11 Transconductance vs. Drain Current



Resistance vs. Gate Source Voltage 100 Static Drain - Source On-State Resistance T_a=25°C Static Drain - Source On-State Resistance 80 $I_{D} = 20A$ 60 $:R_{DS(on)}\left[m\Omega \right]$ $: R_{\text{DS(on)}} \left[\text{m}\Omega\right]$ $I_D = 40A$ 40 20 0 0 5 10 15

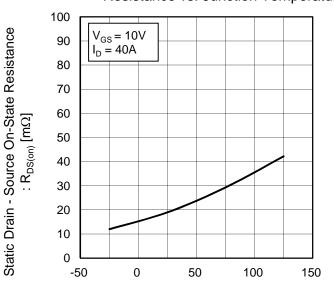
Fig.12 Static Drain - Source On - State

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)



Gate - Source Voltage : $V_{GS}[V]$ Drain Current : $I_{D}[A]$

Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_j [°C]

Resistance vs. Drain Current(II)

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Drain Current : I_D [A]

Fig.15 Static Drain - Source On - State

Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

Tool V_{GS}= 4.5V

T_a=125°C

T_a=75°C

T_a=25°C

T_a=25°C

T_a=-25°C

T_a=-25°C

Drain Current: I_D [A]

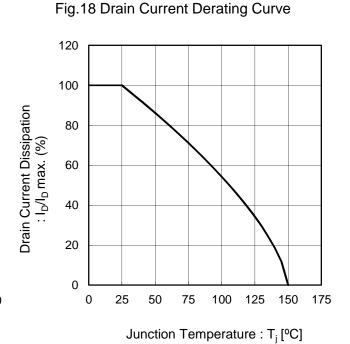
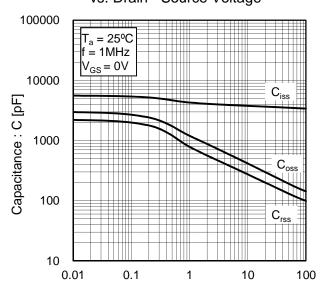


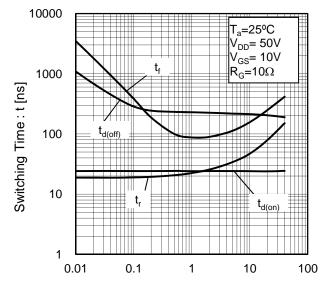


Fig.19 Typical Capacitance vs. Drain - Source Voltage



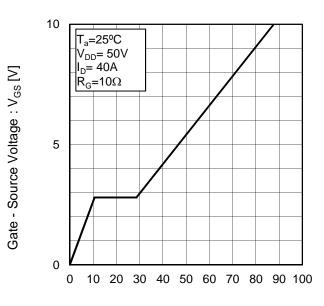
Drain - Source Voltage : V_{DS} [V]

Fig.20 Switching Characteristics



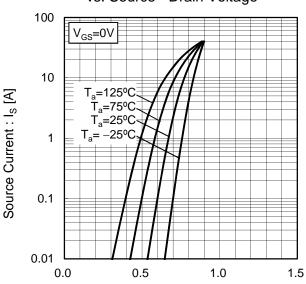
Drain Current : I_D [A]

Fig.21 Dynamic Input Characteristics

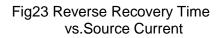


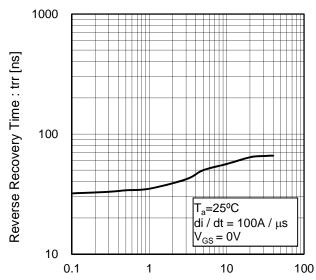
Total Gate Charge : Q_g [nC]

Fig.22 Source Current vs. Source - Drain Voltage



Source-Drain Voltage : V_{SD} [V]





Source Current : I_S [A]

●Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

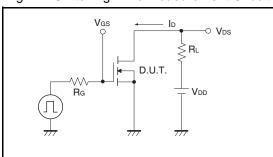


Fig.2-1 Gate Charge Measurement Circuit

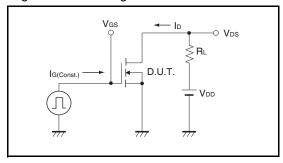


Fig.3-1 Avalanche Measurement Circuit

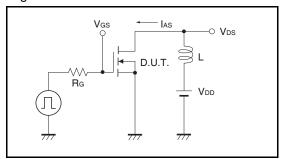


Fig.1-2 Switching Waveforms

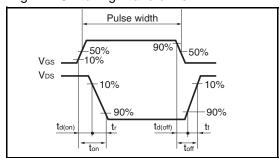


Fig.2-2 Gate Charge Waveform

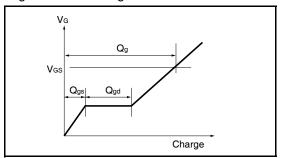
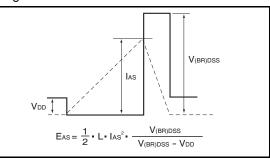
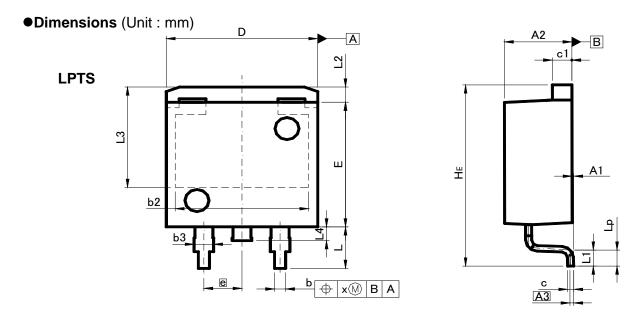
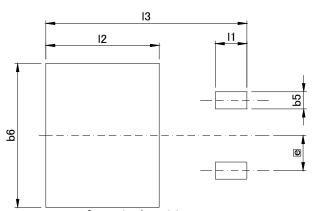


Fig.3-2 Avalanche Waveform







Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
A1	0.00	0.30	0	0.012
A2	4.30	4.70	0.169	0.185
A3	0.	25	0.	01
b	0.68	0.98	0.027	0.039
b2	8.	90	0.	35
b3	1.14	1.44	0.045	0.057
С	0.30	0.60	0.012	0.024
c1	1.10	1.50	0.043	0.059
D	9.80	10.40	0.386	0.409
Е	8.80	9.20	0.346	0.362
е	2.	54	0.	10
HE	12.80	13.40	0.504	0.528
L	2.70	3.30	0.106	0.13
L1	0.90	1.50	0.035	0.059
L2	1.10		0.0)43
L3	7.25		0.285	
L4	1.	00	0.0	39
Lp	0.90	1.50	0.035	0.059
Х	-	0.25	_	0.01

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
b5	-	1.23	ı	0.049
b6	-	10.40	-	0.409
11	-	2.10	-	0.083
12	-	7.55	-	0.297
13	_	13.40	_	0.528

Dimension in mm/inches

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 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
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 - [d] the Products are exposed to high Electrostatic
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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